NIKO-SEM

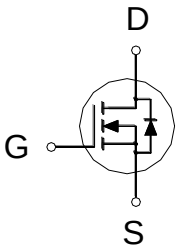
N-Channel Enhancement Mode Field Effect Transistor

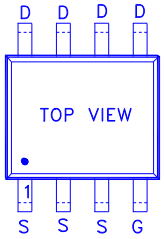
P8008BVA
SOP-8
Halogen-Free & Lead-Free

RoHS Compliant

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
80V	68mΩ	3.5A





G : GATE
D : DRAIN
S : SOURCE

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	±25	V
Continuous Drain Current	T_A = 25 °C	I_D	3.5	A
	T_A = 70 °C		2.8	
Pulsed Drain Current[1]		I_{DM}	14	
Avalanche Current		I_{AS}	14.7	
Avalanche Energy	L = 0.1mH	E_{AS}	10.8	mJ
Power Dissipation	T_A = 25 °C	P_D	1.8	W
	T_A = 70 °C		1.1	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		69	°C / W
Junction-to-Lead	$R_{\theta JL}$		25	°C / W

[1]Pulse width limited by maximum junction temperature.

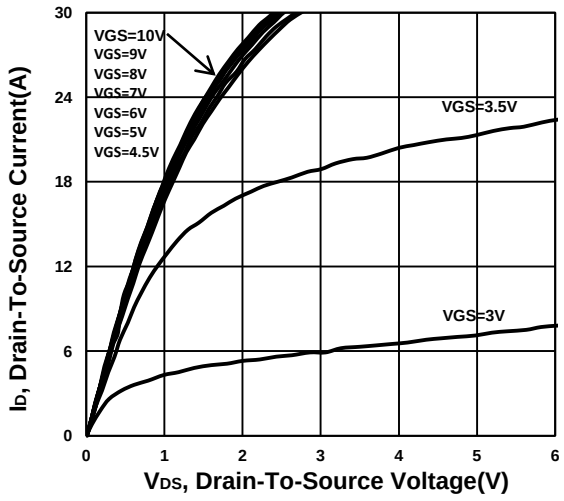
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	80			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±25V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 64V, V_{GS} = 0V			1	μA
		V_{DS} = 64V, V_{GS} = 0V, T_J = 70 °C			10	

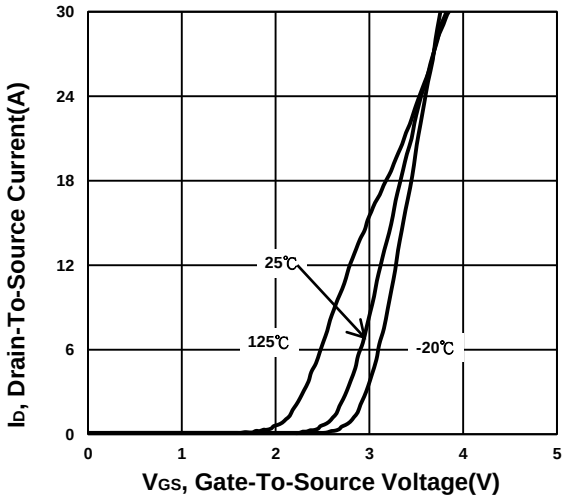
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 1A		47	78	mΩ
		V_{GS} = 10V, I_D = 3A		44	68	
Forward Transconductance[1]	g_{fs}	V_{DS} = 10V, I_D = 3A		17		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		562		pF
Output Capacitance	C_{oss}			63		
Reverse Transfer Capacitance	C_{rss}			38		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		1.2		Ω
Total Gate Charge[2] (10V)	Q_g	V_{DS} = 80V, V_{GS} = 10V, I_D = 3A		14		nC
Total Gate Charge[2] (4.5V)	Q_g			8		
Gate-Source Charge[2]	Q_{gs}			2		
Gate-Drain Charge[2]	Q_{gd}			4.5		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 40V, $I_D \cong$ 3A, V_{GS} = 10V, R_G = 6Ω		15		nS
Rise Time[2]	t_r			14		
Turn-Off Delay Time[2]	$t_{d(off)}$			38		
Fall Time[2]	t_f			17		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Current	I_S				1.4	A
Forward Voltage[1]	V_{SD}	I_F = 3A, V_{GS} = 0V			1.3	V
Reverse Recovery Time	t_{rr}	I_F = 3A , dI_F/dt = 100A / μS		17		nS
Reverse Recovery Charge	Q_{rr}			10		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.
[3]Pulse width limited by maximum junction temperature.

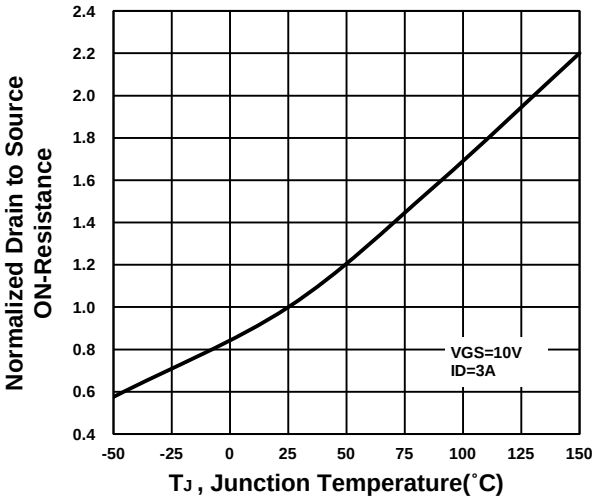
Output Characteristics



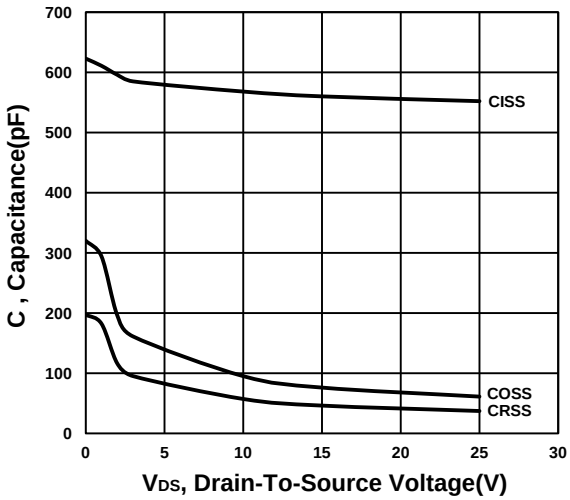
Transfer Characteristics



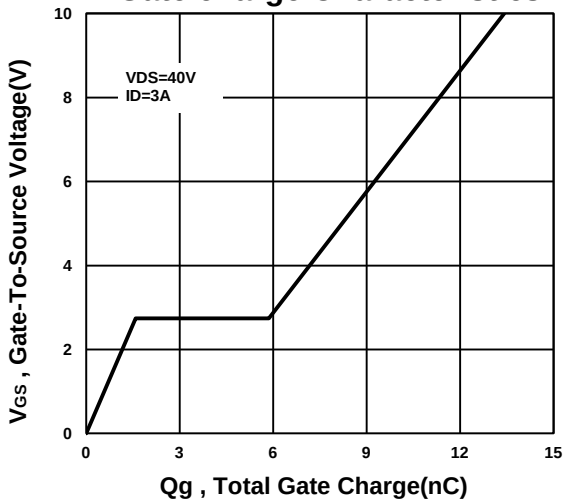
On-Resistance VS Temperature



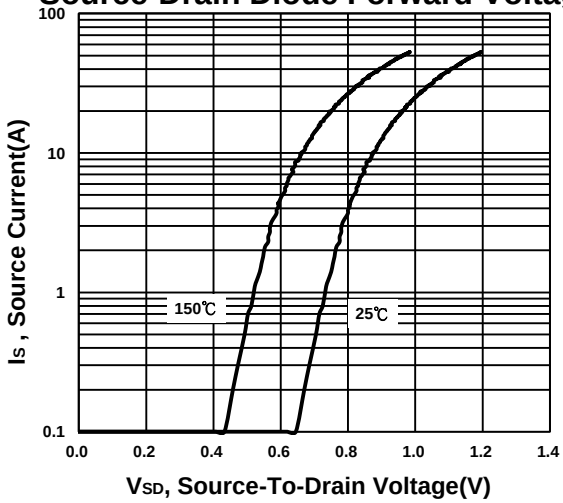
Capacitance Characteristic



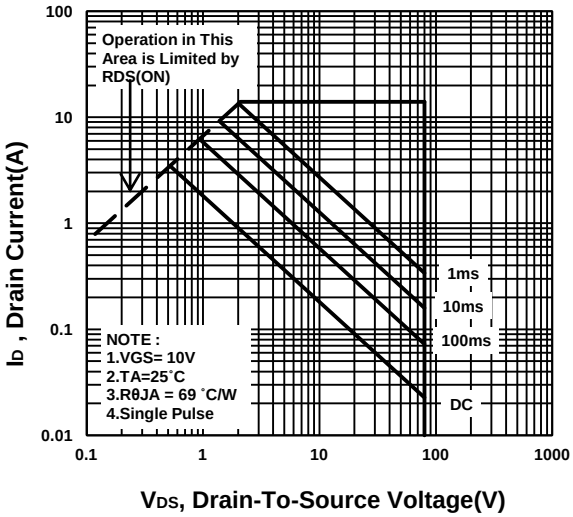
Gate charge Characteristics



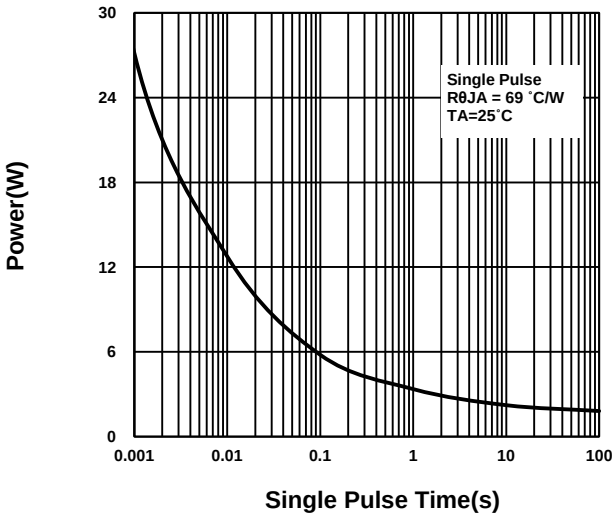
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve