SMD Passive Delay Line

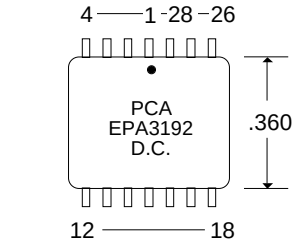
PCA Part Number : **EPA3192**

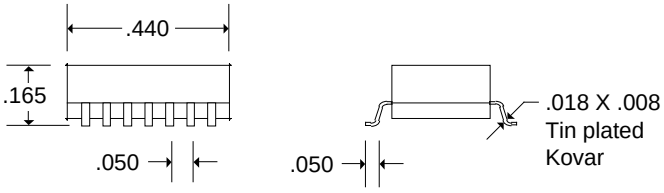
Electrical Specifications	
Impedance	100 Ω ± 10%
TR Output	2.6 nS Max.
TR True	-----
Tap Delay	1.3 nS ± 1 nS
Delay Time	13.0 nS ± 5%
Attenuation	0.8% Ref.
Distortion	± 10.0% Max.
D.C. Resistance	0.86 Ω Ref.
I.R. @ 100 Vdc	1 KM Ω Min.
Dielectric Strength	100 Vdc
Temp. Coef. of Delay	100 PPM / °C
Operating Temp.	0 to 70°C

Test Conditions @ 25°C			
Pw	39.0 nS	**Vcc**	
Prr	1.0 MHz	**Temp**	
Ein	1.2 V	**Load**	
Tri	2.0 nS		
Tfi			

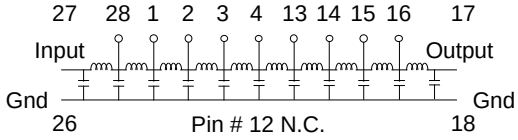
*Unless otherwise specified, all tests to be conducted @ 25°C and RH of 93% Max.

Package





Schematic



Notes :

Unless Otherwise Noted Dimensions in Inches
Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010

PCA ELECTRONICS INC.

16799 SCHOENBORN ST.
NORTH HILLS, CA 91343
TEL: (818) 892-0761
FAX: (818) 894-5791