



**CM100RX-12A**  
**Six IGBTMOD™ + Brake NX-Series Module**  
 100 Amperes/600 Volts

## Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	CM100RX-12A	Units
Power Device Junction Temperature	$T_j$	-40 to 150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-40 to 125	$^\circ\text{C}$
Mounting Torque, M5 Mounting Screws	—	31	in-lb
Mounting Torque, M5 Main Terminal Screws	—	31	in-lb
Module Weight (Typical)	—	330	Grams
Baseplate Flatness, On Centerline X, Y (See Below)	—	$\pm 0 \sim +100$	$\mu\text{m}$
Isolation Voltage, AC 1 minute, 60Hz Sinusoidal	$V_{ISO}$	2500	Volts

### Inverter Sector

Collector-Emitter Voltage (G-E Short)	$V_{CES}$	600	Volts
Gate-Emitter Voltage (C-E Short)	$V_{GES}$	$\pm 20$	Volts
Collector Current ( $T_C = 75^\circ\text{C}$ )*1	$I_C$	100	Amperes
Peak Collector Current (Pulse)*3	$I_{CM}$	200	Amperes
Emitter Current ( $T_C = 25^\circ\text{C}$ )*1	$I_E^{*2}$	100	Amperes
Peak Emitter Current*3	$I_{EM}^{*2}$	200	Amperes
Maximum Collector Dissipation ( $T_C = 25^\circ\text{C}$ )*1*4	$P_C$	400	Watts

### Brake Sector

Collector-Emitter Voltage (G-E Short)	$V_{CES}$	600	Volts
Gate-Emitter Voltage (C-E Short)	$V_{GES}$	$\pm 20$	Volts
Collector Current ( $T_C = 97^\circ\text{C}$ )*1	$I_C$	50	Amperes
Peak Collector Current (Pulse)*3	$I_{CM}$	100	Amperes
Maximum Collector Dissipation ( $T_C = 25^\circ\text{C}$ )*1*4	$P_C$	280	Watts
Repetitive Peak Reverse Voltage (Clamp Diode Part)	$V_{RRM}^{*2}$	600	Volts
Forward Current ( $T_C = 25^\circ\text{C}$ )*1	$I_F^{*2}$	50	Amperes
Forward Current (Pulse)*3	$I_{FM}^{*2}$	100	Amperes

\*1 Case temperature ( $T_C$ ) and heatsink temperature ( $T_f$ ) are defined on the surface of the baseplate and heatsink at just under the chip.

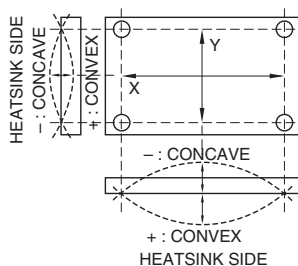
\*2  $I_E$ ,  $I_{EM}$ ,  $V_{EC}$ ,  $t_{rr}$  and  $Q_{rr}$  represent ratings and characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

$I_F$ ,  $I_{FM}$ ,  $I_{RRM}$ ,  $V_{FM}$  and  $V_{RRM}$  represent ratings and characteristics of the clamp diode.

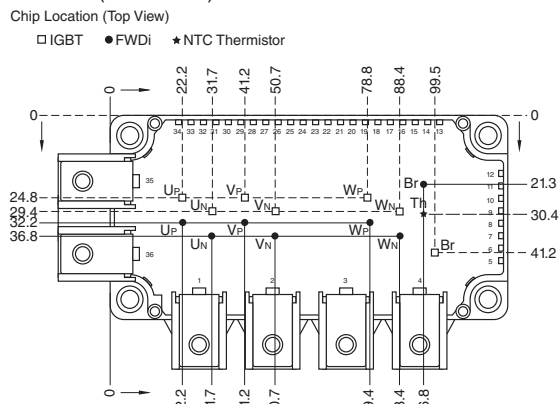
\*3 Pulse width and repetition rate should be such that device junction temperature ( $T_j$ ) does not exceed  $T_{j(max)}$  rating.

\*4 Junction temperature ( $T_j$ ) should not increase beyond  $T_{j(max)}$  rating.

### BASEPLATE FLATNESS MEASUREMENT POINT



### CHIP LOCATION (TOP VIEW)



Dimensions in mm (Tolerance:  $\pm 1\text{mm}$ )

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**Electrical and Mechanical Characteristics,  $T_j = 25^\circ\text{C}$  unless otherwise specified**

**Inverter Sector**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current	$I_{CES}$	$V_{CE} = V_{CES}, V_{GE} = 0V$	—	—	1.0	mA
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$I_C = 10mA, V_{CE} = 10V$	5	6	7	Volts
Gate Leakage Current	$I_{GES}$	$V_{GE} = V_{GES}, V_{CE} = 0V$	—	—	0.5	$\mu A$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 100A, V_{GE} = 15V, T_j = 25^\circ\text{C}^{*5}$	—	1.7	2.1	Volts
		$I_C = 100A, V_{GE} = 15V, T_j = 125^\circ\text{C}^{*5}$	—	1.9	—	Volts
		$I_C = 100A, V_{GE} = 15V, \text{Chip}$	—	1.6	—	Volts
Input Capacitance	$C_{ies}$		—	—	13.3	nF
Output Capacitance	$C_{oes}$	$V_{CE} = 10V, V_{GE} = 0V$	—	—	1.4	nF
Reverse Transfer Capacitance	$C_{res}$		—	—	0.45	nF
Total Gate Charge	$Q_G$	$V_{CC} = 300V, I_C = 100A, V_{GE} = 15V$	—	270	—	nC
Inductive	Turn-on Delay Time	$t_{d(on)}$	—	—	100	ns
	Turn-on Rise Time					
Load	Turn-off Delay Time	$t_{d(off)}$	—	—	300	ns
	Switch					
Time	Reverse Recovery Time	$t_{rr}^{*2}$	—	—	200	ns
	Reverse Recovery Charge					
Emitter-Collector Voltage	$V_{EC}^{*2}$	$I_E = 100A, V_{GE} = 0V, T_j = 25^\circ\text{C}^{*5}$	—	2.0	2.8	Volts
		$I_E = 100A, V_{GE} = 0V, T_j = 125^\circ\text{C}^{*5}$	—	1.95	—	Volts
		$I_E = 100A, V_{GE} = 0V, \text{Chip}$	—	1.9	—	Volts

**Thermal and Mechanical Characteristics,  $T_j = 25^\circ\text{C}$  unless otherwise specified**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case**	$R_{th(j-c)Q}$	Per IGBT <sup>*1</sup>	—	—	0.31	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case**	$R_{th(j-c)D}$	Per FWDi <sup>*1</sup>	—	—	0.59	$^\circ\text{C}/\text{W}$
Contact Thermal Resistance**	$R_{th(j-f)}$	Case to Heatsink (Per 1 Module) Thermal Grease Applied <sup>*1*7</sup>	—	0.015	—	$^\circ\text{C}/\text{W}$
Internal Gate Resistance	$R_{Gint}$	$T_C = 25^\circ\text{C}$	—	0	—	$\Omega$
External Gate Resistance	$R_G$		6	—	62	$\Omega$

\*\*Thermal resistance values are per 1 element.

\*1 Case temperature ( $T_C$ ) and heatsink temperature ( $T_f$ ) are defined on the surface of the baseplate and heatsink at just under the chip.

\*2  $I_E, I_{EM}, V_{EC}, t_{rr}$  and  $Q_{rr}$  represent ratings and characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

$I_F, I_{FM}, I_{RRM}, V_{FM}$  and  $V_{RRM}$  represent ratings and characteristics of the clamp diode.

\*5 Pulse width and repetition rate should be such as to cause negligible temperature rise.

\*7 Typical value is measured by using thermally conductive grease of  $\lambda = 0.9 [\text{W}/(\text{m} \cdot \text{K})]$ .

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### Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

#### Brake Sector

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current	$I_{CES}$	$V_{CE} = V_{CES}, V_{GE} = 0V$	—	—	1.0	mA
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$I_C = 5mA$	5	6	7	Volts
Gate Leakage Current	$I_{GES}$	$V_{GE} = V_{GES}, V_{CE} = 0V$	—	—	0.5	$\mu\text{A}$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 50A, V_{GE} = 15V, T_j = 25^\circ\text{C}^5$	—	1.7	2.1	Volts
		$I_C = 50A, V_{GE} = 15V, T_j = 125^\circ\text{C}^5$	—	1.9	—	Volts
		$I_C = 50A, V_{GE} = 15V, \text{Chip}$	—	1.6	—	Volts
Input Capacitance	$C_{ies}$		—	—	9.3	nF
Output Capacitance	$C_{oes}$	$V_{CE} = 10V, V_{GE} = 0V$	—	—	1.0	nF
Reverse Transfer Capacitance	$C_{res}$		—	—	0.3	nF
Total Gate Charge	$Q_G$	$V_{CC} = 300V, I_C = 50A, V_{GE} = 15V$	—	200	—	nC
Repetitive Reverse Current	$I_{RRM}^{*2}$	$V_R = V_{RRM}$	—	—	1.0	mA
Forward Voltage Drop	$V_{FM}^{*2}$	$I_F = 50A, T_j = 25^\circ\text{C}^5$	—	2.0	2.8	Volts
		$I_F = 50A, T_j = 125^\circ\text{C}^5$	—	1.95	—	Volts
		$I_F = 50A, \text{Chip}$	—	1.9	—	Volts

### Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case**	$R_{th(j-c)Q}$	Per IGBT <sup>*1</sup>	—	—	0.44	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case**	$R_{th(j-c)D}$	Per FWD <sup>*1</sup>	—	—	0.85	$^\circ\text{C}/\text{W}$
Contact Thermal Resistance**	$R_{th(j-f)}$	Case to Heatsink (Per 1 Module) Thermal Grease Applied <sup>*1*7</sup>	—	0.015	—	$^\circ\text{C}/\text{W}$
Internal Gate Resistance	$R_{Gint}$	$T_C = 25^\circ\text{C}$	—	0	—	$\Omega$
External Gate Resistance	$R_G$		13	—	125	$\Omega$

### NTC Thermistor Sector, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Zero Power Resistance	R	$T_C = 25^\circ\text{C}^1$	4.85	5.00	5.15	k $\Omega$
Deviation of Resistance	$\Delta R/R$	$T_C = 100^\circ\text{C}, R_{100} = 493\Omega^1$	-7.3	—	+7.8	%
B Constant	$B_{(25/50)}$	$B = (\ln R_1 - \ln R_2) / (1/T_1 - 1/T_2)^6$	—	3375	—	K
Power Dissipation	$P_{25}$	$T_C = 25^\circ\text{C}^1$	—	—	10	mW

\*\*Thermal resistance values are per 1 element.

\*1 Case temperature ( $T_C$ ) and heatsink temperature ( $T_f$ ) are defined on the surface of the baseplate and heatsink at just under the chip.

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$I_F, I_{FM}, I_{RRM}, V_{FM}$  and  $V_{RRM}$  represent ratings and characteristics of the clamp diode.

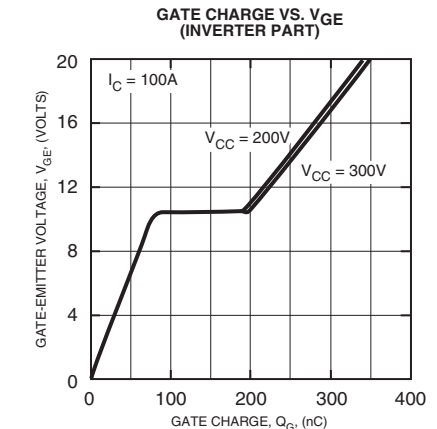
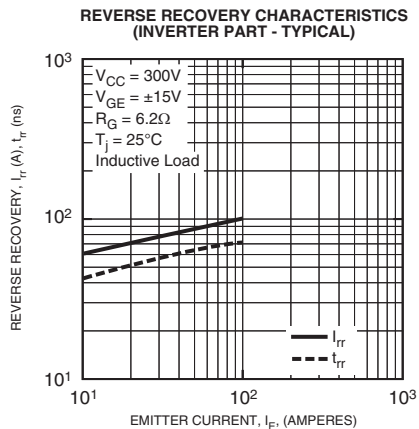
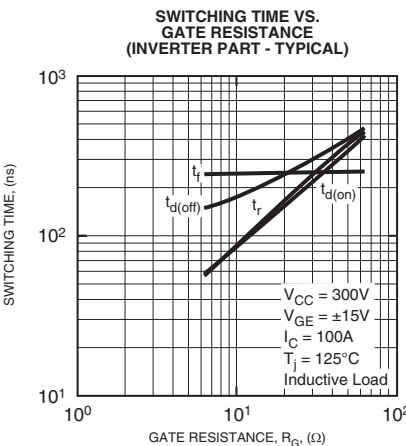
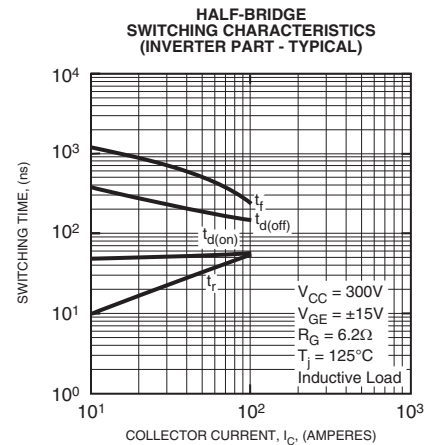
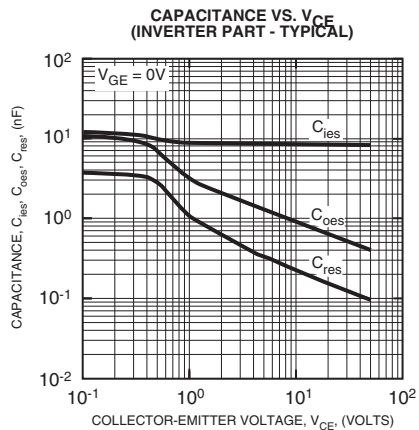
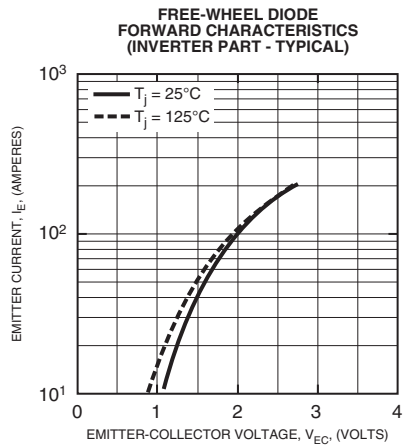
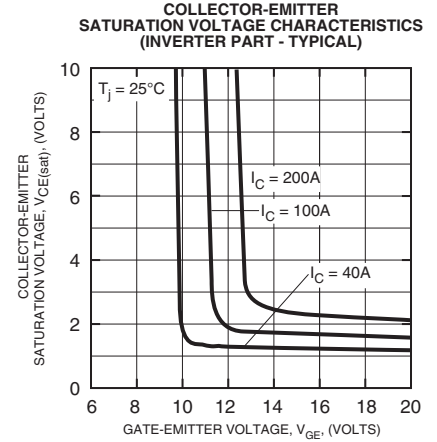
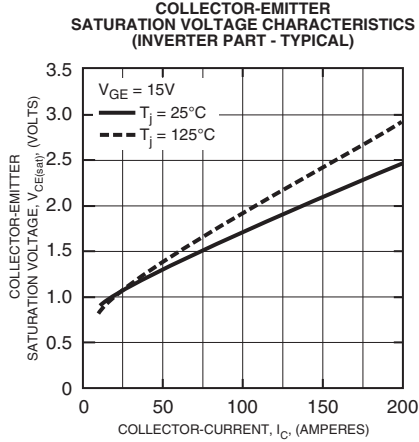
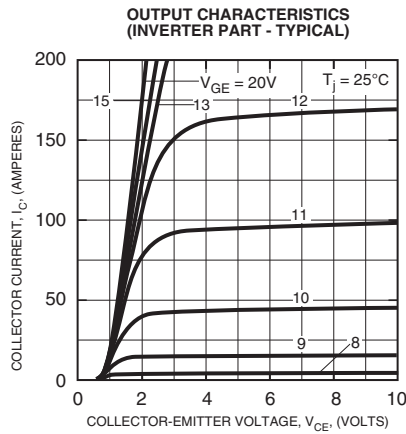
\*5 Pulse width and repetition rate should be such as to cause negligible temperature rise.

\*7 Typical value is measured by using thermally conductive grease of  $\lambda = 0.9 \text{ [W/(m} \cdot \text{K)]}$ .



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