

Specification for Approval

Date: 2021/08/16

Customer:	天诚科技
TAI-TECH P/N:	TMPA1005S-470MN-D
CUSTOMER P/N:	
DESCRIPTION:	
QUANTITY:	

REMARK:												
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西 北 臺 慶 科 技 股 份 有 限 公司 TAI-TECH Advanced Electronics Co.. Ltd

代理商.

深圳市天**诚**科技有限公司 Shenzhen TsaSun Technology Co., Ltd. Room 209, 2/F, Block A, Tengfei Industrial Building, No.6, Taohua Road, Futian District, Shenzhen TEL: 0755-8335 8885 / 0755-8335 9885 E-mail: sales@tsasun.com www.tsacoil.com

□西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u>
NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C.
TEL: +886-3-4641148 FAX: +886-3-4643565 http://www.tai-tech.com.tw
E-mail: sales@tai-tech.com.tw

□ 臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

TEL: +86-512-57619396 FAX: +86-512-57619688

E-mail: hui@tai-tech.com.tw

Sales Dep.

APPROVED	CHECKED
夏暁曼	夏暁曼

R&D Center

APPROVED	CHECKED	DRAWN		
羅宜春	梁周虎	卜文娟		

SMD Power Inductor

TMPA1005S-470MN-D

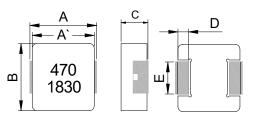
1. Features

- 1. Shielded construction.
- 2. Capable of corresponding high frequency (5MHz).
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.

2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.
- 5. VRM for server.
- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

3. Dimensions







Series	Α	A`	В	С	D	E
TMPA1005	11.0±0.5	10.0±0.5	10.0±0.3	4.8±0.2	2.0±0.3	3.0±0.3

Unit:mm

4. Part Numbering

TMPA	1005	S	-	470	MN -	D
Α	В	С		D	E	F

A: Series

 B: Dimension
 BxC

 C: Type
 Standard.

 D: Inductance
 470=47.0uh

 E: Inductance Tolerance
 M=±20%

F: Code Marking: Black.470 and 1830(18 YY, 30 WW,follow production date).

5. Specification

Part Number	Inductance L0 A(uH) ±20%	Heat Rating Current DC I rms.(A)		Saturation Current DC I sat. (A)		Current DC I sat. (A)		DCR (mΩ)Typ	DCR (mΩ)Max
	12070	Тур	Max	Тур	Max				
TMPA1005S-470MN-D	47.0	4.5	4.0	3.5	4.0	106	127		

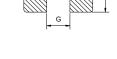
Note:

- 1. Test frequency: Ls: 100KHz /1.0V.
- $3. \ \ Testing\ Instrument (or\ equ): L:\ HP4284A, CH11025, CH3302, CH1320, CH1320S\ LCR\ METER\ /\ Rdc: CH16502, Agilent 33420A\ MICRO\ OHMMETER.$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,\Delta T$ of 40 $^{\circ} \! C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°Cunder worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.









G(mm)

0.15mm and above.

L(mm)

12.5	5.4	3.5	
Note: 1. The	above PCE	3 layout refe	rence only.
2. Re	commend s	older paste	thickness at

H(mm)

6. Material List



NO	Items	Materials
1	Core	Alloy Powder .
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product with taping) 240~+125℃ (on board)	
Electrical Performance	Test	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	Telefic Standard electrical diaracteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop △L(%)
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		· · ·
Life Test		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: $85\pm2\%$ R.H, Temperature: $85^\circ C\pm 2^\circ C$ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24 ± 2 hrs.
Moisture Resistance	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50 $^{\circ}\mathbb{C}$ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65 ± 2 $^{\circ}\mathbb{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\mathbb{C}$ in 2.5hrs. 3. Raise temperature to 65 ± 2 $^{\circ}\mathbb{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\mathbb{C}$ in 2.5hrs, keep at $00000000000000000000000000000000000$
Thermal shock		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1: -40±2℃ 30±5min Step2: 25±2℃ ≤0.5min Step3: 125±2℃ 30±5minNumber of cycles: 500 Measured at room fempraturc after placing for 24±2 hrs.
Vibration		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations).

TAI-TECH

Item	Performance	Test Condition						
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.						
Shock	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not		Туре	Peak value (g's)	dura	ormal ation (D) (ms)	Wave form	Velocity change (Vi)ft/sec
	exceed the specification value		SMD	50		11	Half-sine	11.3
			Lead	50		11	Half-sine	11.3
Solder ability	More than 95% of the terminal electrode should be covered with solder ·	Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4±1sec Depth: completely cover the termination						
Resistance to Soldering Heat		Te	empera 260	ature(°C)		ramp/ir and eme	perature mmersion ersion rate	Number of heat cycles
Terminal Strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	260 ±5 (solder temp) 10 ±1 25mm/s ±6 Preconditioning: Run through IR reflow f J-STD-020DClassification Reflow Profile With the component mounted on a PCI tested, apply a force(>0805:1kg, <=08C device being tested. This force shall seconds. Also the force shall be apply a shock to the component being tested. The press to be a possible to to be a poss			ofiles PCB with =0805:0.5k hall be ag applied gra g tested.	the device to be g)to the side of a oplied for 60 +1		

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAIPAQ terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

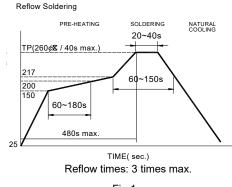
Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

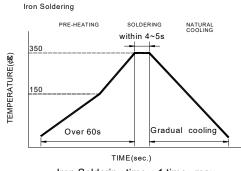
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- · Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355℃ tip temperature (max)
- 1.0mm tip diameter (max)
- · Limit soldering time to 4~5sec.







Iron Soldering times: 1 times max.

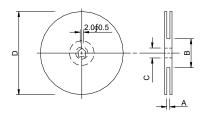
Fig.2

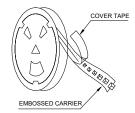
9. Friendly reminder

- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method. Use only alcohol to wash the PCB and dry it off immediately (Marking will be washed away if using alcohol).

10. Packaging Information

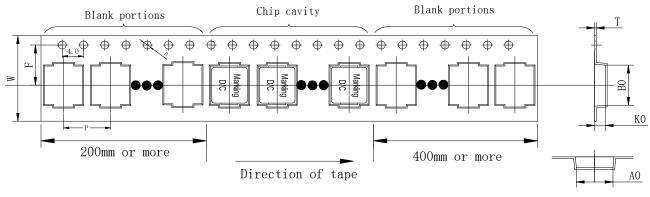
(1) Reel Dimension





Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension

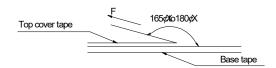


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
TMPA	1005	11.6±0.1	10.4±0.1	5.3±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.1	1.5±0.1

(3) Packaging Quantity

TMPA	1005		
Chip / Reel	500		
Inner box	1000		
Carton	4000		

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060		

Application Notice

- · Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECHproducts meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves

