TOSHIBA BI-CMOS INTEGRATED CIRCUIT SILICON MONOLITHIC

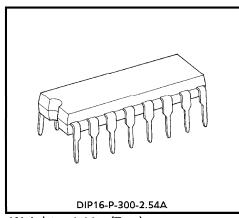
TB6515AP

SENSORLESS MOTOR DRIVER IC

The TB6515AP is a sensorless motor driver IC developed mainly for use with VTR cylinder motors. The PG and FG sensors are sensorless three-phase brushless motor driver ICs with sharing capabilities (specific magnetism is required).

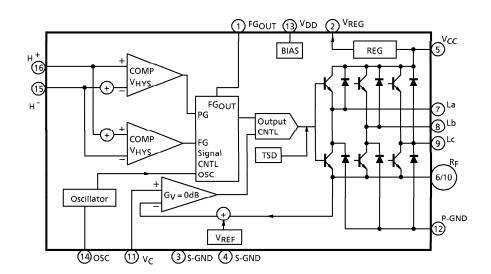
FEATURES

- The PG and FG sensors can be shared, and the motor driver areas are sensorless.
- Three-phase full-wave drive models.
- Equipped with FG output.
- Built-in thermal shut-down circuits.
- Built-in power source for the PG and FG sensors.



Weight: 1.11g (Typ.)

BLOCK DIAGRAM



- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The products described in this document are subject to foreign exchange and foreign trade control laws The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.

 The information contained herein is subject to change without notice.

PIN FUNCTION

PIN No.	PIN SYMBOL	PIN FUNCTION
1	FGOUT	FG signal output pin
2	V _{REG}	Internal power source voltage output pin
3	S-GND	Small signal ground pin
4	S-GND	Small signal ground pin
5	V _{CC}	Power source applied voltage pin
6	R _F	Output current detection pin
7	La	a-phase drive output pin
8	Lb	b-phase drive output pin
9	Lc	c-phase drive output pin
10	RF	Output current detection pin
11	V _C	Control amplifier positive input pin
12	P-GND	Output ground pin
13	V_{DD}	Internal power source voltage output pin
14	OSC	Oscillation condenser connection pin
15	н-	PG/FG comparator negative input pin
16	H ⁺	PG/FG comparator positive input pin

MAXIMUM RATINGS (Ta = 25° C)

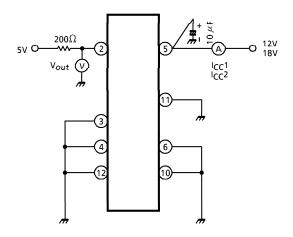
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Power Suplly Voltage	V _C C	18	V	
Output Current	lo	1.0	Α	
Regulator Output Current	I _{REG}	12	mA	
FG Output Current	I _{FG}	2.0	mΑ	
Power Dissipation	P _D	(Note) 1.2	W	
Operating Temperature	T _{opr}	- 30 ∼ 85	°C	
Storage Temperature	T _{stg}	− 55 ~ 150	°C	

(Note) IC units

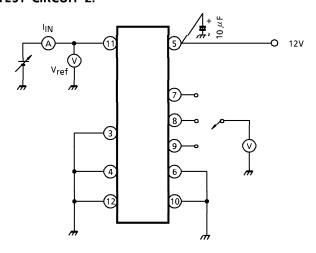
ELECTRICAL CHARACTERISTICS ($V_{CC} = 12V$, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Power Suplly Current		I _{CC1}	1	$V_{CC} = 12V, V_C = 0V,$ $V_{REG} = OPEN$	1	9.0	15	mA
		I _{CC2}	1	$V_{CC} = 18V, V_C = 0V,$ $V_{REG} = OPEN$	1	9.3	15	IIIA
Control Amplifier	Standard Voltage	V_{ref}	2		_	2.6	_	V
	Voltage Gain	G_V	6		_	1.0	_	
	Input Current	I _{IN}	2	V _C = 3.5V	_	2.5	10	μ A
Leak Current	Upper	loL (U)		$V_{CC} = 18V, V_{C} = 0V$	_	_	50	μΑ
	Lower	lol (l)		$V_{CC} = 18V, V_{C} = 0V$	_	_	50	
Output Saturation	Upper	V _{sat} (U)	3	I _O = 1A	_	1.5	1.9	V
Voltage	Lower	V _{sat} (L)	,	I _O = 1A	_	8.0	1.2	V
Correlated Gain Difference		∆G _V	_		_	_		%
Residual Output Voltage		Vor	6	$V_C = 0V$	_	0	10	mV
	FG Upper Level	V_{FGH}	5	L→H	91	104	117	mV
FG/PG Threshold Level	FG Lower Level	V_{FGL}		$H \rightarrow L$	108	121	134	
	PG Upper Level	V_{PGH}		L→H	118	131	144	
	PG Lower Level	V_{PGL}		H→L	139	152	165	
Hall Amp Common-Mode Input Voltage		CMR	_			_	2.0	٧
FG Output Voltage		V _{FG} (L)	4	I _{FG} = 1mA	_	_	1.1	٧
FG Output Current		lFG	_		1.8	2.0	_	mA
Delta-Wave Oscillation Frequency		fosc	7	$C_{OSC} = 0.1 \mu F$	_	8	_	Hz
Rated Voltage Output Circuit	Output Voltage	V _{REG}		$RL = 200\Omega$: 5V	1.35	1.45	1.55	V
	Temperature Variable	ΔVO	1	RL = 200 Ω , T _j = -20~70°C		± 30	_	mV
	Output Current	IREG	_		20	_	_	mΑ
Thermal Shut-Off Circuit Operating Temperature		T _{SD}	_		150	_	_	°C

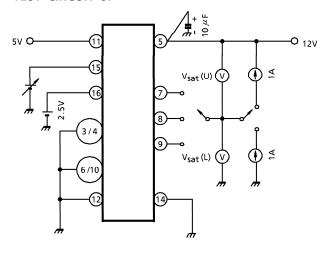
TEST CIRCUIT 1.



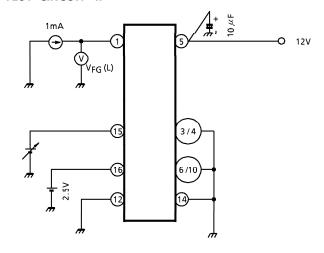
TEST CIRCUIT 2.



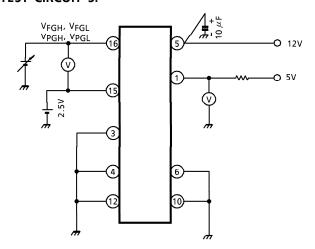
TEST CIRCUIT 3.



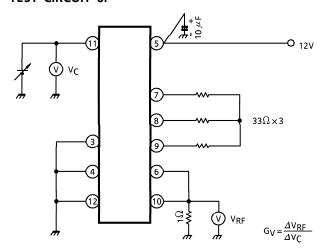
TEST CIRCUIT 4.



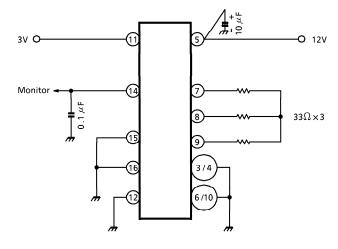
TEST CIRCUIT 5.

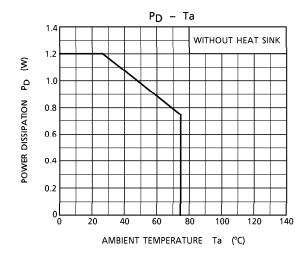


TEST CIRCUIT 6.

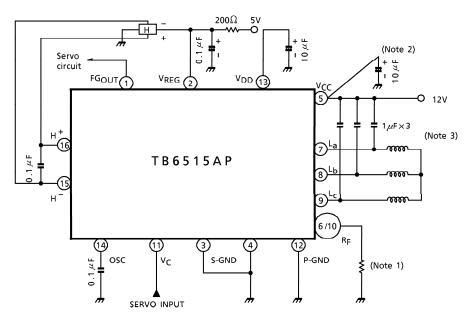


TEST CIRCUIT 7.





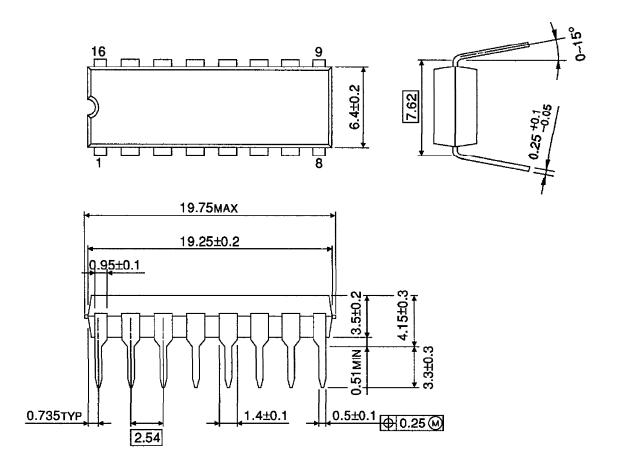
APPLICATION CIRCUIT



- (Note 1) R_F is determined in accordance with coil impedance, F/V conversion voltage (control input), the required torque and other factors, but between approximately 0.3 and 5Ω should be used.
- (Note 2) It is recommended that the IC pin and GND are connected directly. Ever larger levels of capacity may be required depending on the shared impedance of the power source line.
- (Note 3) There may be cases where connections (various output → GND, etc.) and capacity needs to be amended in order to prevent noise and vibrations from the motor.

OUTLINE DRAWING

DIP16-P-300-2.54A Unit: mm



Weight: 1.11g (Typ.)