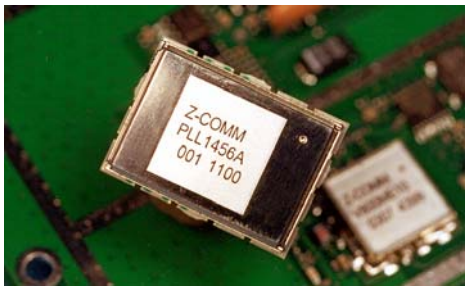
Z-Communications, Inc.
9939 Via Pasar • San Diego, CA 92126
TEL (858) 621-2700 FAX (858) 621-2722

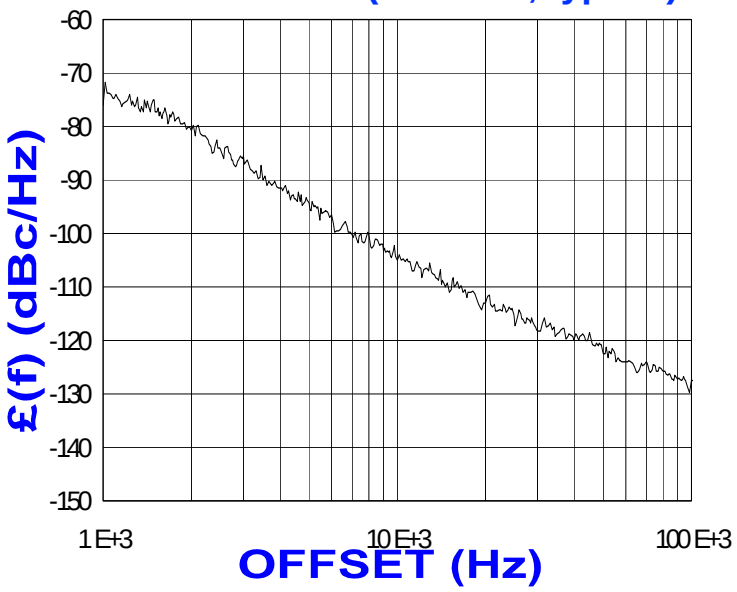
PSA3330C

PHASE LOCKED LOOP

Rev A1



PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 3305 - 3335 MHz
- Step Size: 125 KHz
- PLL - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	3305 - 3335	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-106	dBc/Hz
Harmonic Suppression (2nd, typ.)	-12	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	125	KHz
Charge Pump Output Current	5000	µA
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	1	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	30	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

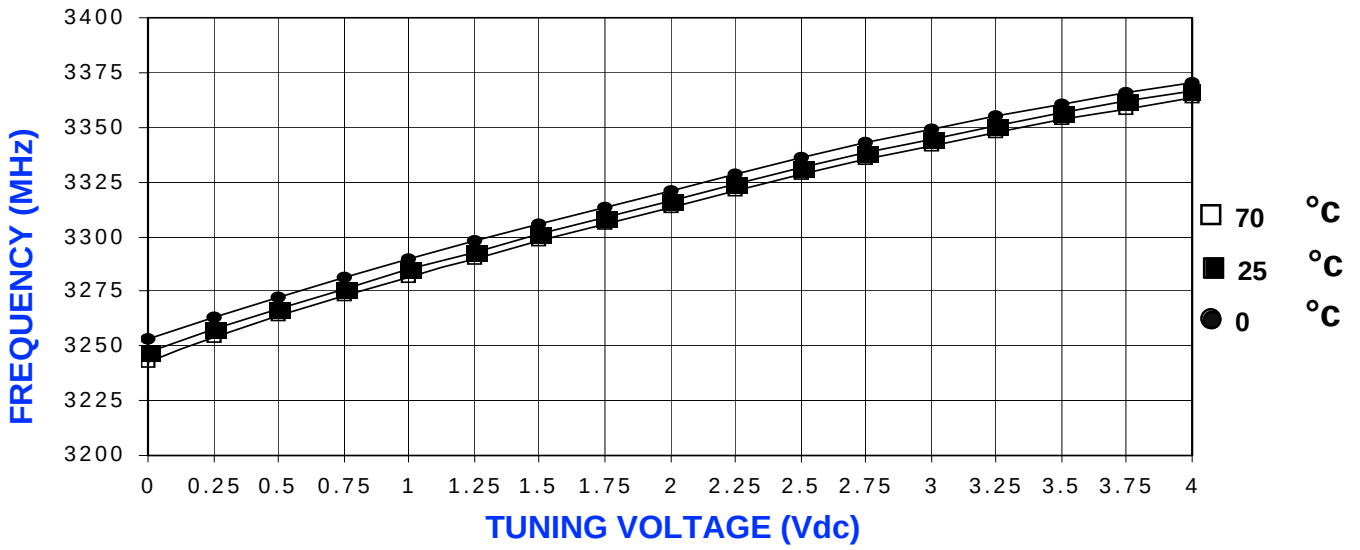
Reference Oscillator Signal: 5 MHz<f_{osc}<100 MHz Prescaler: 32

Frequency Synthesizer: Analog Devices - ADF4106

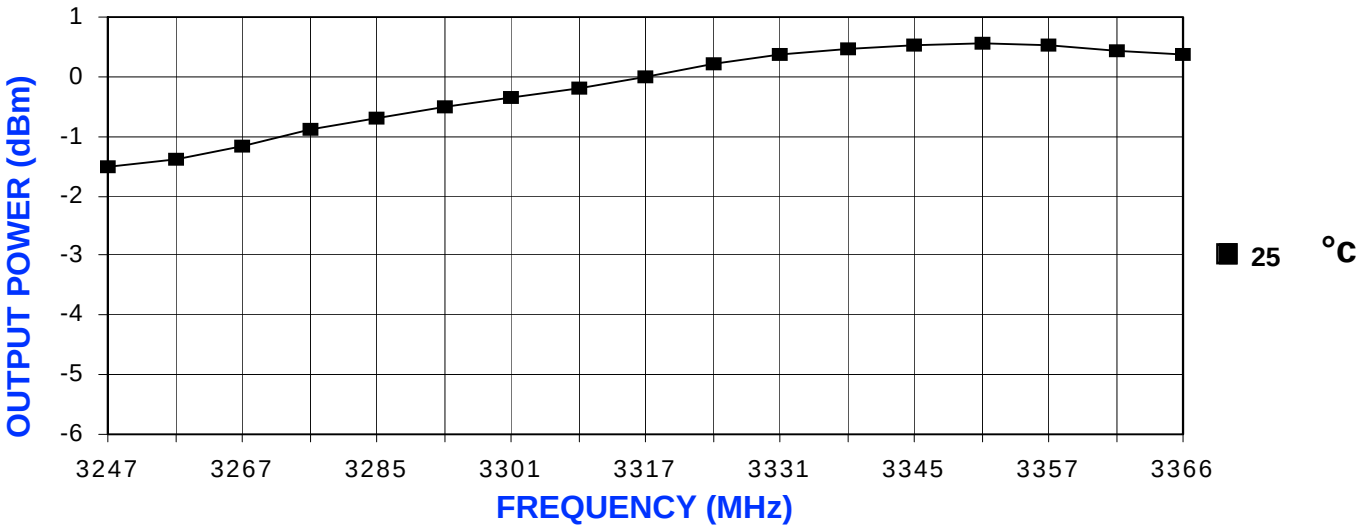
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VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

