

General Description

The WST3416 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.1V. This device is suitable for use as a load switch or in PWM applications. It is ESD protected.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent C_{dv}/dt effect decline
- Green Device Available

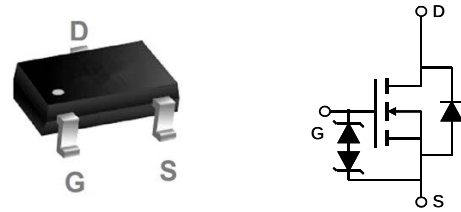
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
20V	18m Ω	6.5A

Applications

- High Frequency Point-of-Load Synchronous
Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT-23-3L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 8	V
$I_D@T_c=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	6.5	A
$I_D@T_c=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	5.2	A
I_{DM}	Pulsed Drain Current ²	30	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ³	1.4	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	125	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	80	$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1mA$	---	0.018	---	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=6.5A$	---	18	24	$m\Omega$
		$V_{GS}=2.5V, I_D=5.5A$	---	22	28	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	0.4	0.7	1.1	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-3.1	---	mV/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=16V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=6.5A$	---	50	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	1.8	3	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=15V, V_{GS}=4.5V, I_D=6.5A$	---	10	15.0	nC
Q_{gs}	Gate-Source Charge		---	4.2	8.9	
Q_{gd}	Gate-Drain Charge		---	2.6	5.2	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=10V, V_{GS}=4.5V, R_G=3\Omega$ $RL=1.54\Omega$	---	280	---	ns
T_r	Rise Time		---	328	---	
$T_{d(off)}$	Turn-Off Delay Time		---	3.76	---	μs
T_f	Fall Time		---	2.24	---	
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	---	1295	---	pF
C_{oss}	Output Capacitance		---	160	---	
C_{rss}	Reverse Transfer Capacitance		---	87	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	2.0	A
I_{SM}	Pulsed Source Current ^{2,4}		---	---	10	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.1	V
t_{rr}	Reverse Recovery Time	$IF=6.5A, dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	31	41	nS
Q_{rr}	Reverse Recovery Charge		---	6.8	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, $t \leq 10\text{sec}$.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

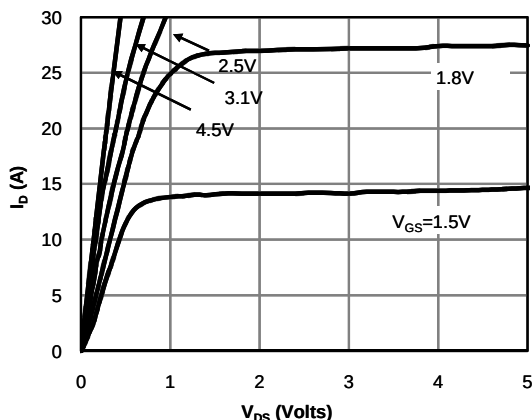


Figure 1: On-Region Characteristics (Note E)

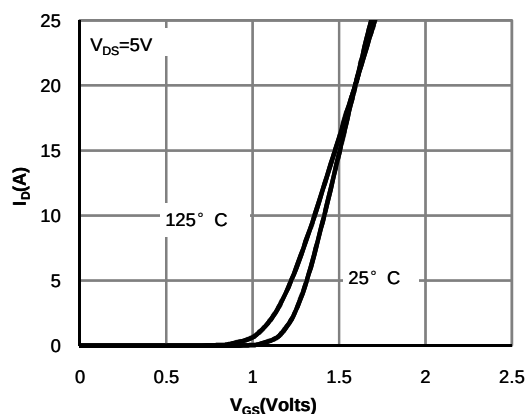


Figure 2: Transfer Characteristics (Note E)

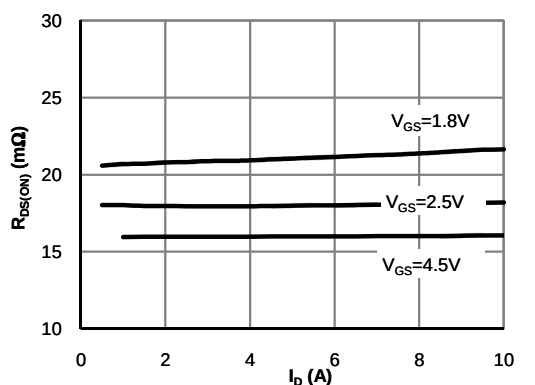


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

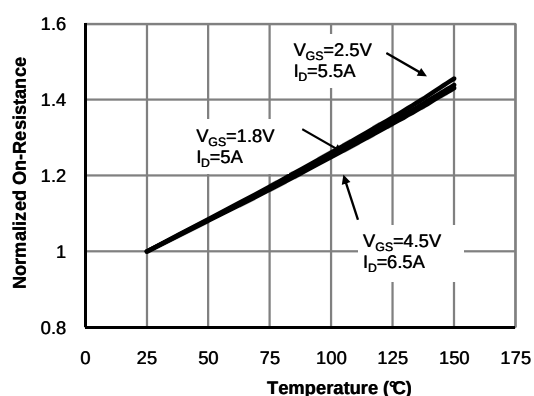


Figure 4: On-Resistance vs. Junction Temperature (Note E)

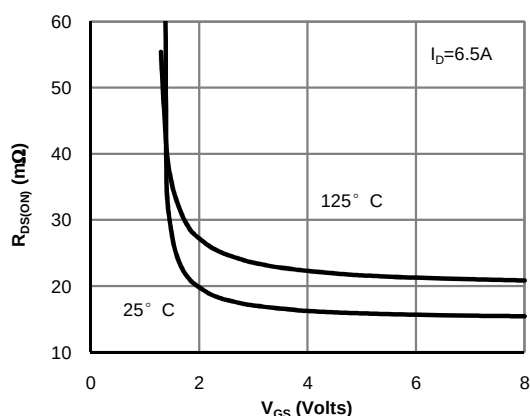


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

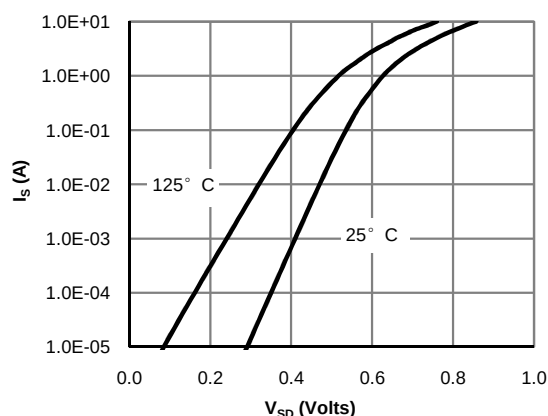
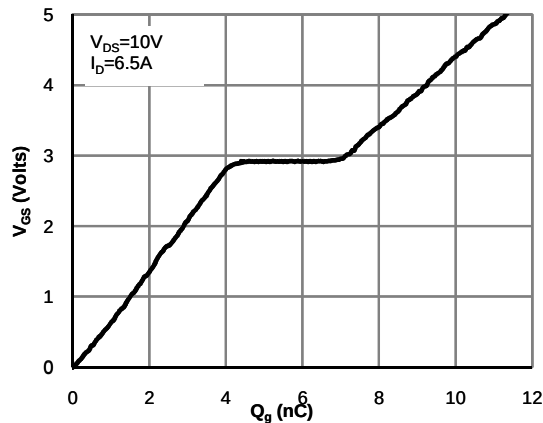
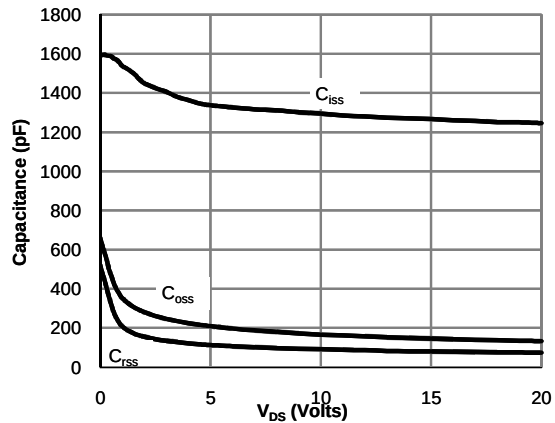
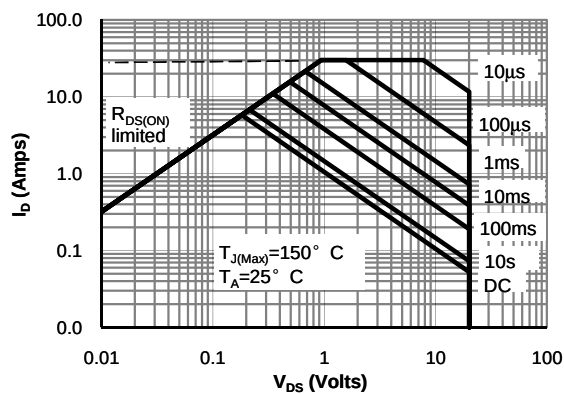
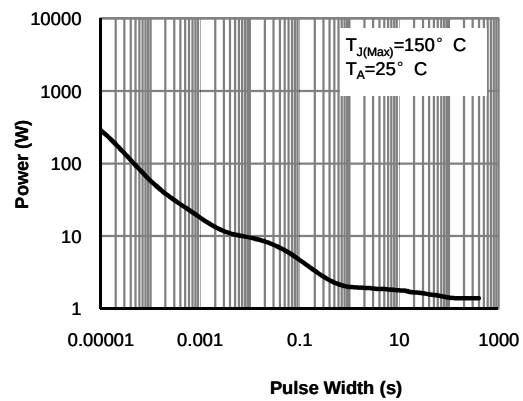
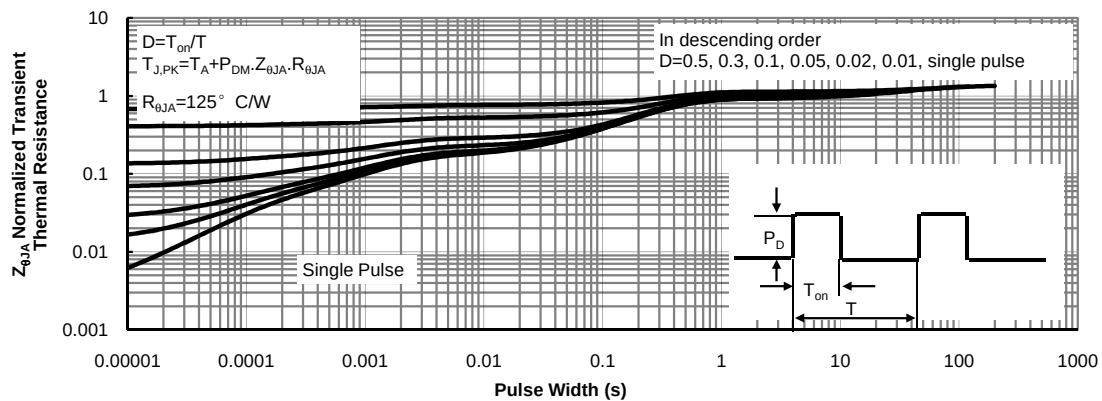


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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