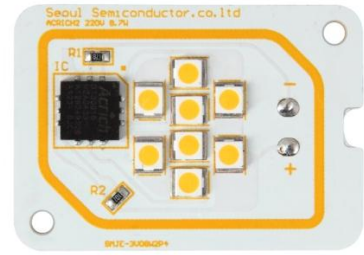


Integrated AC LED Solution

**Acrich2 – 8.7W Eco**

SMJE-XV08W2P4



## Product Brief

### Description

The Acrich2 series of products are designed to be driven directly off the AC line voltage (with an SPC board) and do not need the typical AC/DC converters essential for conventional lighting products.

The converter or the driver found in most general lighting products can limit the overall life of the product, but with the Acrich2 series the lifetime of the product is based on the lifetime of the LED itself.

The Acrich2 series also allows for maximum design flexibility to create a compact and aesthetic luminaire.

The modules also have a high power factor which can contribute to additional energy savings in the end application.

### Features and Benefits

- Connects directly to AC line voltage through a Surge Protection circuit (SPC2.5)
- High Power Factor and Low THD
- Long Life Time with Simple BOM
- Miniaturization
- Lead Free Product
- RoHS Compliant

### Key Applications

- Bulb light
- Down light

**Table 1. Product Selection (CCT)**

Part No.	Vin [Vac]	P [W]	Color	CCT [K]	CRI
					Min.
SMJE-2V08W2P4	120	8.7	Cool	4700 – 6000	80
			Neutral	3700 - 4200	
			Warm	2600 – 3200	
SMJE-3V08W2P4	220	8.7	Cool	4700 – 6000	
			Neutral	3700 - 4200	
			Warm	2600 – 3200	

**Table 2. Product Selection (Flux)**

Part No.	Vin [Vac]	P [W]	Flux Bin	Flux [lm]	
				Min.	Typ.
SMJE-2V08W2P4	120	8.7	-	540	670
SMJE-3V08W2P4	220				

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## Performance Characteristics

**Table 3. Electro Optical Characteristics,  $T_a = 25^\circ\text{C}$** 

Parameter	Symbol	Value			Unit	Mark
		Min.	Typ.	Max.		
Luminous Flux	$\Phi_V$ [2]	540	670	-	lm	-
Correlated Color Temperature [3]	CCT	5300	5600	6000	K	B
		4700	5000	5300		C
		3700	4000	4200		E
		2900	3000	3200		G
		2600	2700	2900		H
CRI	Ra	80	-	-	-	-
Input Voltage [4]	$V_{in}$	120			Vac	2V
		220				3V
Power Consumption	P	8.0	8.7	9.3	W	08W
Operating Frequency	f	50 / 60			Hz	
Power Factor	PF	Over 0.97			-	
Viewing Angle	$2\Theta_{1/2}$	120			deg.	

**Notes :**

- (1) At 120Vac/220Vac,  $T_a = 25^\circ\text{C}$
- (2)  $\Phi_V$  is the total luminous flux output measured with an integrated sphere.
- (3) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram.
- (4) Operating Voltage doesn't indicate the maximum voltage which customers use but means tolerable voltage according to each country's voltage variation rate. It is recommended that the solder pad temperature should be below  $70^\circ\text{C}$ .

## Absolute Maximum Ratings

**Table 4. Absolute Maximum Ratings,  $T_a = 25^\circ\text{C}$** 

Parameter	Symbol	Unit	Value
Maximum Input Voltage @120Vac	$V_{in}$	Vac	140
Maximum Input Voltage @220Vac			264
Power Consumption	P	W	11.5
Operating Temperature	$T_{opr}$	$^\circ\text{C}$	-30 ~ 85
Storage Temperature	$T_{stg}$	$^\circ\text{C}$	-40 ~ 100
ESD Sensitivity	-	-	$\pm 4,000\text{V HBM}$

## Thermal Resistance

Part	Package Power Dissipation [W]	Maximum Junction Temperature [°C]	$R_{\theta_{js}}$ [°C/W]
Acrich2 LED	SAW8WA2A Max 1.55	125	23
Acrich2 IC	Max 1.0	150	11.25

The Acrich2 LED has a thermal resistance of 23°C/W from junction of the LED to the LED lead.

The maximum junction temperature of the Acrich2 LED package is 125°C, therefore the maximum lead temperature  $T_{s\_max}$  is

$$\begin{aligned}
 T_{s\_max} &= T_{j\_max} - (R_{\theta_{js}} * P_d) \\
 &= 125^\circ\text{C} - (23^\circ\text{C/W} * 1.55\text{W}) = 89.35^\circ\text{C}
 \end{aligned}$$

Although this is the maximum lead temperature, it is recommended to keep the lead temperature under 70°C.

The Acrich2 IC has a 11.25°C/W thermal resistance from junction to the top surface.

The maximum junction temperature ( $T_{j\_max}$ ) of the IC is 150°C, therefore the maximum temperature of the top of the IC is

$$\begin{aligned}
 T_{t\_max} &= T_{j\_max} - (R_{\theta_{jt}} * P_d) \\
 &= 150^\circ\text{C} - (11.25^\circ\text{C/W} * 1.0\text{W}) = 138.75^\circ\text{C}
 \end{aligned}$$

## Relative Spectral Distribution

Fig 1. Relative Spectral Distribution vs. Wavelength Characteristic – G, H

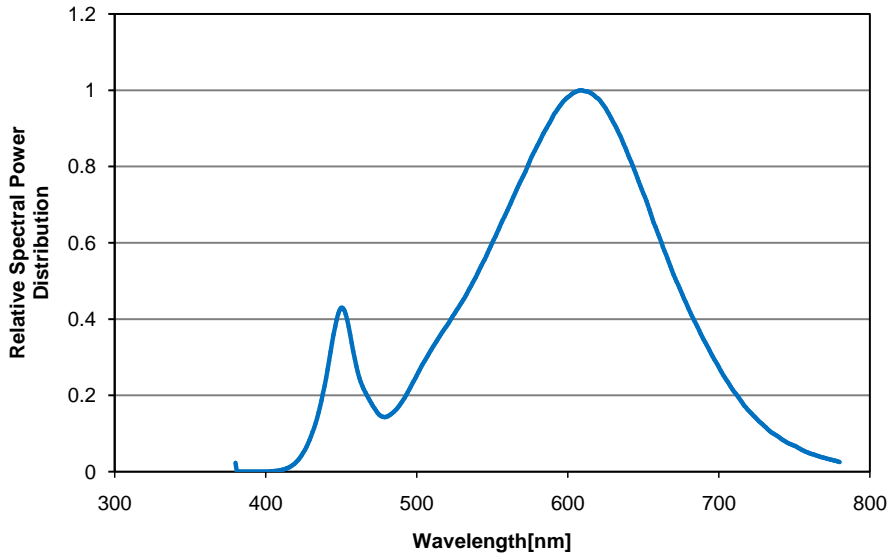
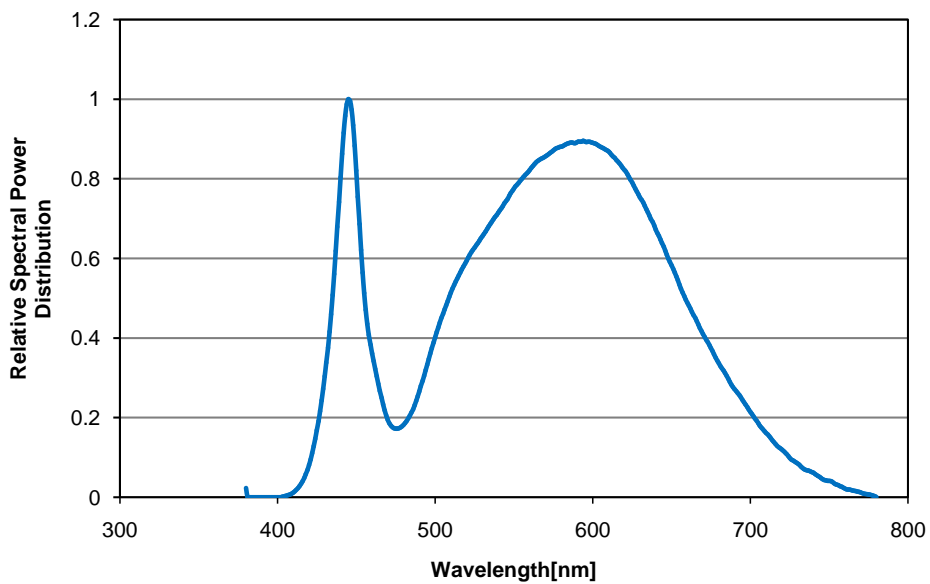
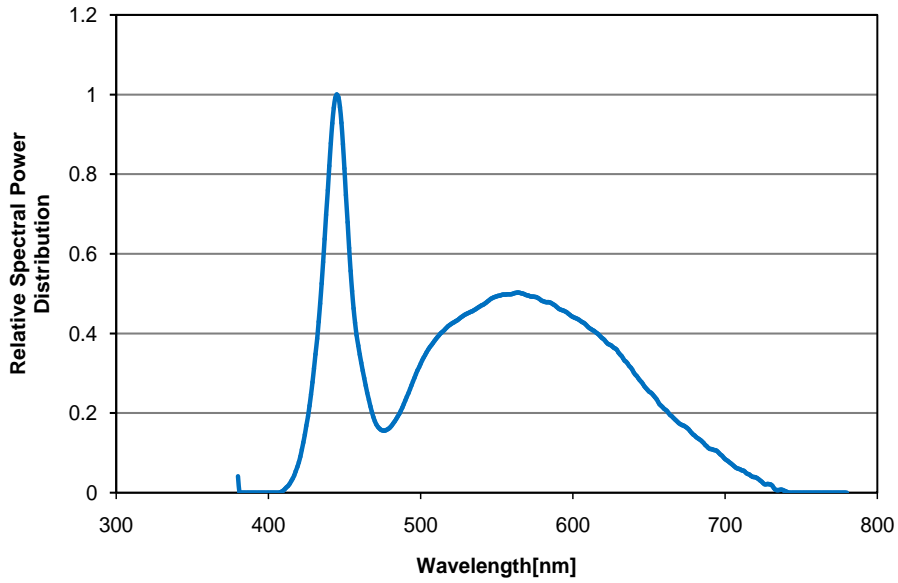


Fig 2. Relative Spectral Distribution vs. Wavelength Characteristic – E

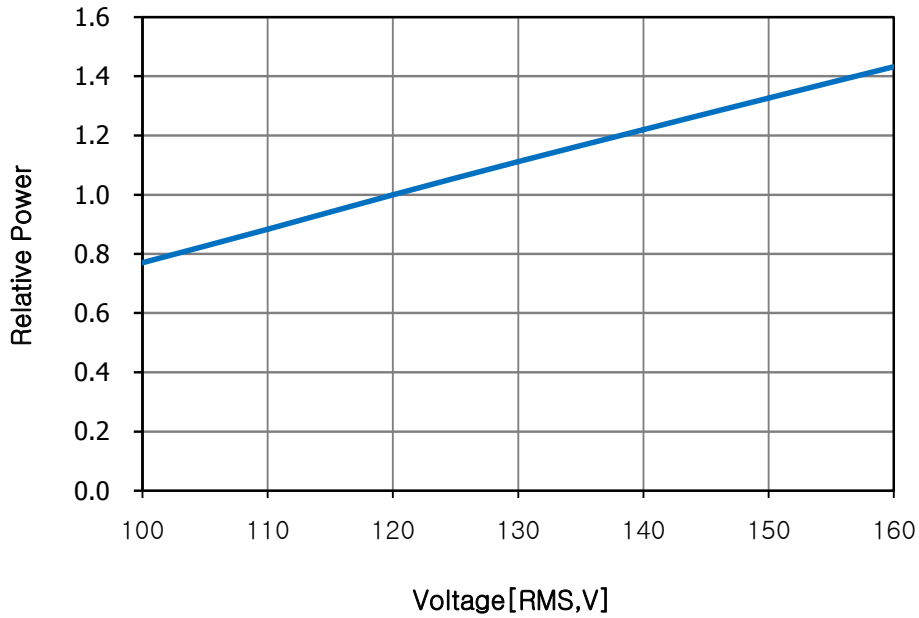
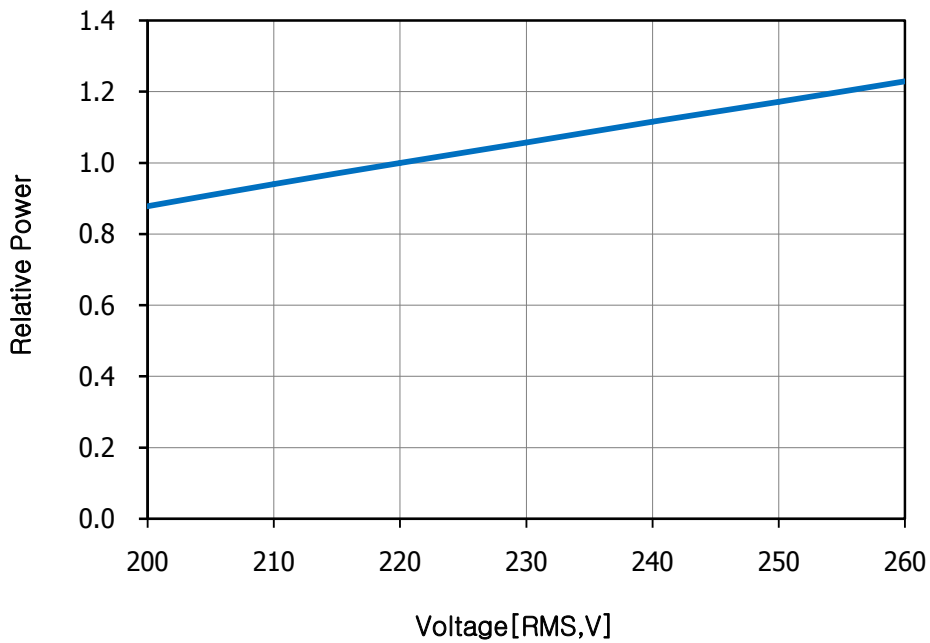


## Relative Spectral Distribution

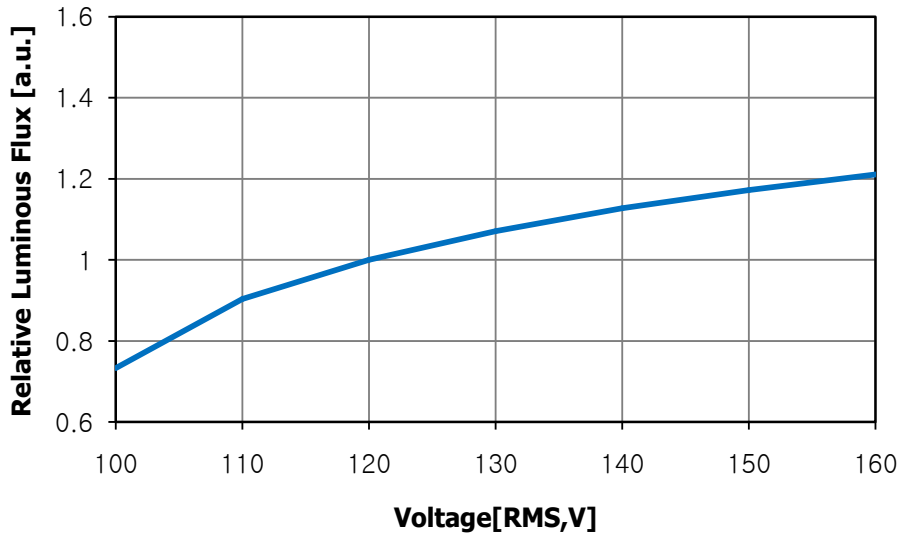
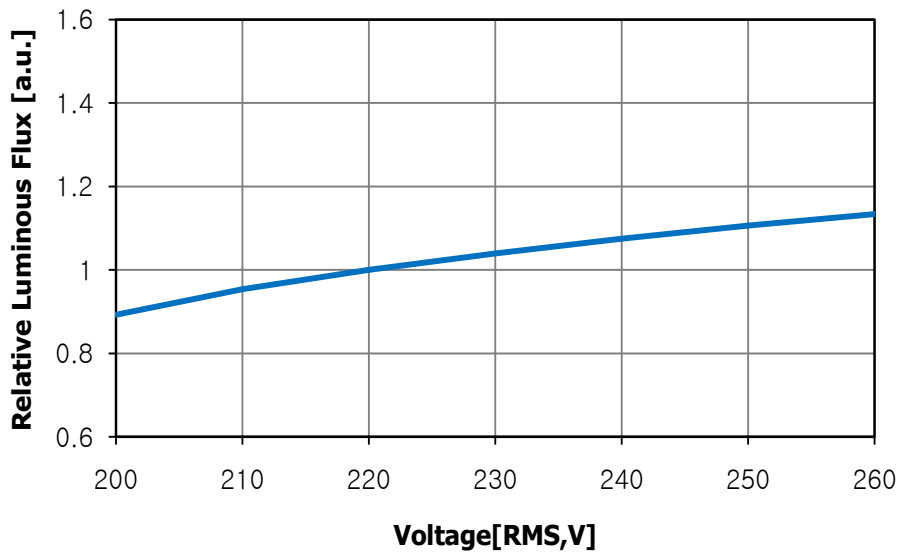
Fig 3. Relative Spectral Distribution vs. Wavelength Characteristic – B, C



## Relative Power Distribution

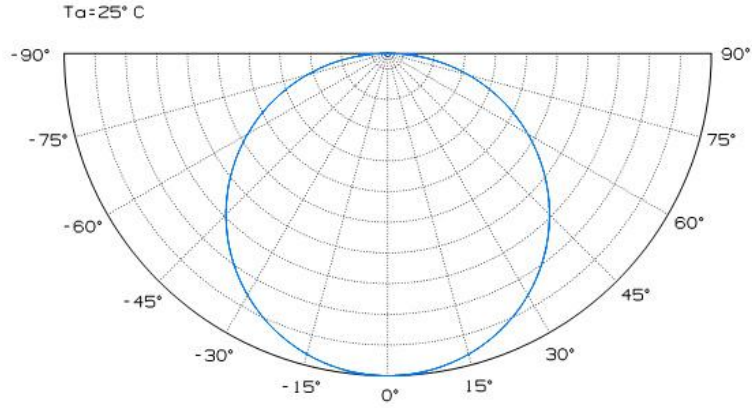
**Fig 4. Relative Power Distribution vs. Voltage at  $T_a = 25^\circ\text{C}$ , 120V**

**Fig 5. Relative Power Distribution vs. Voltage at  $T_a = 25^\circ\text{C}$ , 220V**


## Relative Luminous Distribution

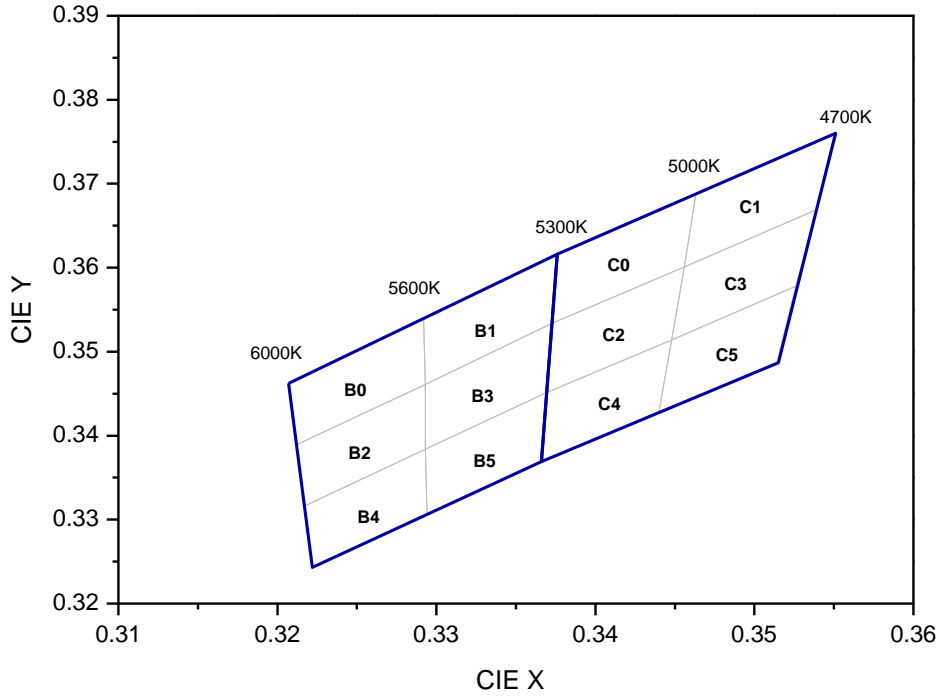
**Fig 6. Relative Luminous Flux vs. Voltage at  $T_a=25^\circ\text{C}$ , 120V**

**Fig 7. Relative Luminous Flux vs. Voltage at  $T_a=25^\circ\text{C}$ , 220V**


## Luminous Flux Characteristics

Fig 8. Radiant Pattern,  $T_a = 25^\circ\text{C}$

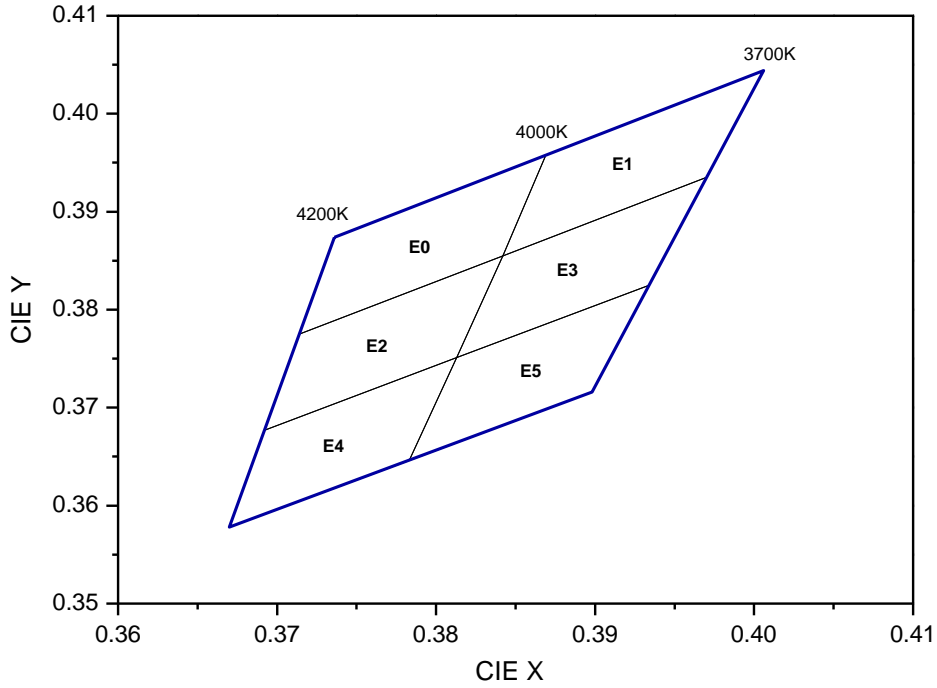


## Color Bin Structure

**CIE Chromaticity Diagram**


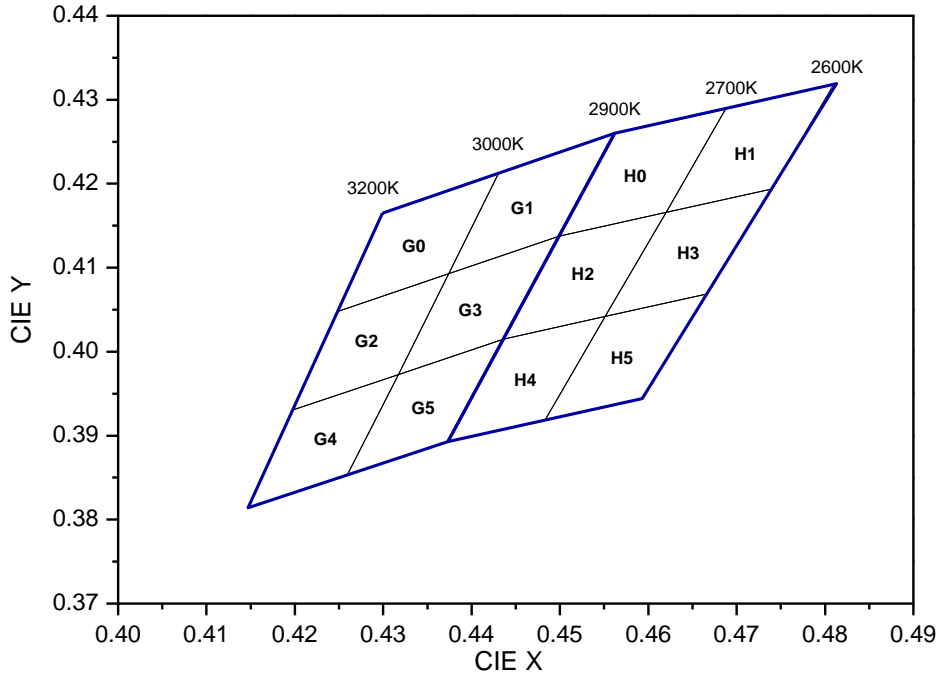
Bin code	X	Y	Bin code	X	Y	Bin code	X	Y
B0	0.3207	0.3462	B1	0.3292	0.3539	B2	0.3212	0.3389
	0.3212	0.3389		0.3293	0.3461		0.3217	0.3316
	0.3293	0.3461		0.3373	0.3534		0.3293	0.3384
	0.3292	0.3539		0.3376	0.3616		0.3293	0.3461
B3	0.3293	0.3461	B4	0.3217	0.3316	B5	0.3293	0.3384
	0.3293	0.3384		0.3222	0.3243		0.3294	0.3306
	0.3369	0.3451		0.3294	0.3306		0.3366	0.3369
	0.3373	0.3534		0.3293	0.3384		0.3369	0.3451
C0	0.3376	0.3616	C1	0.3463	0.3687	C2	0.3373	0.3534
	0.3373	0.3534		0.3456	0.3601		0.3369	0.3451
	0.3456	0.3601		0.3539	0.3669		0.3448	0.3514
	0.3463	0.3687		0.3552	0.3760		0.3456	0.3601
C3	0.3456	0.3601	C4	0.3369	0.3451	C5	0.3448	0.3514
	0.3448	0.3514		0.3366	0.3369		0.3440	0.3428
	0.3526	0.3578		0.3440	0.3428		0.3514	0.3487
	0.3539	0.3669		0.3448	0.3514		0.3526	0.3578

## Color Bin Structure



Bin code	X	Y	Bin code	X	Y	Bin code	X	Y
E0	0.3736	0.3874	E1	0.3869	0.3958	E2	0.3714	0.3775
	0.3714	0.3775		0.3842	0.3855		0.3692	0.3677
	0.3841	0.3855		0.3970	0.3935		0.3813	0.3751
	0.3869	0.3958		0.4006	0.4044		0.3842	0.3855
E3	0.3842	0.3855	E4	0.3692	0.3677	E5	0.3813	0.3751
	0.3813	0.3751		0.3670	0.3578		0.3783	0.3646
	0.3934	0.3825		0.3783	0.3646		0.3898	0.3716
	0.3970	0.3935		0.3813	0.3751		0.3934	0.3825

## Color Bin Structure



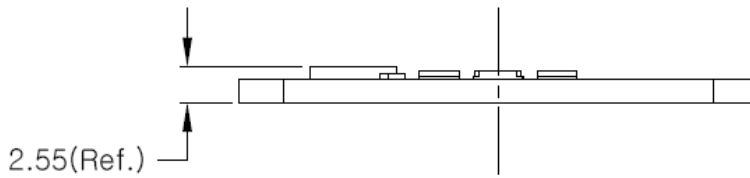
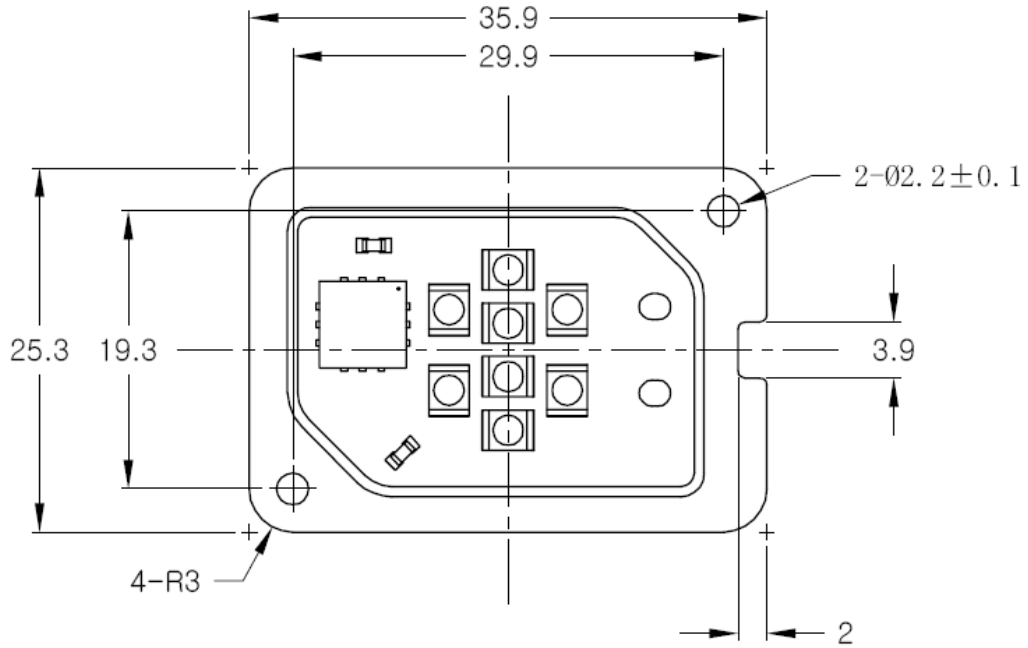
Bin code	X	Y	Bin code	X	Y	Bin code	X	Y
G0	0.4299	0.4165	G1	0.4430	0.4212	G2	0.4248	0.4048
	0.4248	0.4048		0.4374	0.4093		0.4198	0.3931
	0.4374	0.4093		0.4499	0.4138		0.4317	0.3973
	0.4430	0.4212		0.4562	0.4260		0.4374	0.4093
G3	0.4374	0.4093	G4	0.4198	0.3931	G5	0.4317	0.3973
	0.4317	0.3973		0.4147	0.3814		0.4259	0.3853
	0.4436	0.4015		0.4259	0.3853		0.4373	0.3893
	0.4499	0.4138		0.4317	0.3973		0.4436	0.4015
H0	0.4562	0.4260	H1	0.4687	0.4289	H2	0.4499	0.4138
	0.4499	0.4138		0.4620	0.4166		0.4436	0.4015
	0.4620	0.4166		0.4740	0.4194		0.4551	0.4042
	0.4687	0.4289		0.4810	0.4319		0.4620	0.4166
H3	0.4620	0.4166	H4	0.4436	0.4015	H5	0.4551	0.4042
	0.4551	0.4042		0.4373	0.3893		0.4483	0.3919
	0.4666	0.4069		0.4483	0.3919		0.4593	0.3944
	0.4740	0.4194		0.4551	0.4042		0.4666	0.4069

## Part List

**Table 5. Part List**

No	Part	Reference	Specification	Quantity
1	PCB	-	Al, 35.9*25.3(mm), T=1.6, 1 layer / Cu 1oz / White PSR	1
2	LED	-	SAW8WA2A	8
3	IC	IC	DT3001A	1
4	Resistor	R1	R1608, 1.6k $\Omega$ , 1%(F) @120Vac	1
			R1608, 820 $\Omega$ , 1%(F) @220Vac	1
5		R2	R1608, 1k $\Omega$ , 1%(F)	1

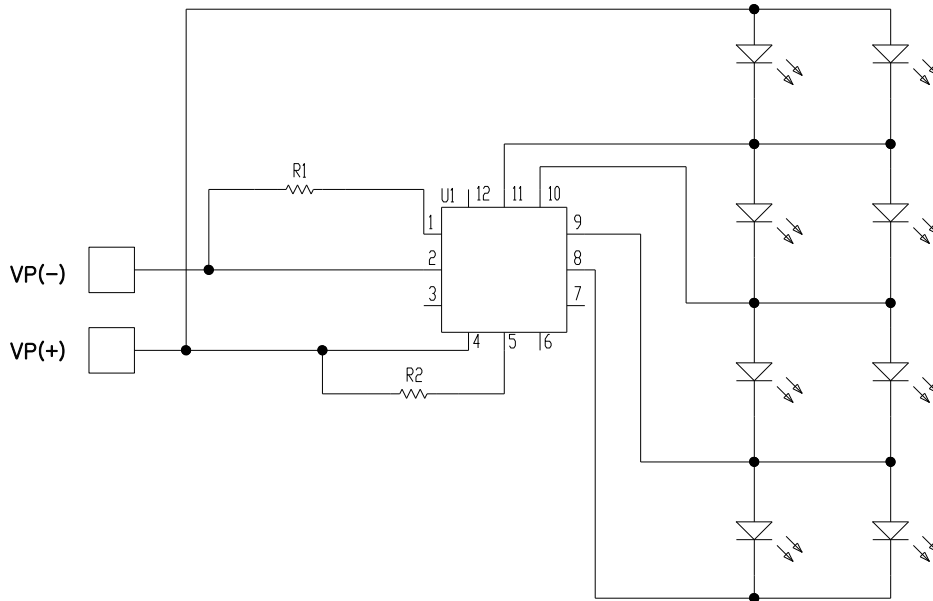
## Mechanical Dimensions


**Notes :**

- (1) All dimensions are in millimeters. (Tolerance :  $\pm 0.2$ )
- (2) Undefined R0.5
- (3) Scale : None

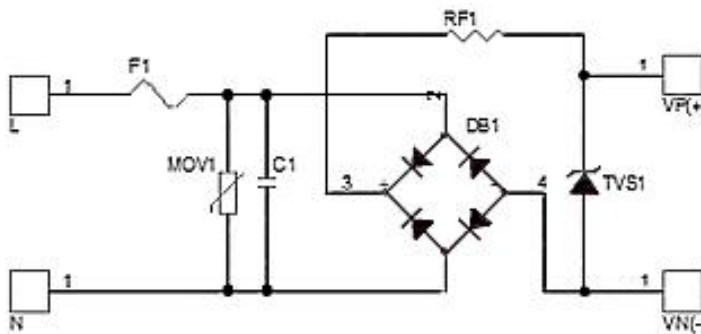
## Circuit Drawing

Acrich module



(SMJE-2V08W2P4)

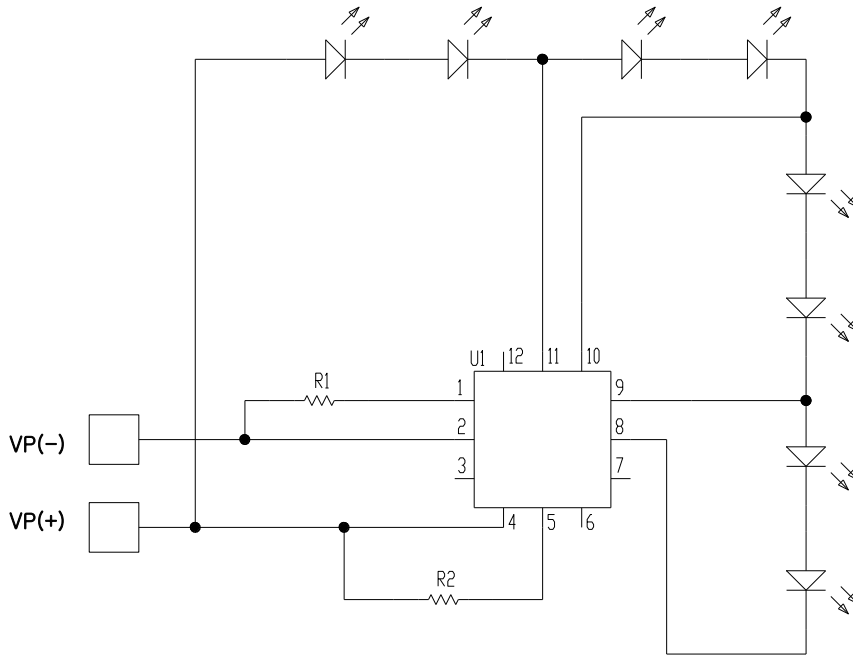
Surge Protection Circuit 2.5 (Additional daughter board)



(SMJJ-SPCR0AC2.5)

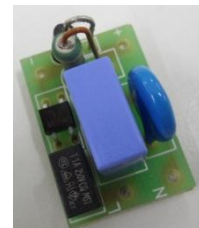
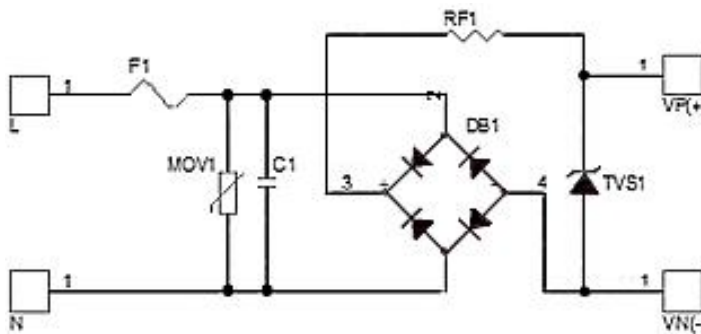
## Circuit Drawing

Acrich module



(SMJE-3V08W2P4)

Surge Protection Circuit 2.5 (Additional daughter board)



(SMJJ-SPCR0AC2.5)

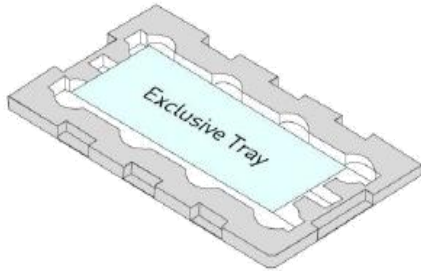
## Marking Information



- **XX00 : CCT**
  - H Rank : 2700
  - G Rank : 3000
  - E Rank : 4000
  - C Rank : 5000
  - B Rank : 5600
- **YYMMDD : Production Date**
- **AAA : LED PKG. Flux Rank**
- **BB : Color Bin**

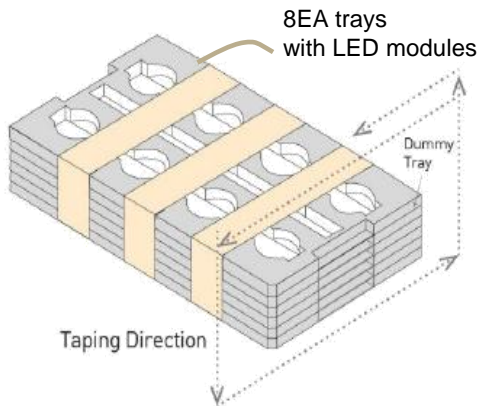
## Packing

### 1. Tray information



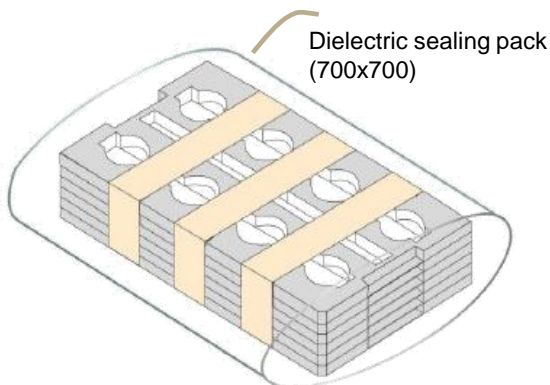
- 50 PCS LED modules packed per tray

### 2. Tray stack and taping

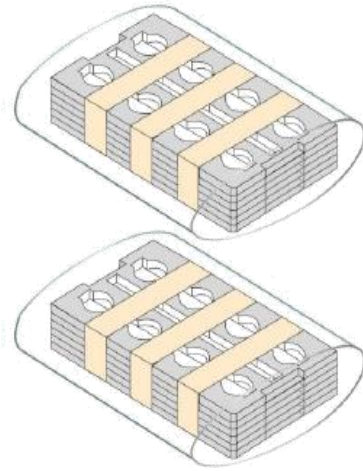


- 8 LED module trays and additional 2 dummy trays each up and down of box
- Add silica gel (1EA) on top of the tray

### 3. Sealing packing




### 4. Box information & packing



- 800 PCS modules per BOX 1EA


\*\* 1 Box : 50 PCS per tray x 16 trays = 800 PCS

## Label Information

<b>Model No.</b>	<b>SMJE-XV08W2P4 (1)</b> 
<b>Rank</b>	<b>ALLX06A (2)</b> 
<b>Type</b>	<b>STD (3)</b>
<b>Quantity</b>	<b>XX</b> 
<b>Lot No.</b>	<b>YYMDDXXXXX-XXXXXXX</b> 
	<b>SEOUL SEMICONDUCTOR CO.,LTD.</b>

**Notes**

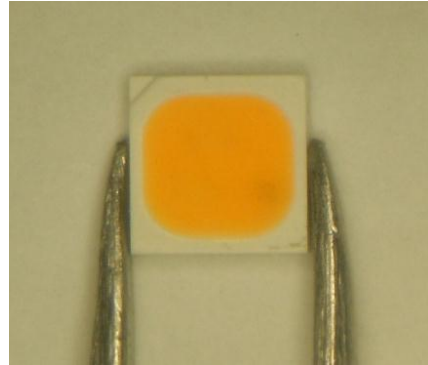
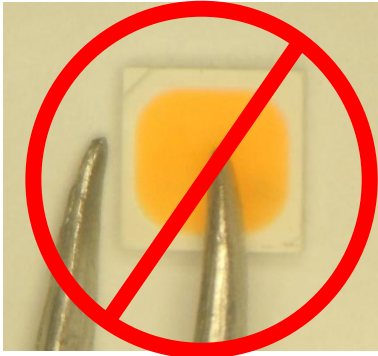
- (1) The model number designation is explained as follow  
 SMJE : Seoul Semiconductor internal code  
 XV : Input Voltage (2V = 120V, 3V = 220V)  
 08W : About Power Consumption  
 2 : Acrich IC Version  
 P4 : MJT PKG (SAW8WA2A)
- (2) It represents the LED module rank.  
 ALL : Single Bin  
 X06 : Each Sub-Bin NO. (X = CCT)  
 A : Single Bin
- (3) It represents single bin(STD).
- (4) It is attached to the top of sealing pack & the bottom right corner of the box.

<p><b>TOTAL Quantity</b></p> <p>                           </p> <p><b>XX</b></p>
 <b>SEOUL SEMICONDUCTOR CO.,LTD.</b>

**Notes**

- (1) It is attached to the bottom right corner of the box.

## Handling of Silicone Resin for LEDs



- (1) Acrich2 series is encapsulated with silicone resin for high optical efficiency.
- (2) Please do not touch the silicone resin area with sharp objects such as pincette(tweezers).
- (3) Finger prints on silicone resin area may affect the performance.
- (4) Please store LEDs in covered containers to prevent dust accumulation as this may affect performance.
- (5) Excessive force more than 3000gf to the silicone lens can result in fatal or permanent damage with LEDs.
- (6) Please do not cover the silicone resin area with any other resins such as epoxy, urethane, etc.

## Precaution for Use

- (1) Please review the Acrich2 Application Note for proper protective circuitry usage.
- (2) Please note, Acrich2 products run off of high voltage, therefore caution should be taken when working near Acrich2 products.
- (3) Make sure proper discharge prior to starting work.
- (4) DO NOT touch any of the circuit board, components or terminals with body or metal while circuit is active.
- (5) Please do not add or change wires while Acrich2 circuit is active.
- (6) Long time exposure to sunlight or UV can cause the lens to discolor.
- (7) Please do not use adhesives to attach the LED that outgas organic vapor.
- (8) Please do not use together with the materials containing Sulfur.
- (9) Please do not assemble in conditions of high moisture and/or oxidizing gas such as Cl, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, NO<sub>x</sub>, etc.
- (10) Please do not make any modification on module.
- (11) Please be cautious when soldering to board so as not to create a short between different trace patterns.
- (12) Do not impact or place pressure on this product because even a small amount of pressure can damage the product. The product should also not be placed in high temperatures, high humidity or direct sunlight since the device is sensitive to these conditions.
- (13) When storing devices for a long period of time before usage, please following these guidelines:
  - \* The devices should be stored in the anti-static bag that it was shipped in from Seoul-Semiconductor with opening.
  - \* If the anti-static bag has been opened, re-seal preventing air and moisture from being present in the bag.
- (14) LEDs and IC are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). The Acrich2 product should also not be installed in end equipment without ESD protection. Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

### a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

## Precaution for Use

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

### b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package  
(If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package  
(shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires
- This damage usually appears due to the thermal stress produced during the EOS event

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device



## Company Information

### Published by

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### Company Information

Seoul Semiconductor ([www.SeoulSemicon.com](http://www.SeoulSemicon.com)) manufactures and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

### Legal Disclaimer

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