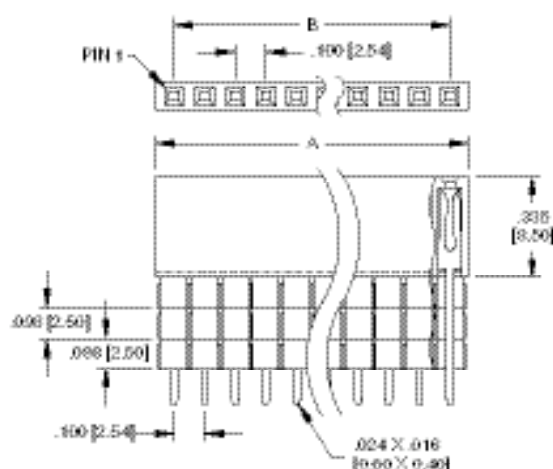
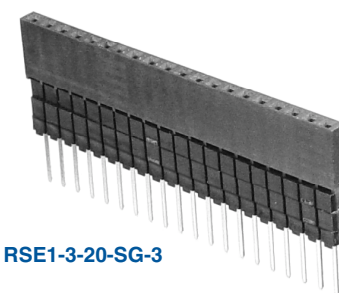
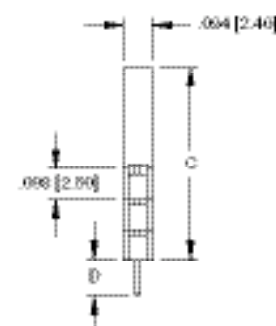
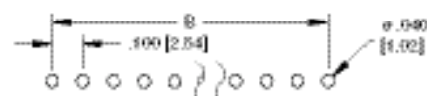




$$B = .100^{(2.54)} \times \text{No. of spaces}$$

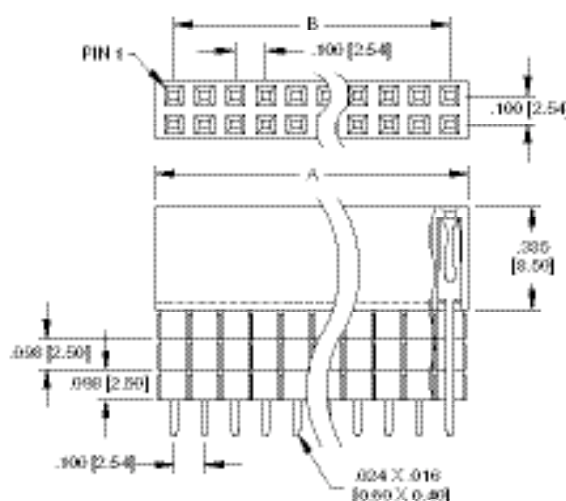


RSE1-3-20-SG-3

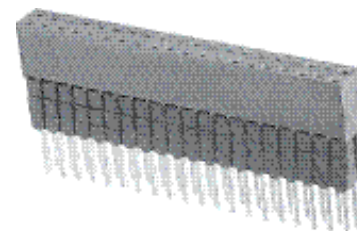
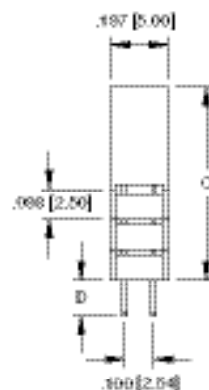


Recommended PCB Layout

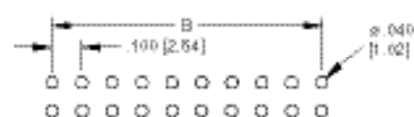
RSE1



$$B = 100^B [2.54] \times \text{No. of spaces}$$



RSE2-3-40-SG-3



Recommended PCB Layout

RSE2

ORDERING INFORMATION

RSE1

2

20

SG

1

SERIES INDICATOR

RSE1 = Single row,
vertical
elevated
socket strip

RSE2 = Dual row,
vertical
elevated
socket strip

POSITIONS

Single Row
01 thru 40

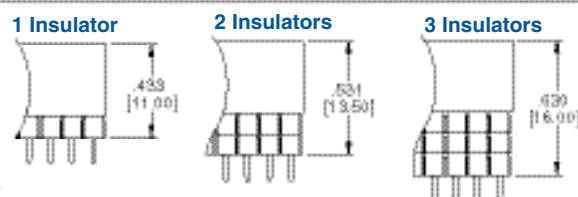
Dual Row
02 thru 80

HEIGHT

1 = .433 [11.00]
2 = .531 [13.50]
3 = .630 [16.00]

PLATING

SG = Selective Gold
Plating in contact area,
Tin Plated tails
T = Tin Plated



PART NUMBER	INSULATORS	DIM. C	DIM. D
PSEK-1-XC-SG-1	1	.400 [11.00]	.110 [3.00]
PSEK-1-XC-SG-2	1	.400 [11.00]	.310 [8.00]
PSEK-1-XC-SG-3	1	.400 [11.00]	.440 [11.40]
PSEK-2-XC-SG-1	2	.501 [13.00]	.210 [5.50]
PSEK-3-XC-SG-1	3	.604 [16.12]	.110 [3.00]
PSEK-3-XC-SG-2	3	.604 [16.12]	.260 [6.40]