



PRODUCT SPECIFICATION

Part Number

PT644857E-TLMWD-EMR04

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	5.7" TFT LCD, Medium Bright, RTP
APPROVED BY	
DATE	

**P-TEC****MODEL NO.**

PT644857E-TLMWD-EMR04

**SPEC &
SAMPLE****PAGE**

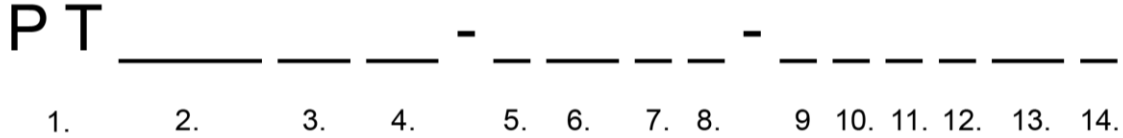
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3. Module Numbering System



1. P-TEC TFT

8. VIEWING DIRECTION

D: 6 o'clock
U: 12 o'clock
F: Full Viewing Angle

2. LENGTH x WIDTH PIXELS

If third character is a zero, it is removed to shorten part number. Example: 240 x 320 = PT3224

9. A ~ Z CODE

Assigned by P-tec

3. DIAGONAL DIMENSIONS

Example: 3.5" display = 35 in part number

11. TEMPERATURE RANGE

Normal: Left Blank
Wide: X

4. PRODUCT VERSION

Series assigned by P-tec

12. LUMINANCE

Blank: Normal (<300 nit)
M: Middle (>= 300 nit)
H: High (> 600 nit)

5. LCD MODE

T: TN
I: IPS
V: VA

13. TOUCH PANEL OPTION

No TP: Left Blank
C: Capacitive TP
R: Resistive TP

6. POLARIZER

LM: Transmissive
LF: Transflective

14. SPECIAL CHARACTERS

Customer special requirements

7. BACKLIGHT COLOR

No Backlight: Left Blank
W: White
B: Blue/Green
S: Yellow/Green



4. Application

This specification is applied to the 5.7 inch VGA supported TFT-LCD module, and can display true 262,144 colors(6 bit/ color). The module is designed for OA, Car TV application and other electronic products which require flat panel display of digital signal interface. This module is composed of a 5.7”TFT-LCD panel, a driver circuit and LED backlight unit and used as the input devices for general electric appliances via both finger and pen-entry.

5. Features

- VGA (640×480 pixels) resolution.
- LVDS Receiver 18 bit Interface
- Dot inversion mode with stripe type.
- Transparent Touch panel
 - 4-Wire
 - Analog Resistive
 - Lower Operation Force

6. General Specifications

Item	Specifications	Unit
Screen Size	5.7 (Diagonal)	inch
Display Format	640RGB(H)×480(V)	dot
Active Area	115.2(H)×86.4(V)	mm
Dot Size	0.060(H)×0.180(V)	mm
Pixel Configuration	RGB Vertical Stripe	-
Display Mode	TN Type Transmissive Mode Normally White	-
Surface Treatment	Anti-Glare and Hard Coating(3H)	-
Viewing Direction	6 O'clock (The Gray Inversion will appear at this direction)	-
Outline Dimension	144.0(W)×104.6(H)×14.5(D)	mm
LVDS Receiver IC	THine THC63LVDF84B	-
Weight	194	g
RoHS Compliance	P-tec certifies this product to be in compliance with European Union Directive 2011/65/EU on the restriction of certain hazardous substances in electrical and electronic equipment. _	-



7. Absolute Maximum Ratings

7.1 Absolute Ratings of Environment

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T _{ST}	-30	+80	°C	(1)(3)
Operating Temperature	T _{OP}	-20	+70	°C	(1)(2)

Note1: Background color changes slightly depending on ambient temperature.
This phenomenon is reversible.

Note2: Ta ≤ 70°C: 75%RH max.

Note3: Please refer to item of RELIABILITY.

7.2 Electrical Absolute Ratings

7.2.1 TFT-LCD Module

(Ta=25±2°C, GND=V_{SS}=0V)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	V _{CC}	-0.3	4.0	V	-
LVDS Driver Output Voltage	-	-0.3	V _{CC} + 0.3	V	-

7.2.2 Backlight Unit

(Ta=25±2°C)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Current of Backlight Unit	I _B	-	250	mA	(1)
Reverse voltage	V _R	-	15	V	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.

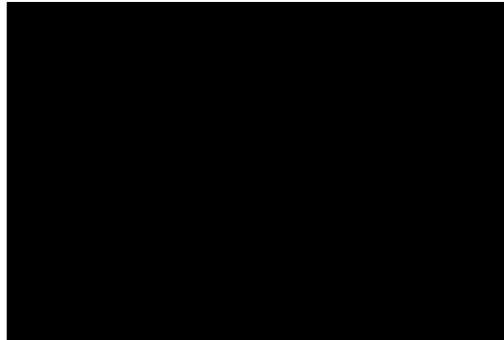
**8. Electrical Characteristics****8.1 TFT-LCD Module**

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V _{CC}	3.0	3.3	3.6	V	-
Power Supply Current	I _{CC}	-	225	315	mA	-
Differential Input High Threshold Voltage	V _{TH}	-	-	100	mV	-
Differential Input Low Threshold Voltage	V _{TL}	-100	-	-	mV	-
Power Consumption	P _L	-	(742.5)	1040	mW	(1)
Frame Frequency	F _V	-	60	-	Hz	-
Dot Clock	DCLK	-	25.175	-	MHz	-

Note (1) The specified power consumption is under the conditions at V_{CC}=3.3V, F_V=60Hz, whereas a power dissipation check pattern below is displayed.

Black Pattern / 0 Gray



Active Area

8.2 Backlight Unit

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Current of Backlight Unit	I _B	-	200	-	mA	-
Voltage of Backlight Unit	V _B	-	9.9	-	V	I _B =200mA
Power Consumption	P _{BL}	-	(1.98)	-	W	I _B =200mA
LED Life Time(25°C)	-	40000	50000	-	hr	-



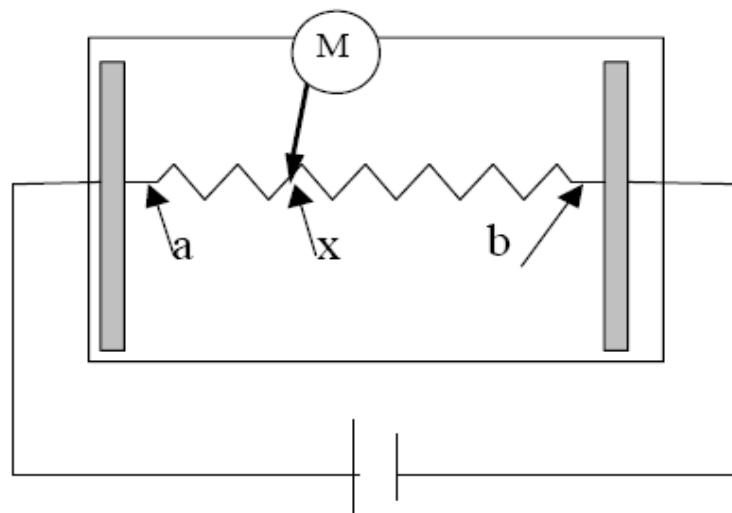
8.3 Transparent Touch panel

Electrical characteristics

Item		Value			Unit	Note
		Min.	Typ.	Max.		
Operating Voltage		-	5	7	V	-
Terminal Resistance	X-direction	290	-	880	Ω	At connector
	Y-direction	260	-	530	Ω	At connector
Insulation Resistance		$\geq 20M\Omega$				At DC25V
Linearity		$\leq 1.5\%$				(1)
Chatting		≤ 10 ms				At connector

Note 1: Measurement condition of Linearity

Linearity Definition



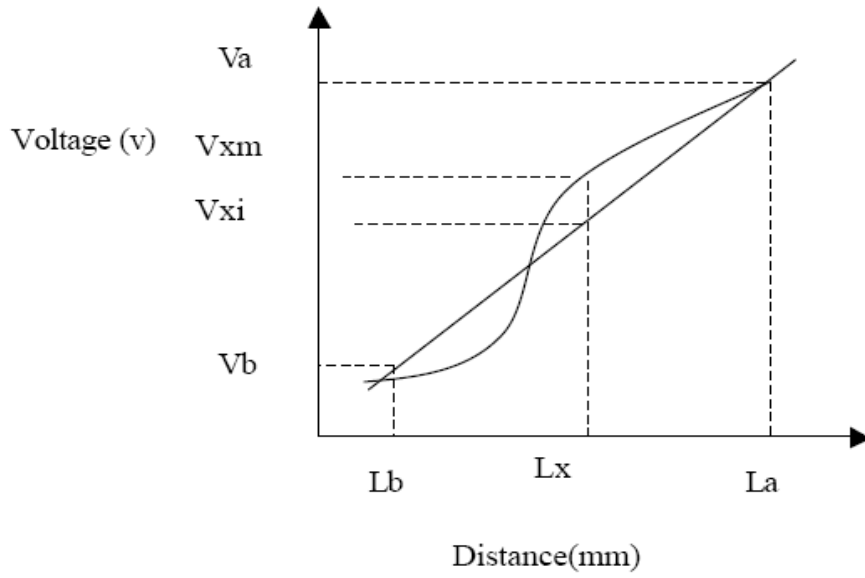
Va : maximum voltage in the active area of touch panel

Vb: minimum voltage in the active area of touch panel

X : random measuring point

Vxm: Actual voltage of Lx point

Vxi : Theoretical voltage of Lx point

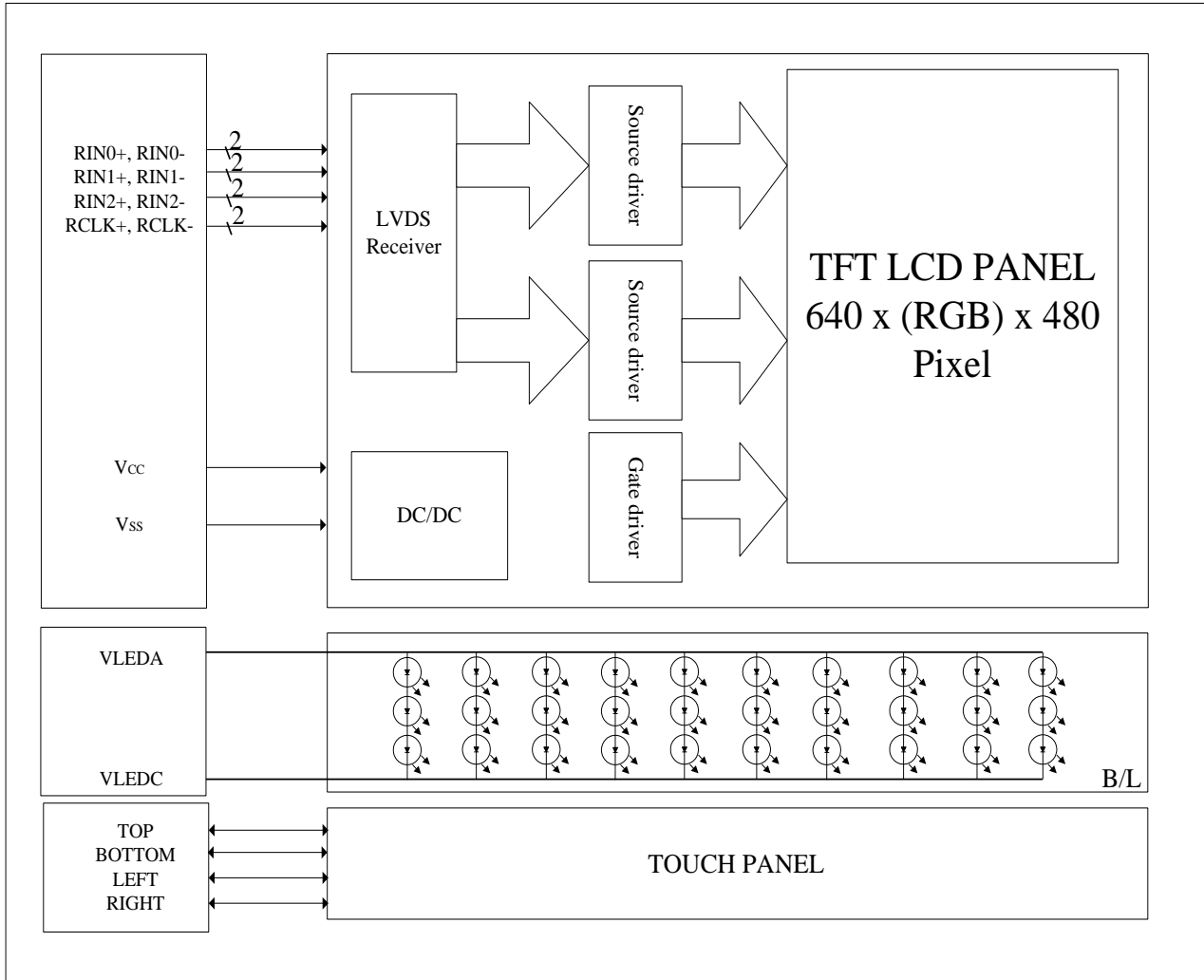


$$\text{Linearity} : [| V_{xi} - V_{xm} | / (V_a - V_b)] * 100\%$$



9. Block Diagram

9.1 TFT-LCD Module with Backlight Unit





10. Input / Output Terminals Pin Assignment

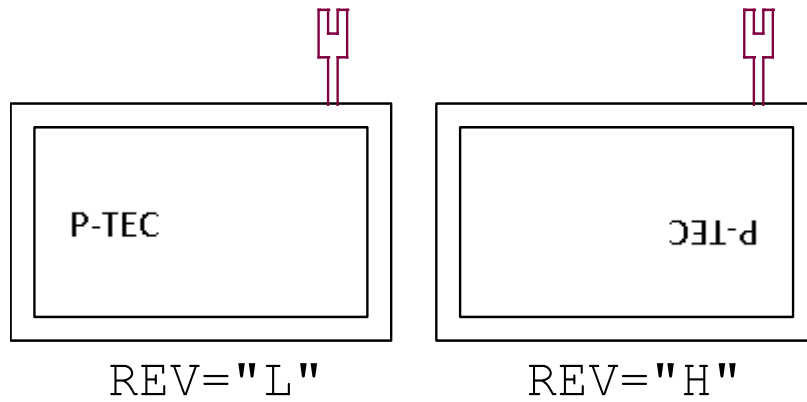
10.1 TFT-LCD Module

Connector: HIROSE DF19G-20P-1H

Pin No.	Symbol	I/O	Description
1	Vcc	I	+3.3V power supply
2	Vcc	I	+3.3V power supply
3	Vss	I	Ground
4	Vss	I	Ground
5	RIN0-	I	Negative LVDS differential data input
6	RIN0+	I	Positive LVDS differential data input
7	Vss	I	Ground
8	RIN1-	I	Negative LVDS differential data input
9	RIN1+	I	Positive LVDS differential data input
10	Vss	I	Ground
11	RIN2-	I	Negative LVDS differential data input
12	RIN2+	I	Positive LVDS differential data input
13	Vss	I	Ground
14	RCLK-	I	Negative LVDS differential clock input
15	RCLK+	I	Positive LVDS differential clock input
16	Vss	I	Ground
17	NC	I	Not connection
18	NC	I	Not connection
19	REV	I	Selection signal for horizontal/ vertical scanning direction. Note (1)
20	Vss	I	Ground



Note (1)



10.2 Backlight Unit

Connector: JST BHSR-02VS-1(N)

Pin No.	Symbol	I/O	Description	Wire Color
1	VLEDA	I	Backlight LED Anode.	Red
2	VLEDC	I	Backlight LED Cathode.	Black

10.3 Transparent Touch Panel

Connector: CVILUX CF25041D0R0-10

Pin No.	Symbol
1	TOP
2	RIGHT
3	BOTTOM
4	LEFT



10.3 Color Data Input Assignment

The brightness of each primary color(red, green and blue) is based on the 6 bit gray scale data input for the color. The higher the binary input, the brighter the color. The table provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		D05	D04	D03	D02	D01	D00	D15	D14	D13	D12	D11	D10	D25	D24	D23	D22	D21	D20
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of RED	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

**11. Interface Timing****11.1 Input Signal Characteristics**

PARAMETER	Symbol	Min.	Typ.	Max.	Unit
HS setup time	T_{hst}	10	-	-	ns
HS hold time	T_{hhd}	10	-	-	ns
VS setup time	T_{vst}	10	-	-	ns
VS hold time	T_{vhd}	10	-	-	ns
Data setup time	T_{dsu}	10	-	-	ns
Data hold time	T_{dhd}	10	-	-	ns
DEN setup time	T_{esu}	10	-	-	ns
VS falling to HS falling time on odd field @ RGB mode	T_{HV_O}	-4	0	+4	T_{CPH}
VS falling to HS falling time on even field @ RGB mode	T_{HV_E}	0.4	0.5	0.6	T_H

PARAMETER	Symbol	Min.	Typ.	Max.	Unit
CLK frequency	F_{CPH}	-	25.175	-	MHz
CLK period	T_{CPH}	-	39.7	-	ns
CLK pulse duty	T_{CWH}	40	50	60	%
HS period	T_H	-	800	-	T_{CPH}
HS pulse width	T_{WH}	5	30	-	T_{CPH}
HS-DEN time	T_{HS}	112	144	175	T_{CPH}
DEN pulse width	T_{EP}	-	640	-	T_{CPH}
VS pulse width	T_{WV}	1	3	5	T_H
VS-DEN time	T_{STV}	-	35	-	T_H
VS period	T_V	-	525	-	T_H

Note: When SYNC mode is used, 1st data start from 144th CLK after HS falling (when $STHD[5:0]=00000$)

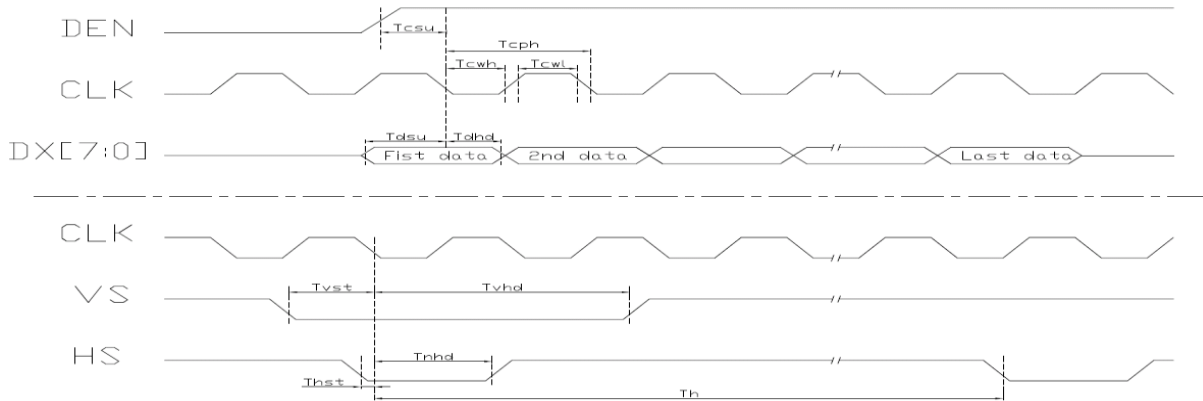
**11.2 LVDS Switching Characteristics**

Symbol	Parameter		Min.	Typ.	Max.	Unit
t _{RCP}	CLK OUT Period	VCC = 3.0 - 3.6V	11.76	T	50.0	ns
		VCC = 2.5 - 3.6V	14.28	T	50.0	ns
t _{RCH}	CLK OUT High Time			4T/7		ns
t _{RCL}	CLK OUT Low Time			3T/7		ns
t _{RCD}	RCLK +/- to CLK OUT Delay			5T/7		ns
t _{RS}	TTL Data Setup to CLK OUT		0.35T-0.3			ns
t _{RH}	TTL Data Hold from CLK OUT		0.45T-1.6			ns
t _{TLH}	TTL Low to High Transition Time			2.0	3.0	ns
t _{THL}	TTL High to Low Transition Time			1.8	3.0	ns
t _{RIP1}	Input Data Position0 (T = 11.76ns)		-0.4	0.0	0.4	ns
t _{RIP0}	Input Data Position1 (T = 11.76ns)		T/7-0.4	T/7	T/7+0.4	ns
t _{RIP6}	Input Data Position2 (T = 11.76ns)		2T/7-0.4	2T/7	2T/7+0.4	ns
t _{RIP5}	Input Data Position3 (T = 11.76ns)		3T/7-0.4	3T/7	3T/7+0.4	ns
t _{RIP4}	Input Data Position4 (T = 11.76ns)		4T/7-0.4	4T/7	4T/7+0.4	ns
t _{RIP3}	Input Data Position5 (T = 11.76ns)		5T/7-0.4	5T/7	5T/7+0.4	ns
t _{RIP2}	Input Data Position6 (T = 11.76ns)		6T/7-0.4	6T/7	6T/7+0.4	ns
t _{RPLL}	Phase Lock Loop Set				10.0	ms

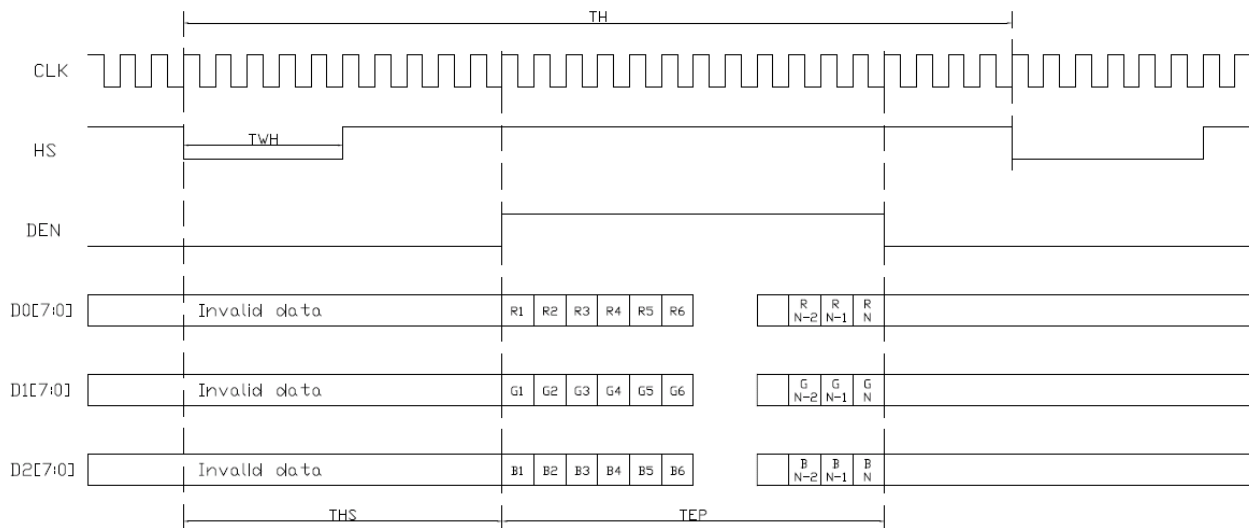


11.2 Waveform

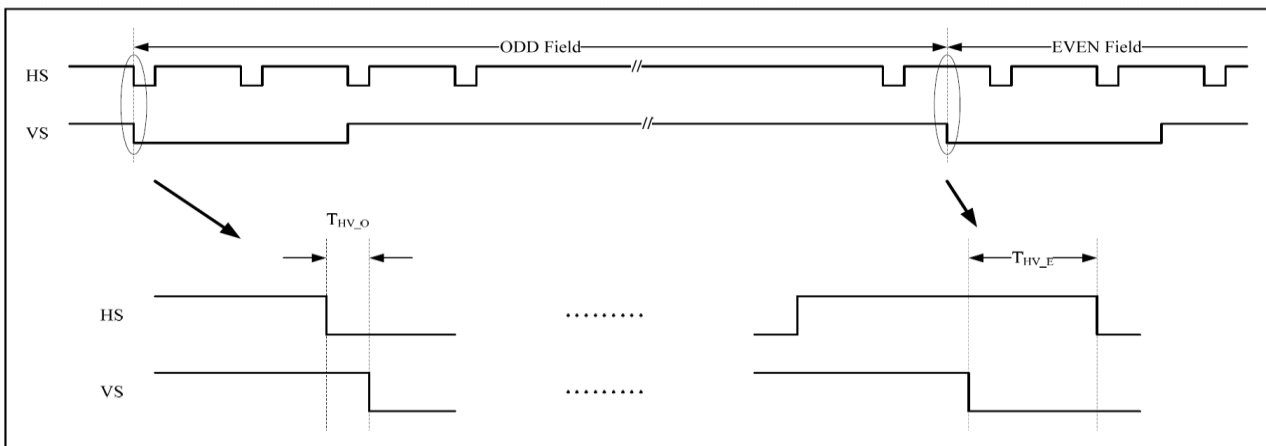
11.2.1 Clock and Data input waveforms



11.2.2 Data input format for RGB Mode

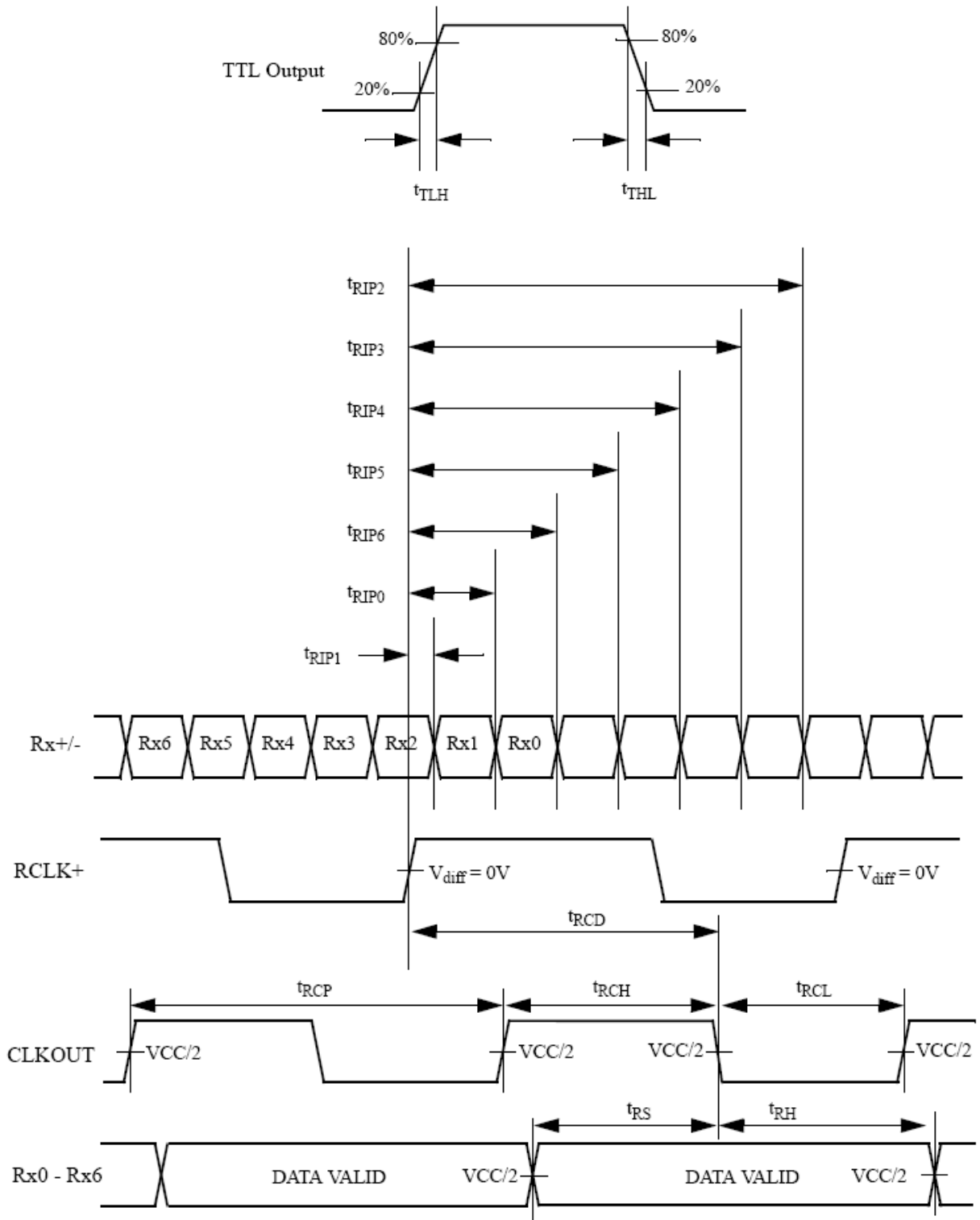


11.2.3 The HS & VS timing of the ODD/EVEN field.





11.2.4 LVDS AC Timing



Note:
 1) $V_{diff} = (RA+) - (RA-), \dots, (RCLK+) - (RCLK-)$



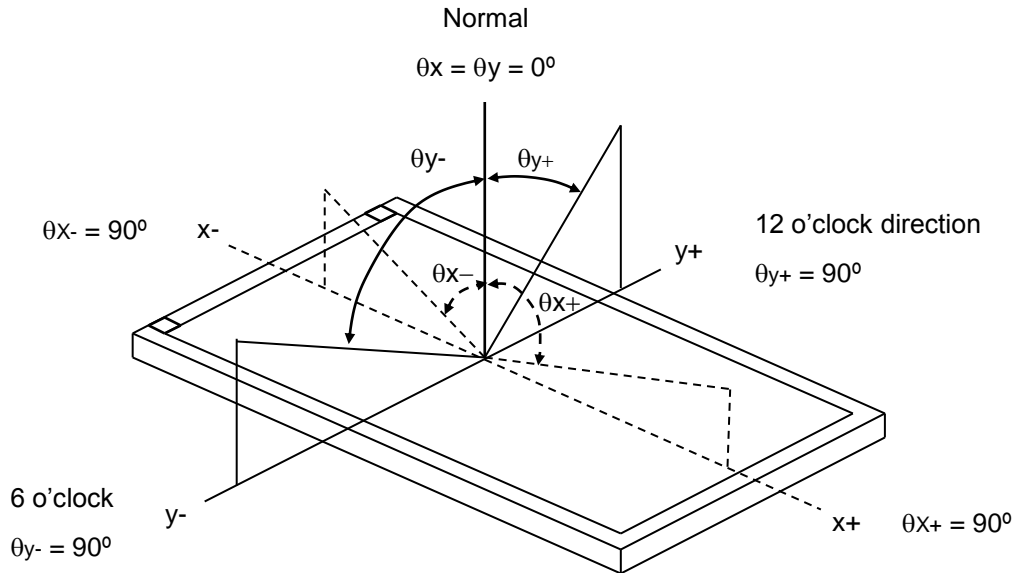
12. Optical Characteristics

The optical characteristics should be measured in a dark environment (≤ 1 lux) or equivalent state with the methods shown in Note (4).

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing Normal Angle	200	(350)	-	-	(2)
Response Time		T_R		-	15	-	ms	(3)
		T_F		-	35	-	ms	
Luminance(Center)		Y		480	(560)	-	cd/m ²	(4)
Brightness uniformity		BUNI		80	(85)	-	%	(5)
Color Chromaticity	Red	R_x		0.550	0.600	0.650	-	(1),(4)
		R_y		0.300	0.350	0.400	-	
	Green	G_x		0.270	0.320	0.370	-	
		G_y		0.500	0.550	0.600	-	
	Blue	B_x		0.090	0.140	0.190	-	
		B_y	0.100	0.150	0.200	-		
	White	W_x	0.280	0.320	0.380	-		
		W_y	0.330	0.380	0.430	-		
Viewing Angle	Horizontal	θ_{x+}	55	(65)	-	deg.		
		θ_{x-}	55	(65)	-			
	Vertical	θ_{y+}	45	(55)	-			
		θ_{y-}	55	(65)	-			



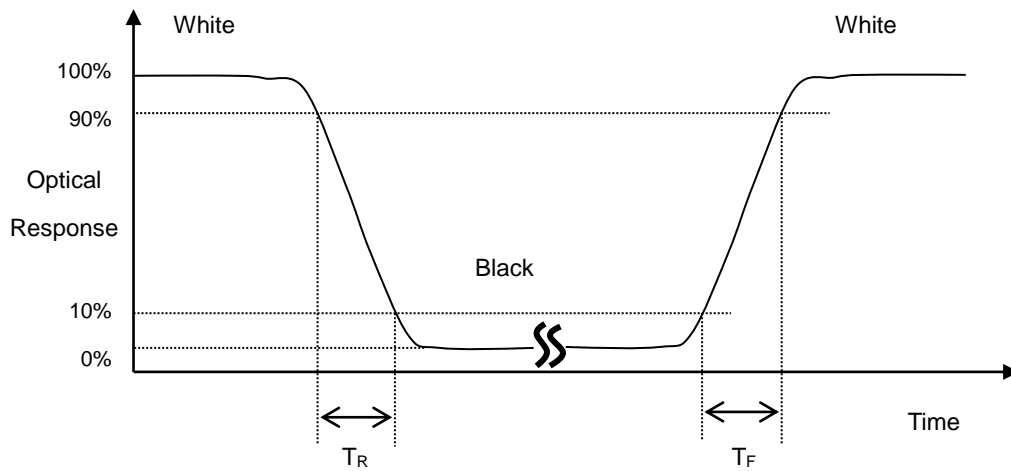
Note (1) Definition of Viewing Angle (θ_x, θ_y):



Note (2) Definition of Contrast Ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

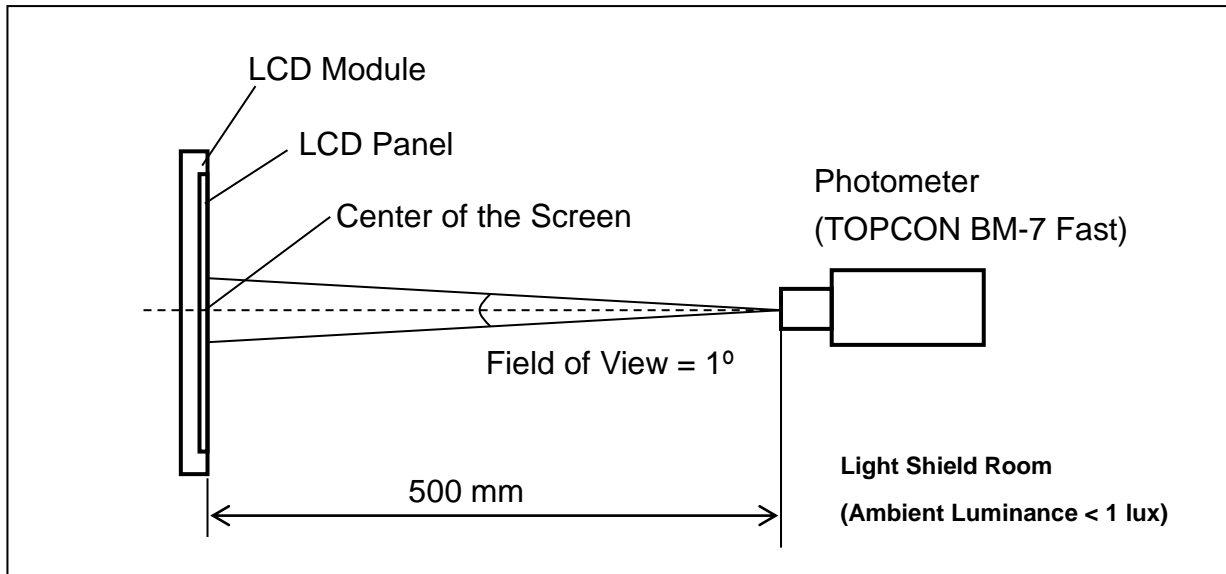
Note (3) Definition of Response Time (T_R, T_F):





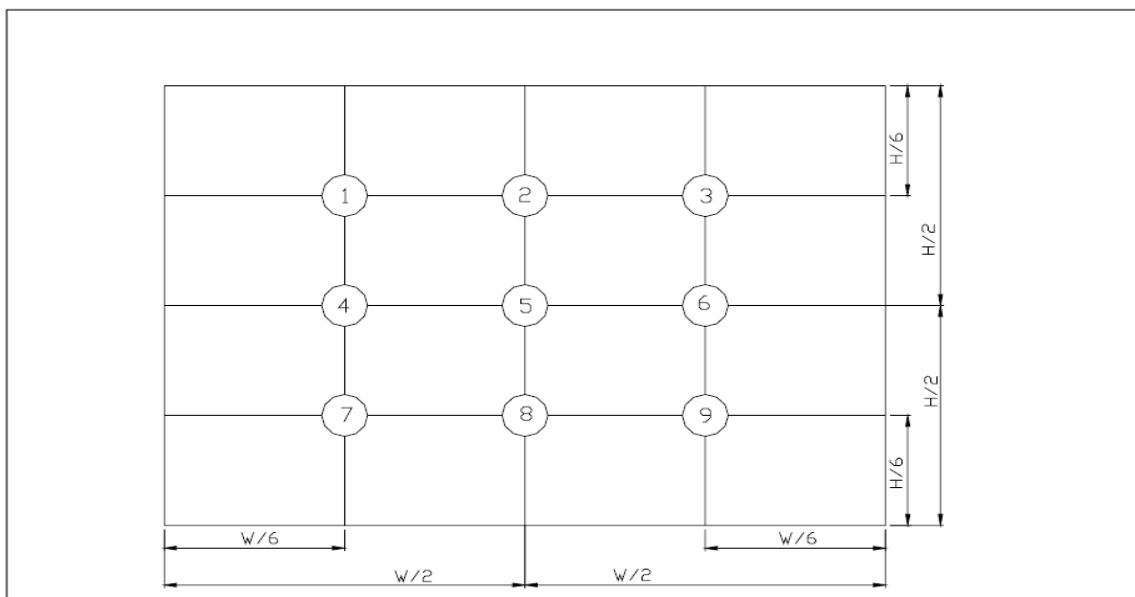
Note (4) Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a dark room or equivalent condition.



Note (5) Definition of brightness uniformity

Brightness uniformity=(Min Luminance of 9 points)/(Max Luminance of 9 points)×100%



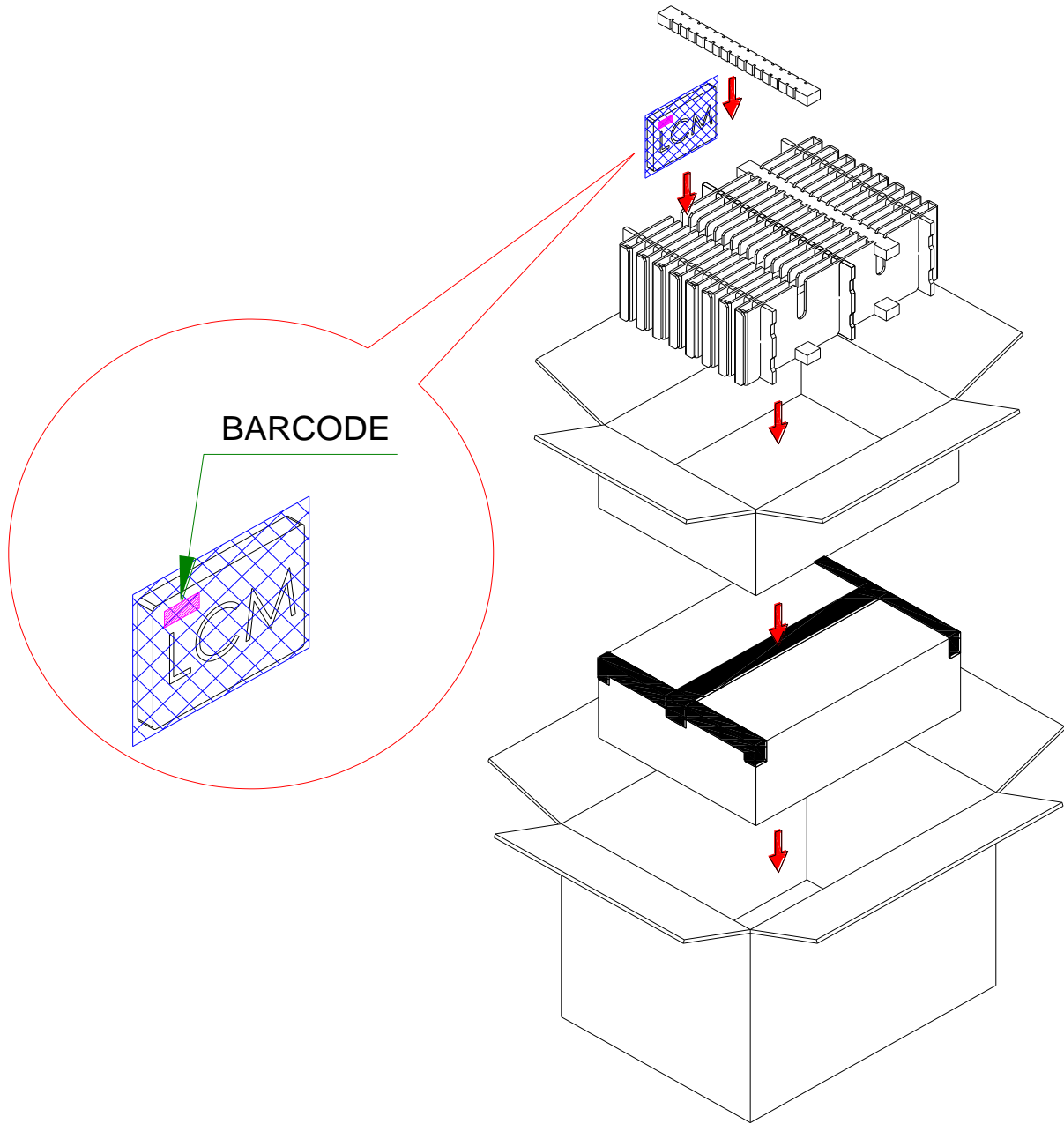
(單位 : mm)

**13. Reliability Test**

No.	Test Items	Test Condition	Remark
1	High Temperature Storage Test	T _a = 80°C 240 hours	-
2	Low Temperature Storage Test	T _a = -30°C 240 hours	-
3	High Temperature Operation Test	T _a = 70°C 240 hours	-
4	Low Temperature Operation Test	T _a = -20°C 240 hours	-
5	High Temperature and High Humidity Operation Test	T _a =60°C 90%RH 240 hours	-
6	Electro Static Discharge Test (non-operating)	-Panel Surface/Top Case : 150pF, 330Ω Air: ±15kV, Contact: ±8kV	-
7	Mechanical Shock Test (non-operating)	Half sine wave, 100G, 6ms 3 times shock of each six surfaces	-
8	Vibration Test (non-operating)	Sine wave, 10 ~ 55 ~ 10Hz, 3 axis, 2 hours/axis	-
9	Thermal Shock Test (non-operating)	-20°C(30min) ~ 70°C(30min),100 cycles	-
10	Drop Test(with Carton) (non-operating)	Height: 80cm 1 corner, 3 edges, 6 surfaces	-



14. Packaging



PARTS LIST					
	ITEM	SIZE(LxWxH) unit:mm	MATERIAL	Q.T.Y	NOTE
1	STATIC SHIEDING BAGS	300.0x145.0x0.09		60	
2	EPE PAD	345.0x30.0x20.0	EPE	8	
3	CARD BOARD	345.0x150.0x3.5	CARTON	6	
4	CARD BOARD	450.0x23.0x150.0	CARTON	16	
5	INTERNAL BOX	455.0x350.0x164.0	CARTON	2	
6	EXTERNAL BOX	475.0x370.0x375.0	CARTON	1	
7	PRODUCT	166.6x109.4x10.0		60	



15. Precautions

15.1 Assembly and Handling Precautions

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) It's recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) Don't apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.
- (4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
- (5) Do not plug in or pull out the I/F connector while the module is in operation.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) Moisture can easily penetrate into LCD module and may cause the damage during operation.
- (9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD module in the specified storage conditions.
- (10) When ambient temperature is lower than 10°C, the display quality might be reduced. For example, the response time will become slow.

15.2 Safety Precautions

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the module's end of life, it is not harmful in case of normal operation and storage.

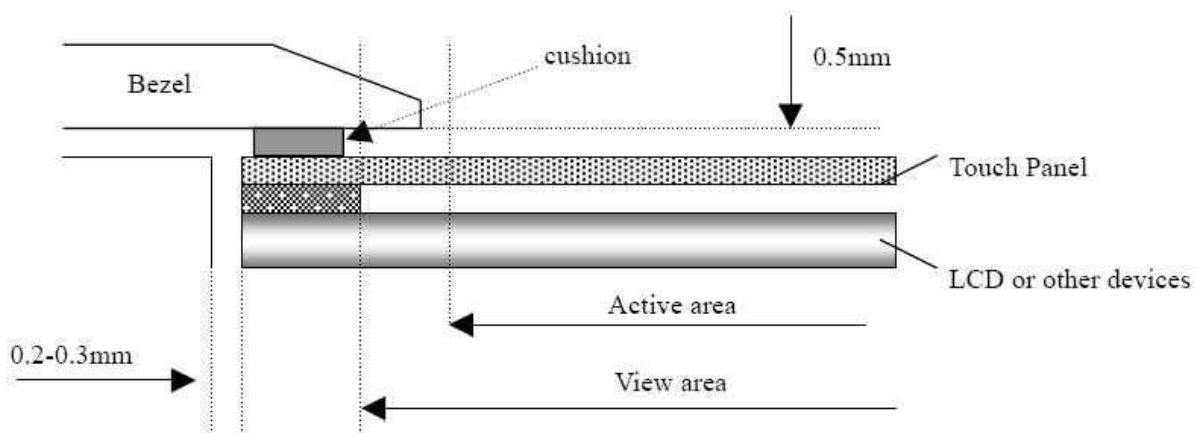
15.3 Terms of Warrant

- (1) Acceptance inspection period
The period is within one month after the arrival of contracted commodity at the buyer's factory site.
- (2) Applicable warrant period
The period is within twelve months since the date of shipping out under normal using and storage conditions.



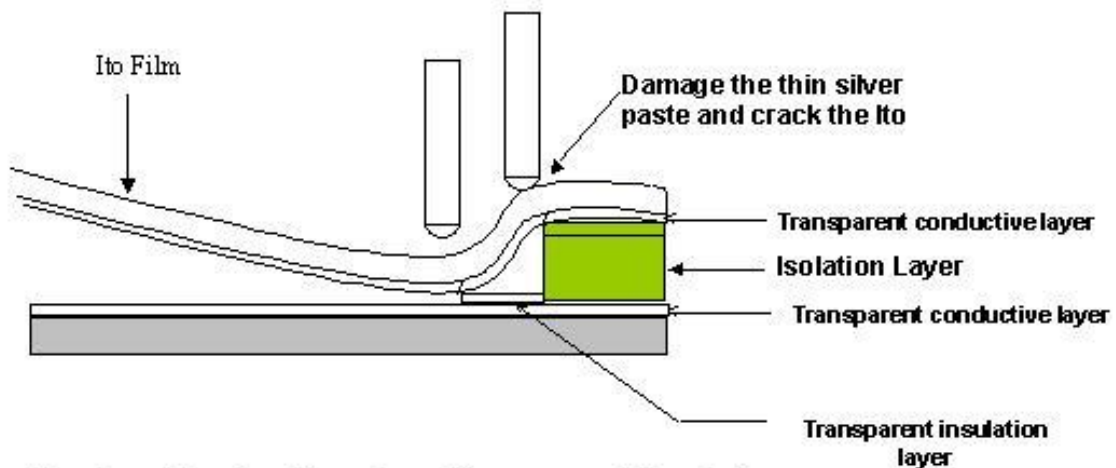
15.4 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area.
The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode.
It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.



15.5 Operation Prohibit

Not Suggested Pen Input Position On Touch Panel



Pen input load on the edge of transparent insulation area might damage the ITO of ITO Pet-Film and reduce the durability of touch panel



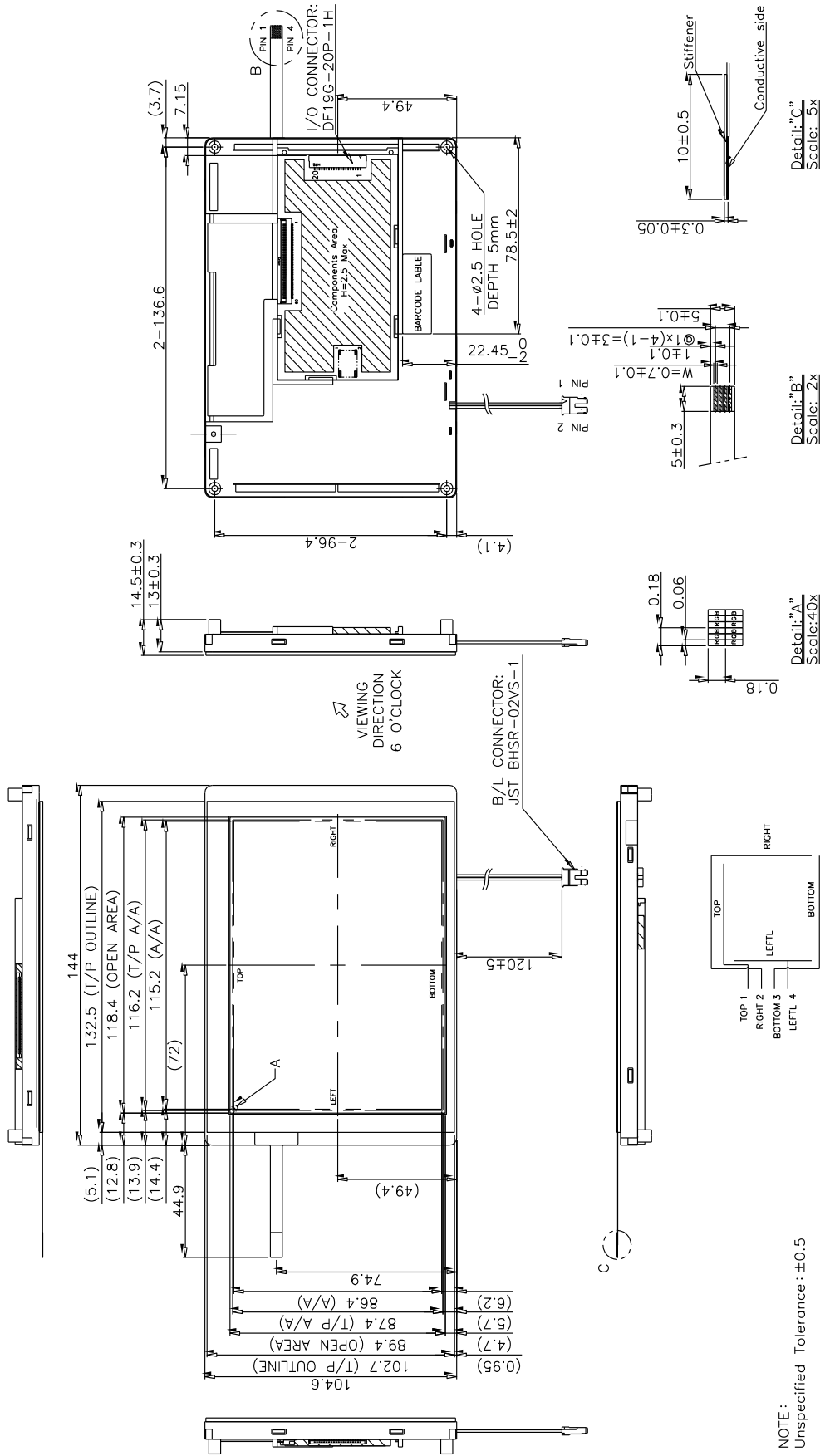
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PT644857E-TLMWD-EMR04

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16.Outline Drawing



NOTE: Unspecified Tolerance: ±0.5



17. Definition of Labels

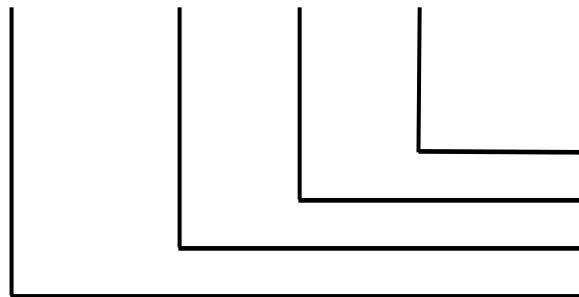
The bar code nameplate is pasted on each module as illustration, and its definitions are as following explanation.



(a) Module Name : PT644857E-TLMWD-EMR04

(b) Serial ID :

A B C D E F G H I J K L



Serial No.
Factory Code
Manufactured Date
Screen Size

Serial ID includes the information as below:

(a) Screen size (Diagonal): Inch Code (ABCD)

3.5" → 0350

10.4" → 1040

(b) Manufactured Date: Year, Month, Day (EFG)

Year (E)

Year	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009
Mark	0	1	2	3	4	5	6	7	8	9
Year	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019
Mark	A	B	C	D	E	F	G	H	I	J



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Month (F)

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

Day (G)

Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Mark	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
Mark	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	

(c) Factory Code (H):

For P-TEC internal use.

(d) Serial No. (IJKL):

Manufacturing sequence of product, for example: 0001~9999.

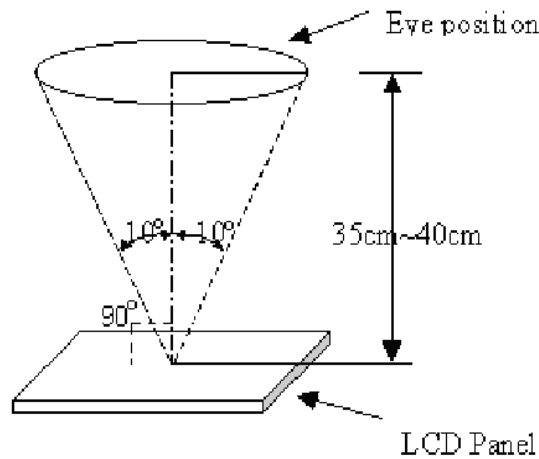


18. Incoming Inspection Standards

18.1 The environmental condition of inspection

The environmental condition and visual inspection shall be conducted as below.

- (1) Ambient temperature $25 \pm 5^{\circ}\text{C}$
- (2) Humidity: $60 \pm 5\% \text{ RH}$
- (3) Viewing distance is approximately 35 ~ 40 cm
- (4) Viewing angle is normal to the LCD panel as Fig_1(10°)
- (5) Ambient Illumination: 300 ~ 500 Lux for external appearance inspection



Fig_1

18.2 The defects classify of AQL as following:

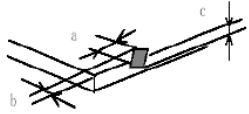
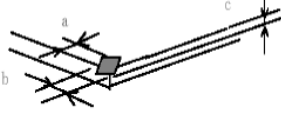

Class of defects	AQL	Definition
Major	0.65%	It is defect that is likely to result in failure or to reduce materially the usability of the intended function.
Minor	1.5%	It is a defect that will not result in functioning problem with deviation classified.



18.3 Inspection Parameters

Item		Specification/Description			Note	
Display	Function	No Display			-	
		Malfunction			-	
Operating	Contrast ratio	Out of Spec			-	
	Line defect	No obvious Vertical and Horizontal line defect in bright , dark and colored.			-	
	Point Defect (red,green,blue,dark , white)	Item	Acceptable number			Note: 1、4、 5、6
			A	B	Total	
		BRIGHT DOT	$N \leq 2$	$N \leq 2$	$N \leq 7$	
		DARK DOT	$N \leq 3$	$N \leq 4$		
		TOTAL DOT	$N \leq 4$	$N \leq 5$		
TWO ADJACENT DOT	NOT ALLOWED					
THREE OR MORE ADJACENT DOT	NOT ALLOWED					
External Inspection (non-operating)	Scratch on the polarizer	L(mm)	W(mm)	Acceptable number	Note:2	
		$L \leq 2.5$	$W \leq 0.1$	4		
		$L > 2.5$	$W > 0.1$	0		
	Dent or bubble on the polarizer	Dimension(mm)		Acceptable number		Note:3
		$D \leq 0.5$		4		
		$D \leq 0.15$		Disregard		
	Foreign material on the polarizer	Dimension(mm)		Acceptable number		Note:3
		$D \leq 0.5$		4		
		$D \leq 0.15$		Disregard		

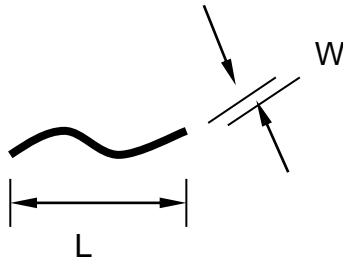


Item		Specification/Description			Note
Touch Panel	Scratch	L(mm)	W(mm)	Acceptable number	Note:2
		$L \leq 10$	$W < 0.05$	Disregard	
			$0.05 \leq W < 0.1$	$N \leq 4$	
			$W \geq 0.1$	0	
	Foreign Materials (Linear shape)	$L \leq 10$	$W < 0.05$	Disregard	Note:2
			$0.05 \leq W < 0.1$	$N \leq 3$	
			$W \geq 0.1$	0	
	Foreign Materials (Circular shape)	Dimension(mm)		Acceptable number	Note:3
		$D \leq 0.25$		Disregard	
		$0.25 < D \leq 0.5$		$N \leq 6$	
$D > 0.5$		0			
Glass chipping			$a \leq 5\text{mm}$ $b \leq 3\text{mm}$ $c \leq t$ (t : Glass think)	Note:7	
			$a \leq 3\text{mm}$ $b \leq 3\text{mm}$ $c \leq t$ (t : Glass think)	Note:7	
Newton-ring	(In case of doubtful situations) Observe on 60° from the product surface under a white Fluorescent lamp(3-wavelength lamp).	Average diameter $\leq 1/3$ Touch Panel area Disregard.		Note:7	
Membrane Drum		$H \leq 0.35\text{mm}$			

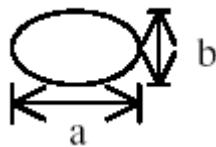


Note1. The definition of dot defect : The dot defect was judged after repair and the size of adefective dot over 1/2 of whole dot is regarded as one defective dot.

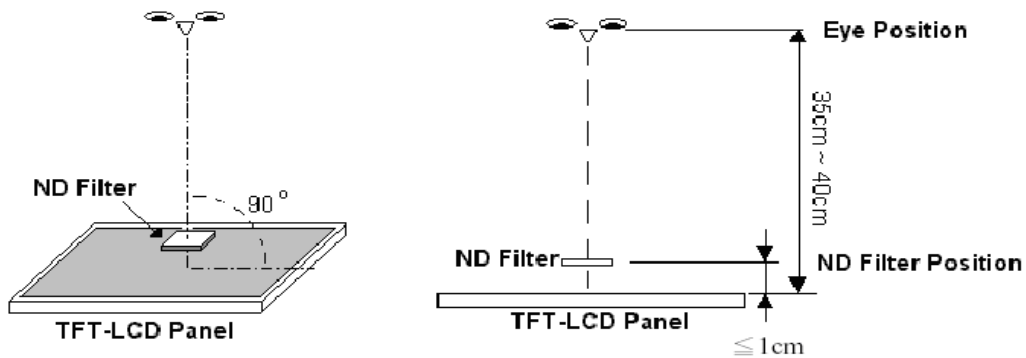
Note2.



Note3. D : Diameter $D=(a+b)/2$



Note4. Bright dot is defined through 6% transmission ND Filter as following.

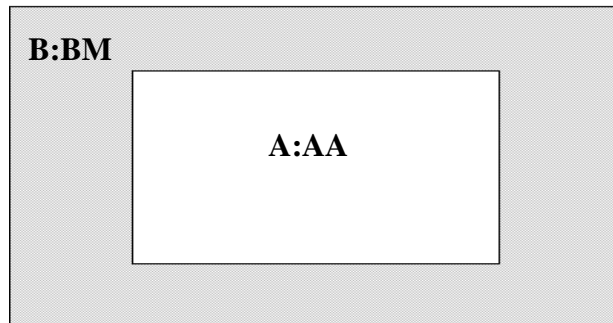


Note5. ADJACENT DOT

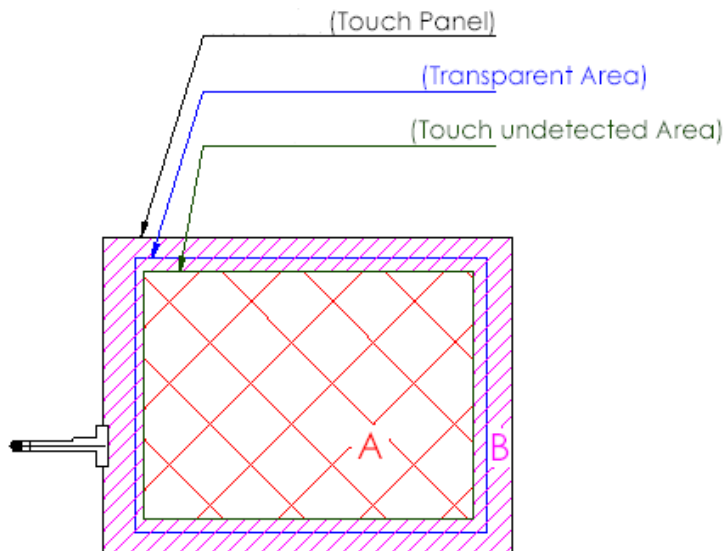




Note6.



Note7.



A area : Without any defect point effect on normal operation.

B area : None-specify

18.4 Handling of LCM

- (1) Don't give external shock.
- (2) Don't apply excessive force on the surface.
- (3) Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't disassemble the LCM.