

## **KBP4005 THRU KBP410**

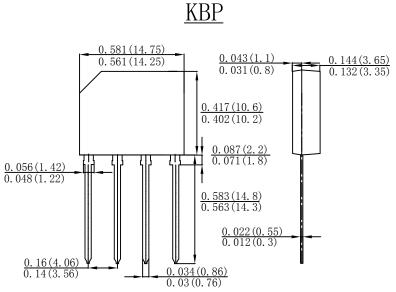
#### SINGLE PHASE 4.0AMP GLASS PASSIVATED BRIDGE RECTIFIER

#### **Features**

- · Glass passivated die construction
- · Low forward voltage drop
- High current capability
- · High surge current capability
- Plastic material-UL flammability 94V-0

#### **Mechanical Data**

- · Case: KBP, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- · Polarity: as marked on case
- Mounting position: Any
- Marking: type number
- · Lead Free: For RoHS / Lead Free Version



Dimensions in inches and (millimeters)

#### **Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified. Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	KBP 4005	KBP 401	KBP 402	KBP 404	KBP 406	KBP 408	KBP 410	UNITS
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM	50	100	200	400	600	800	1000	V
	VRWM								
	VDC								
RMS Reverse Voltage	VRMS	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @Tc=50 ℃	<b>I</b> F(AV)	4.0							А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	80							А
I <sup>2</sup> t Rating for Fusing (t < 8.3ms)	l <sup>2</sup> t	26.56						A <sup>2</sup> s	
Forward Voltage per element @IF=4.0A	VFM	1.1							V
Peak Reverse Current @T <sub>A</sub> =25℃ At Rated DC Blocking Voltage @T <sub>A</sub> =125℃	<b>I</b> R	5.0 500							uA
Typical Thermal Resistance per leg (Note 2)	Reja	40							°C/W
	Rejl	20							
Operating and Storage Temperature Range	TJ,Tstg	-55to+150							$^{\circ}$

Note:1. Mounted on glass epoxy PC board with 1.3mm<sup>2</sup> solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C..

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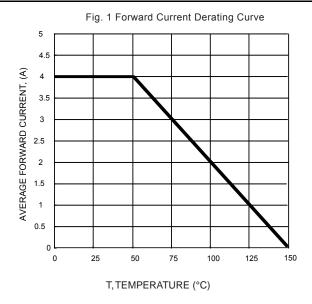


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

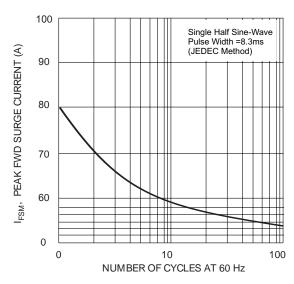


Fig. 5 T ypical Reverse Characteristics (per element)

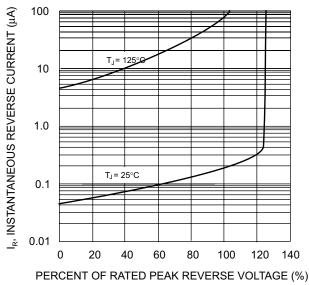


Fig. 2 Typical Fwd Characteristics

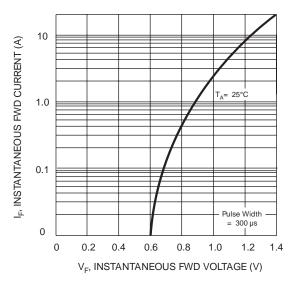
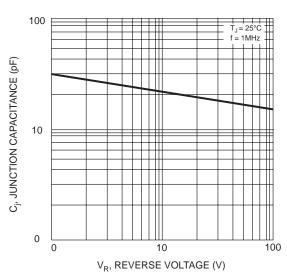


Fig. 4 Typical Junction Capacitance



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