



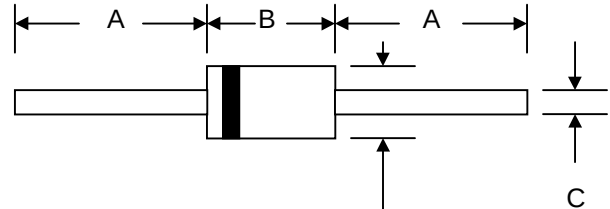
SF30AG – SF30JG



3.0A SUPERFAST GLASS PASSVITED RECTIFIER

Features

- Glass Passivated Die Construction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability



DO-201AD		
Dim	Min	Max
A	24.5	—
B	7.20	9.50
C	1.10	1.30
D	5.00	5.60
All Dimensions in mm		

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version**

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	SF30AG	SF30BG	SF30CG	SF30DG	SF30EG	SF30GG	SF30JG	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	150	200	300	400	600	V
RMS Reverse Voltage	V _{R(RMS)}	35	70	105	140	210	280	420	V
Average Rectified Output Current @T _L = 120°C	I _O	3.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	125							A
Forward Voltage									

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Measured with I_F = 0.5A, I_R = 1.0A, IRR = 0.25A. See figure 5.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

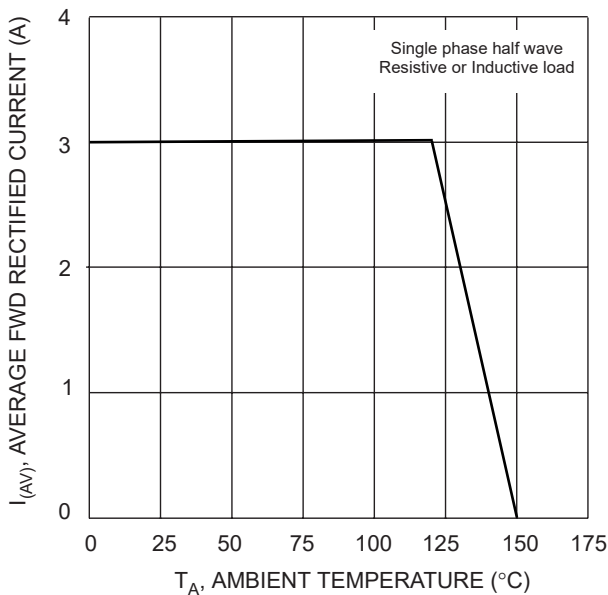


Fig. 1 Forward Current Derating Curve

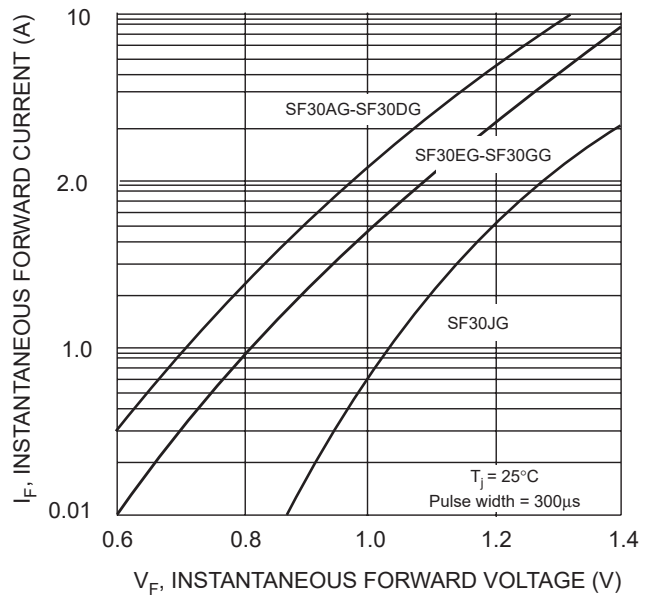


Fig. 2 Typical Forward Characteristics

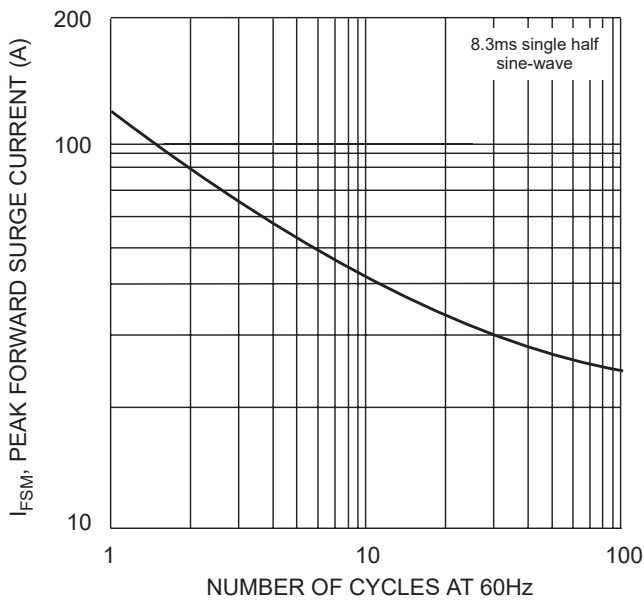


Fig. 3 Peak Forward Surge Current

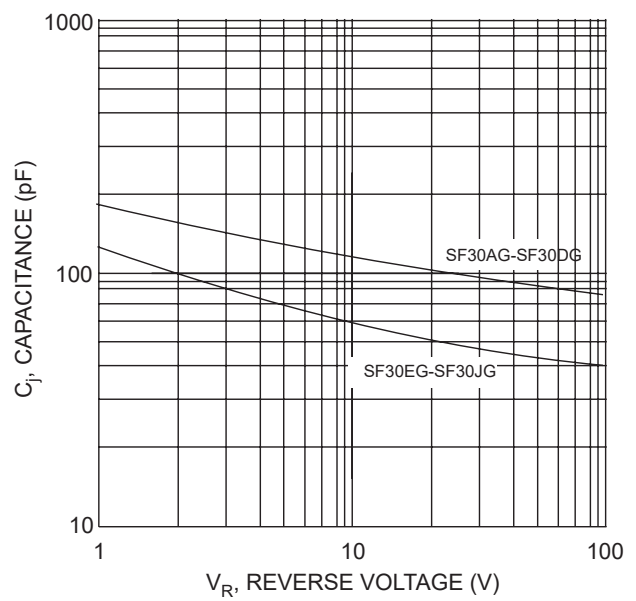
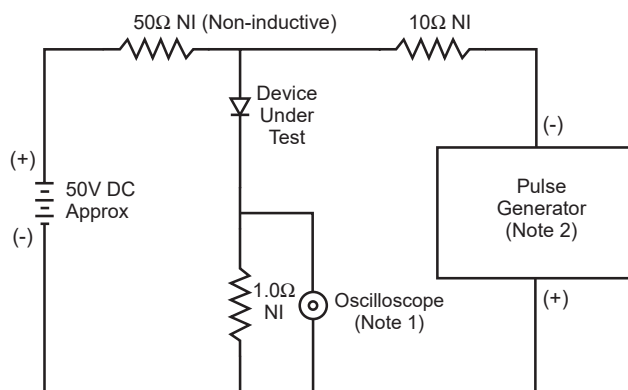
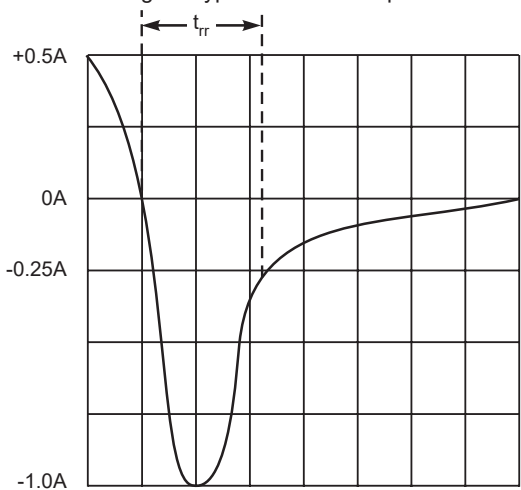


Fig. 4 Typical Junction Capacitance



Notes:

1. Rise Time = 7.0ns max. Input Impedance = 1.0M Ω , 22pF.
2. Rise Time = 10ns max. Input Impedance = 50 Ω .



Set time base for 5/10ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit