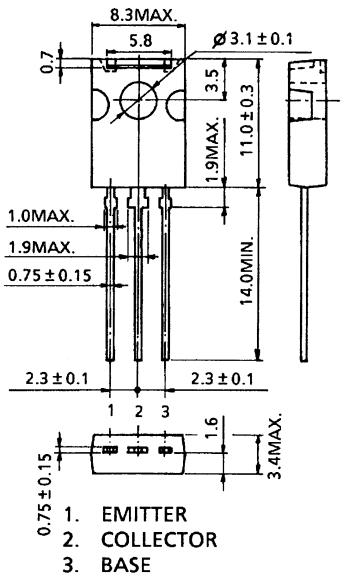
TOSHIBA Transistor Silicon PNP Triple Diffused Type

2SA1924

High-Voltage Switching Applications

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	2-8H1A

Weight: 0.82 g (typ.)

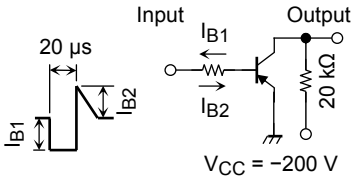
- High breakdown voltage: V_{CEO} = −400 V
- Low saturation voltage: $V_{CE\ (sat)}$ = −1 V (max)
  (I_C = −100 mA, I_B = −10 mA)
- Collector metal (fin) is fully covered with mold resin.

Absolute Maximum Ratings (Tc = 25°C)

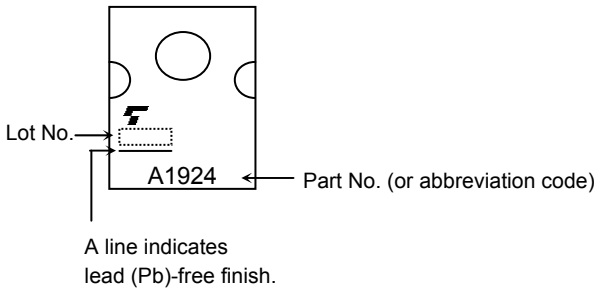
Characteristics		Symbol	Rating	Unit
Collector-base voltage		V_{CBO}	−400	V
Collector-emitter voltage		V_{CEO}	−400	V
Emitter-base voltage		V_{EBO}	−7	V
Collector current	DC	I_C	−0.5	A
	Pulse	I_{CP}	−1	
Base current		I_B	−0.25	A
Collector power dissipation	Ta = 25°C	P_C	1.5	W
	Tc = 25°C		10	
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

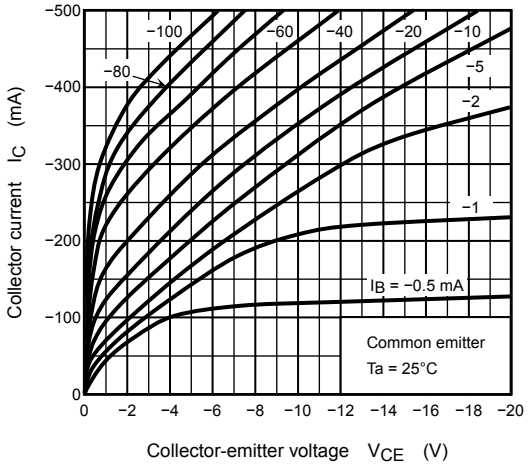
Electrical Characteristics (Tc = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	V_{CB} = −400 V, I_E = 0	—	—	−10	μA
Emitter cut-off current		I_{EBO}	V_{EB} = −7 V, I_C = 0	—	—	−1	μA
Collector-emitter breakdown voltage		V_{CEO}	I_C = −10 mA, I_B = 0	−400	—	—	V
DC current gain		$h_{FE\ (1)}$	V_{CE} = −5 V, I_C = −20 mA	140	—	450	
		$h_{FE\ (2)}$	V_{CE} = −5 V, I_C = −100 mA	140	—	400	
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	I_C = −100 mA, I_B = −10 mA	—	−0.4	−1.0	V
Base-emitter saturation voltage		$V_{BE\ (sat)}$	I_C = −100 mA, I_B = −10 mA	—	−0.76	−0.9	V
Transition frequency		f_T	V_{CE} = −5 V, I_C = −50 mA	—	35	—	MHz
Collector output capacitance		C_{ob}	V_{CB} = −10 V, I_E = 0, f = 1 MHz	—	18	—	pF
Switching time	Turn-on time	t_{on}		—	0.2	—	μs
	Storage time	t_{stg}		—	2.3	—	
	Fall time	t_f	I_{B1} = −10 mA, I_{B2} = 20 mA, duty cycle ≤ 1%	—	0.2	—	

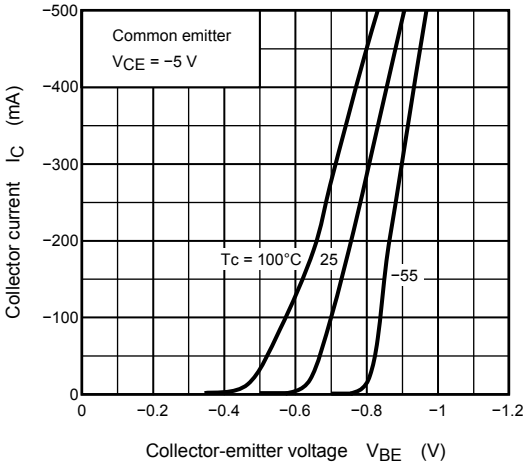
Marking



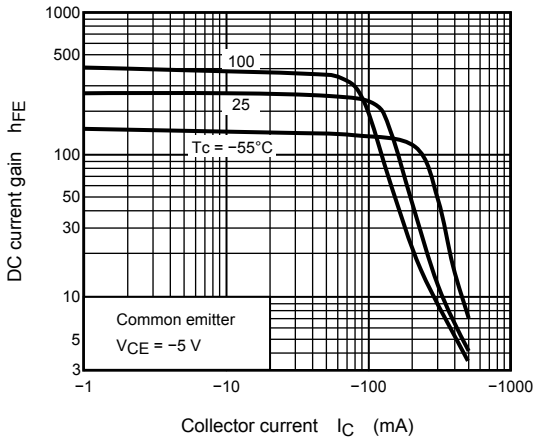
$I_C - V_{CE}$



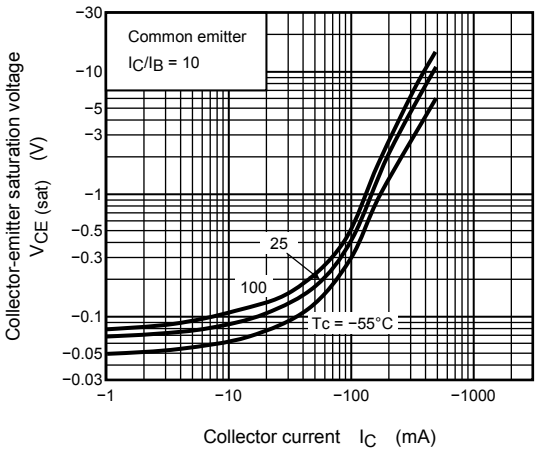
$I_C - V_{BE}$



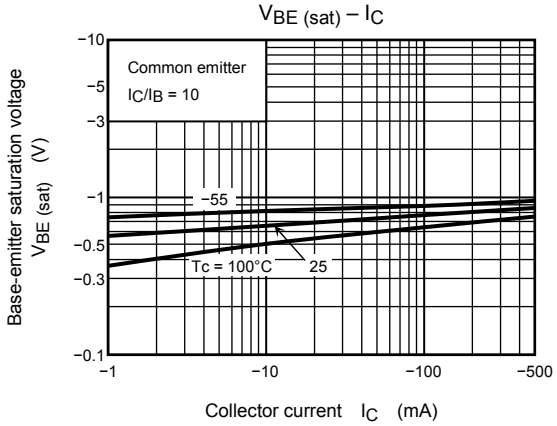
$h_{FE} - I_C$



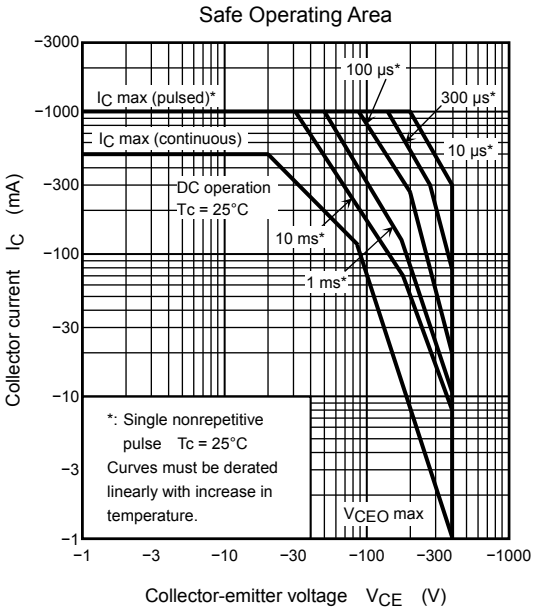
$V_{CE\ (sat)} - I_C$



$V_{BE\ (sat)} - I_C$



Safe Operating Area



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20070701-EN GENERAL

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