Advanced Semiconductor Business Inc.

plerow™ ALN1100T2

Internally Matched LNA Module

Features

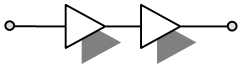
- S_{21} = 26 dB @ 950 MHz
  = 24 dB @ 1250 MHz
- NF of 0.85 dB over frequency
- Unconditionally Stable
- Single 5V Supply
- High OIP3 @ Low Current

Description

The plerow™ ALN-series is the compactly designed surface-mount module for the use of the LNA with or without the following gain blocks in the infrastructure equipment of the mobile wireless (CDMA, GSM, PCS, PHS, WCDMA, DMB, WLAN, WiBro, WiMAX), GPS, satellite communication terminals, CATV and so on. It has an exceptional performance of low noise figure, high gain, high OIP3, and low bias current. The stability factor is always kept more than unity over the application band in order to ensure its unconditionally stable implementation to the application system environment. The surface-mount module package including the completed matching circuit and other components necessary just in case allows very simple and convenient implementation onto the system board in mass production level.

RoHS-compliant





2-stage Single Type

Specifications (in Production)

Typ. @ T = 25°C, V_s = 5 V, Freq. =1100 MHz, $Z_{o.sys}$ = 50 ohm

Parameter	Unit	Specifications		
		Min	Typ	Max
Frequency Range	MHz	950		1250
Gain	dB	24	25	
Gain Flatness	dB		± 1.0	± 1.2
Noise Figure	dB		0.85	0.90
Output IP3 (1)	dBm	17	18	
S11 / S22 (2)	dB			-18 / -10
Output P1dB	dBm	11	12	
Switching Time (3)	μsec		-	
Supply Current	mA		20	25
Supply Voltage	V	5		
Impedance	Ω	50		
Max. RF Input Power	dBm	C.W 23 ~ 25 (before fail)		
Package Type & Size	mm	Surface Mount Type, 10Wx10Lx3.8H		

Operating temperature is -40°C to +85°C.
1) OIP3 is measured with two tones at an output power of -15 dBm / tone separated by 1 MHz.
2) S11/S22 (max) is the worst value within the frequency band.
3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_S.

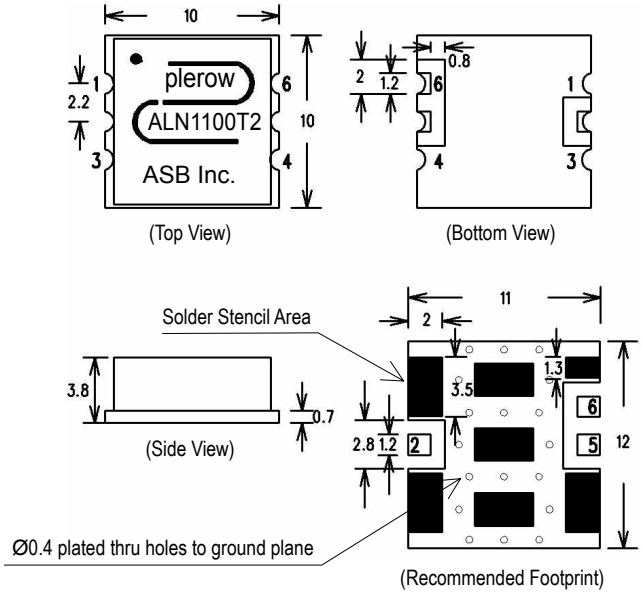
More Information

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Outline Drawing (Unit: mm)



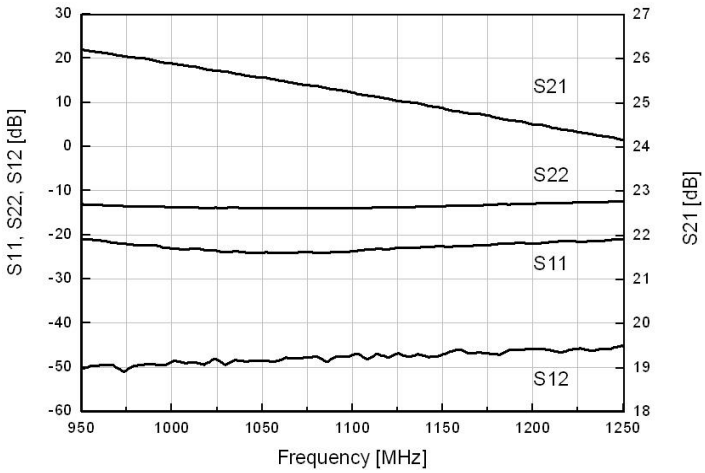
Pin Number	Function
2	RF In
4	+Vcc
6	RF Out
Others	Ground

Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.
2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

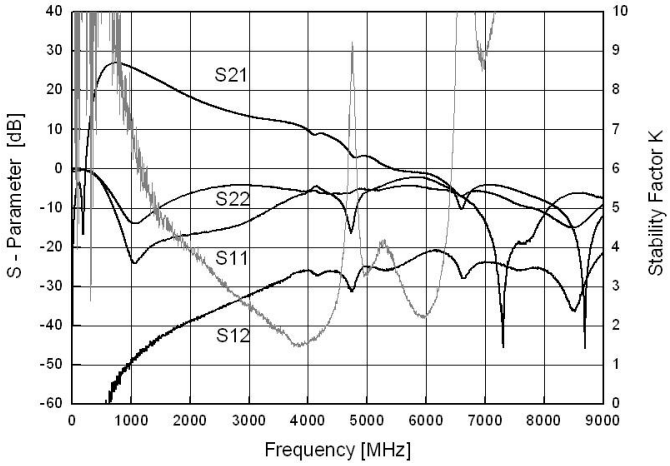
Typical Performance (Measured)

	950~1250
	+5 V

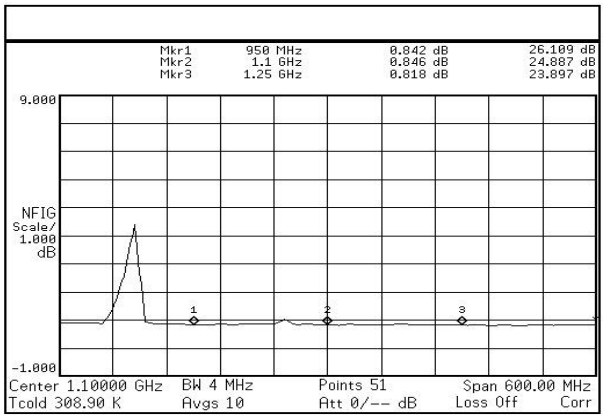
S-parameters



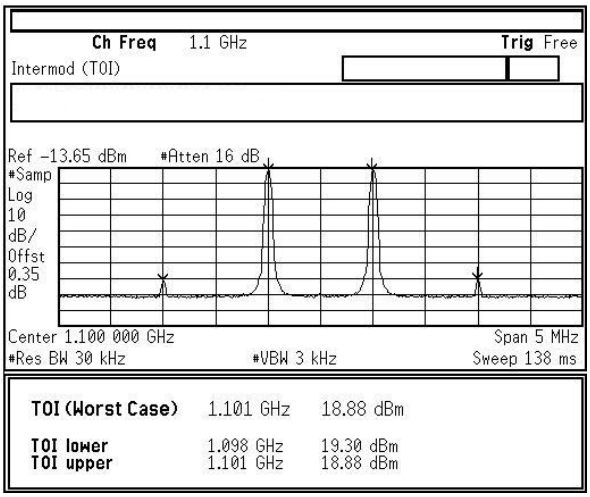
S-parameters & K Factor



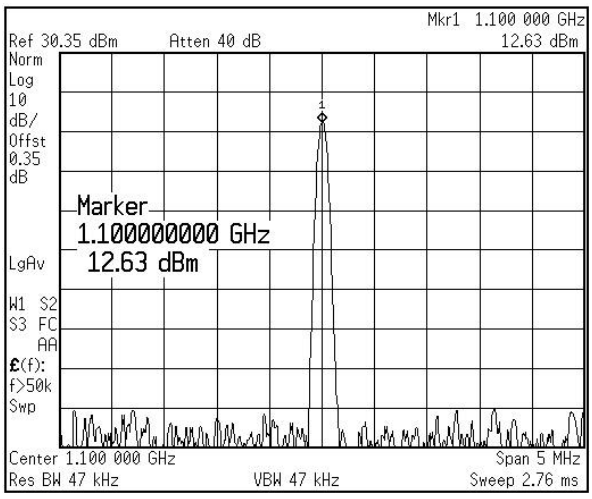
Noise Figure



OIP3



P1dB



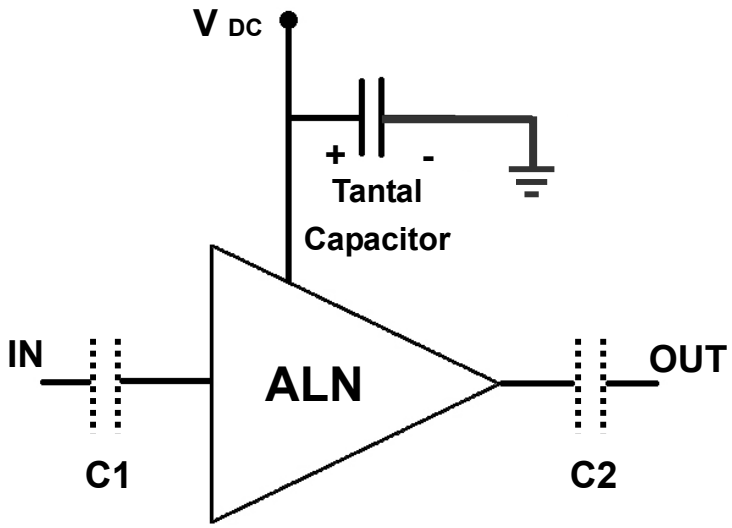
RF Performance with Voltage Change

Item / Voltage	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	NF (dB)	P1dB (dBm)	OIP3 (dBm)	Current (mA)
4.5	-17.21	-11.12	22.79	1.97	0.926	12.66	13.58	13
4.6	-17.80	-11.46	23.50	1.98	0.914	12.65	14.73	14
4.7	-18.44	-11.48	24.12	1.99	0.896	12.63	16.04	15
4.8	-19.08	-11.29	24.65	2.01	0.879	12.42	16.97	17
4.9	-19.75	-10.98	25.12	**2.03**	0.855	12.32	18.55	19
5.0	**-20.49**	**-10.59**	**25.52**	**2.05**	**0.839**	**12.15**	**19.61**	**20**
5.1	-21.27	-10.22	25.88	2.07	0.831	12.15	20.72	22
5.2	-21.94	-9.86	26.18	2.09	0.817	12.67	21.81	24
5.3	-21.97	-9.53	26.45	2.11	0.814	13.12	22.75	27
5.4	-22.11	-9.23	26.69	2.13	0.794	13.62	23.63	29
5.5	-22.12	-9.68	26.90	2.12	0.785	14.27	24.72	32

RF Performance with Operating Temperature

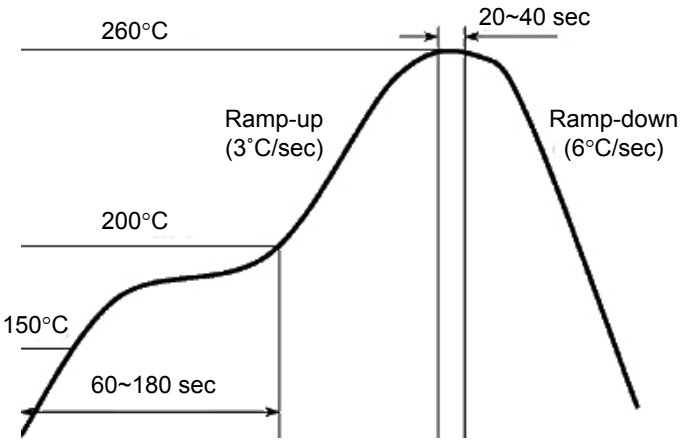
Item / Temp.	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	NF (dB)	P1dB (dBm)	OIP3 (dBm)	Current (mA)
- 40°C	-20.57	-10.50	25.43	2.0	0.607	13.85	15.26	17
- 20°C	-22.55	-10.47	25.42	2.1	0.664	13.52	26.23	18
0°C	-21.42	-10.51	25.59	2.04	0.742	12.89	17.41	19
25°C	**-20.58**	**-10.44**	**25.70**	**2.04**	**0.845**	**12.13**	**18.88**	**21**
40°C	-19.92	-10.41	25.68	2.04	0.913	12.05	19.40	22
60°C	-19.36	-10.44	25.61	2.04	0.956	11.95	20.37	24
80°C	-18.82	-10.51	25.57	2.05	1.070	11.93	21.35	25
100°C	-18.38	-10.55	25.52	2.05	1.175	11.98	22.02	27

Application Circuit

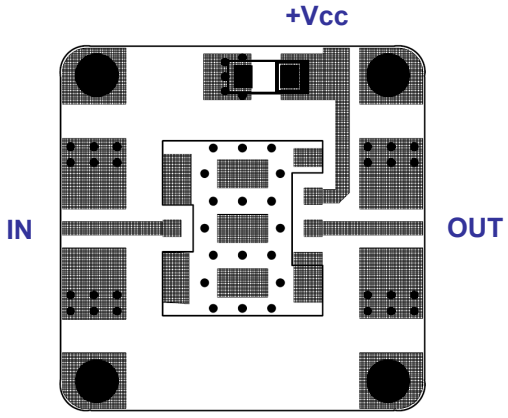


1) The tantal capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status.
2) So-called DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the LNA module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



Evaluation Board Layout



Size 25 x 25mm
(for ALN-AT, BT, T Series – 10x10mm)