Technical Specification
PQ40150QGA07

18-75V Input	15.0V Output	6.67Amp Current	2000Vdc Isolation	Quarter-brick DC/DC Converter

The PQ40150QGA07 PowerQor® Giga quarter-brick converter is a next-generation, board-mountable, isolated, fixed switching frequency DC/DC converter that uses synchronous rectification to achieve extremely high conversion efficiency. The power dissipated by the converter is so low that a heatsink is not required, which saves cost, weight, height, and application effort. All of the power and control components are mounted to the multi-layer PCB substrate with high-yield surface mount technology for greater reliability. The PQ40 module offers a wide input voltage range to cover both 24V and 48V applications. RoHS compliant (see page 13).



PQ40150QGA07 Module

Operational Features

- High efficiency, 90% at full rated load current
- Delivers up to 6.67 amps of output current (100W) with minimal derating - no heatsink required
- Ultra wide input voltage range: 18V – 75V, with 100V 100ms input voltage transient capability
- Fixed frequency switching provides predictable EMI performance
- No minimum load requirement means no preload resistors required

Mechanical Features

- Industry standard quarter-brick pin-out configuration
- Industry standard size: 1.45" x 2.3" (36.8x58.4mm)
- Total height only 0.43" (10.9mm), permits better airflow and smaller card pitch
- Total weight: 1.2 oz. (34 grams)
- Flanged pins designed to permit surface mount soldering (avoid wave solder) using FPiP technique

Control Features

- On/Off control referenced to input side (positive and negative logic options are available)
- Remote sense for the output voltage compensates for output distribution drops
- Output voltage trim permits custom voltages and voltage margining

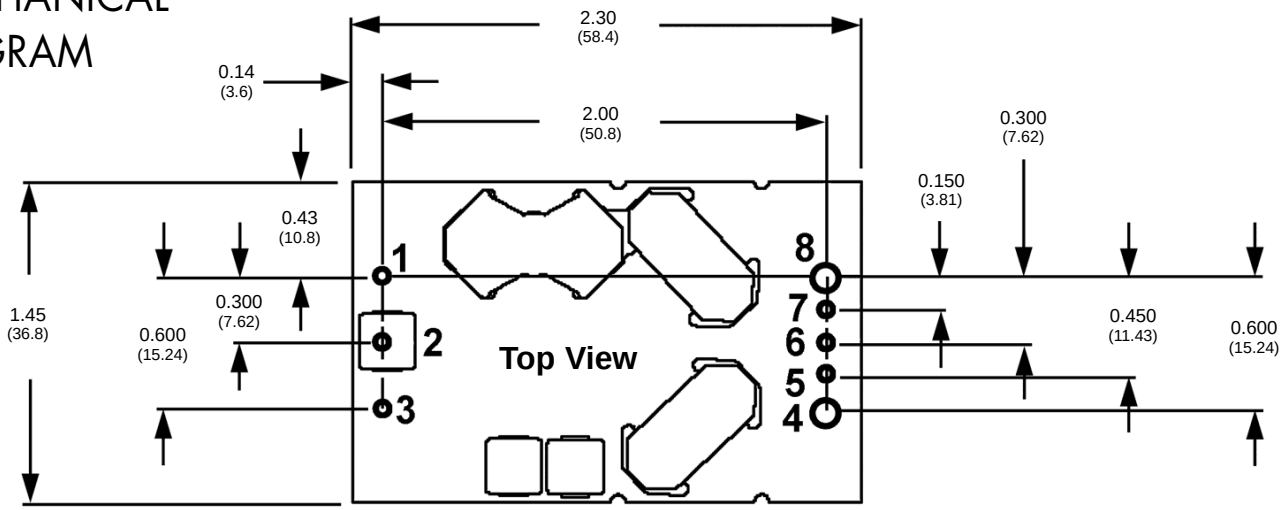
Protection Features

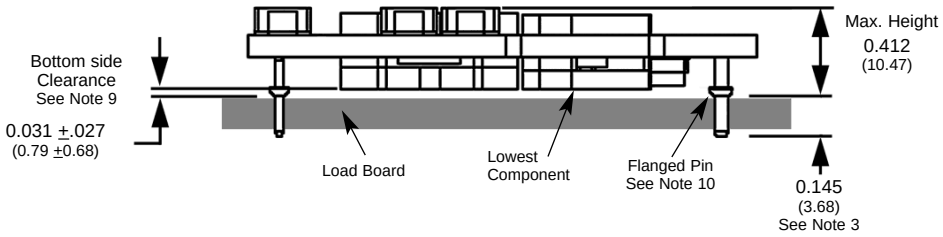
- Input under-voltage lockout disables converter at low input voltage conditions
- Output current limit and short circuit protection protects converter and load from damage and consequent hazardous conditions
- Active back bias limit prevents damage to converter from external load induced pre-bias
- Output over-voltage protection protects load from damaging voltages
- Thermal shutdown protects converter from abnormal environmental conditions

Safety Features

- 2000V, 30 MΩ input-to-output isolation provides input/output ground separation
- UL/cUL 60950-1 recognized (US & Canada), basic insulation rating
- TUV certified to EN60950-1
- Meets 72/23/EEC and 93/68/EEC directives which facilitates CE Marking in user's end product
- Board and plastic components meet UL94V-0 flammability requirements

MECHANICAL DIAGRAM





NOTES

1) Pins 1-3, 5-7 are 0.040" (1.02mm) diameter with 0.080" (2.03 mm) diameter standoff shoulders.
2) Pins 4 and 8 are 0.062" (1.57 mm) diameter with 0.100" (2.54 mm) diameter standoff shoulders.
3) Other pin extension lengths available. Recommended pin length is 0.03" (0.76mm) greater than the PCB thickness.
4) All Pins: Material - Copper Alloy; Finish - Matte Tin over Nickel plate
5) Undimensioned components shown are for visual reference only.
6) Weight: 1.2 oz. (34 g)
7) All dimensions in inches (mm)
   Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)
   x.xxx +/-0.010 in. (x.xx +/-0.25mm)
8) Workmanship: Meets or exceeds IPC-A-610C Class II
9) UL/TUV standards require a clearance greater than 0.04" (1.02mm) between input and output for Basic insulation. This issue should be considered if any copper traces are on the top side of the user's board. Note that the ferrite cores are considered part of the input/primary circuit.
10) The flanged pins are designed to permit surface mount soldering (allowing to avoid the wave soldering process) through the use of the flanged pin-in-paste technique.

PIN DESIGNATIONS

Pin No.	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	TTL input to turn converter on and off, referenced to Vin(-), with internal pull up.
3	Vin(-)	Negative input voltage
4	Vout(-)	Negative output voltage
5	SENSE(-)	Negative remote sense[1]
6	TRIM	Output voltage trim[2]
7	SENSE(+)	Positive remote sense[3]
8	Vout(+)	Positive output voltage

Notes:

1. SENSE(–) should be connected to Vout(–) either remotely or at the converter.
2. Leave TRIM pin open for nominal output voltage.
3. SENSE(+) should be connected to Vout(+) either remotely or at the converter.

PQ40150QGA07 ELECTRICAL CHARACTERISTICS

T_A=25°C, airflow rate=300 LFM, V_{in}=48Vdc unless otherwise noted; full operating temperature range is -40°C to +100°C ambient temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS					
Input Voltage					
Non-Operating			100	V	Continuous
Operating			80	V	Continuous
Operating Transient Protection			100	V	100ms transient, square wave
Isolation Voltage (input to output)			2000	V	Basic insulation, Pollution Degree 2
Operating Temperature	-40		100	°C	
Storage Temperature	-55		125	°C	
Voltage at ON/OFF input pin	-2		18	V	
INPUT CHARACTERISTICS					
Operating Input Voltage Range	18	48	75	V	
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	16.0	16.5	17.0	V	
Turn-Off Voltage Threshold	14.5	15.0	15.5	V	
Lockout Voltage Hysteresis	1.0	1.5	2.0	V	
Maximum Input Current			6.17	A	100% Load, 18 Vin
No-Load Input Current		80	160	mA	
Disabled Input Current		2.0	4	mA	
Inrush Current Transient Rating			0.03	A^2s	
Response to Input Transient		350		mV	1000V/ms input transient, 48V-75V step
Input Reflected Ripple Current		5		mA	P-P thru 10µH inductor; Figures 17 & 19
Input Terminal Ripple Current		145		mA	RMS; Figures 17 & 18
Recommended Input Fuse			20.0	A	Fast blow external fuse recommended
Input Filter Component Values (L\C)		1.5 \ 6.6		µH\µF	Internal values; Figure E
Recommended External Input Capacitance		47		µF	
OUTPUT CHARACTERISTICS					
Output Voltage Set Point	14.77	15.00	15.23	V	
Output Voltage Regulation					
Over Line		±0.04 \ 6	±0.2 \ 30	%\mV	
Over Load		±0.04 \ 6	±0.2 \ 30	%\mV	
Over Temperature		±112	±300	mV	
Total Output Voltage Range	14.47		15.54	V	Over sample, line, load, temperature & life
Output Voltage Ripple and Noise[1]					20MHz bandwidth; Figures 17 & 20
Peak-to-Peak		75	150	mV	Full Load; Figures 17 & 20
RMS		15	30	mV	Full Load; Figures 17 & 20
Operating Output Current Range	0		6.67	A	Subject to thermal derating; Figures 5 - 12
Output DC Current-Limit Inception	7	8	9	A	Output Voltage 10% Low; Figure 21
Output DC Current-Limit Shutdown Voltage		13.2		V	
Back-Drive Current Limit while Enabled	0.1	0.3	0.8	A	Negative current drawn from output
Back-Drive Current Limit while Disabled	0	10	50	mA	Negative current drawn from output
Maximum Output Capacitance			2,000	µF	15Vout at 6.7A Resistive Load
DYNAMIC CHARACTERISTICS					
Input Voltage Ripple Rejection		45		dB	120 Hz; Figure 24
Output Voltage During Load Current Transient					
For a Step Change in Output Current (0.1A/µs)		800		mV	50% to 75% to 50% Iout max; Figure 15
For a Step Change in Output Current (4A/µs)		280		mV	50% to 75% to 50% Iout max; Figure 16
Settling Time		800		µs	To within 1% Vout nom
Turn-On Transient					
Turn-On Time	4	8	16	ms	Full load, Vout=90% nom.; Figures 13 & 14
Start-Up Inhibit Time	180	200	240	ms	-40°C to +125°C; Figure F
Output Voltage Overshoot		0		%	
EFFICIENCY					
100% Load (24Vin)		90.0		%	Figures 1 - 4, 24Vin
50% Load (24Vin)		91.5		%	Figures 1 - 4, 24Vin
100% Load (48Vin)		89.5		%	Figures 1 - 4, 48Vin
50% Load (48Vin)		90.5		%	Figures 1 - 4, 48Vin
TEMPERATURE LIMITS FOR POWER DERATING CURVES					
Semiconductor Junction Temperature			125	°C	Package rated to 150°C
Board Temperature			125	°C	UL rated max operating temp 130°C
Transformer Temperature			125	°C	See Figures 5 - 12 for derating curves
ISOLATION CHARACTERISTICS					
Isolation Voltage (dielectric strength)		2000		V	
Isolation Resistance		30		MΩ	
Isolation Capacitance[2]		470		pF	

Note 1: For applications requiring reduced output voltage ripple and noise, consult SynQor applications support (e-mail: support@synqor.com)
Note 2: Higher values of isolation capacitance can be added external to the module.

ELECTRICAL CHARACTERISTICS (Continued)

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
FEATURE CHARACTERISTICS					
Switching Frequency	291	315	340	kHz	Regulation stage and Isolation stage
ON/OFF Control (Option P)					
Off-State Voltage	-2		0.8	V	
On-State Voltage	2.4		18	V	
ON/OFF Control (Option N)					
Off-State Voltage	2.4		18	V	
On-State Voltage	-2		0.8	V	
ON/OFF Control (Either Option)					Figures A & B
Pull-Up Voltage		Vin/4.25	17.7	V	
Pull-Up Resistance		40		kΩ	
Output Voltage Trim Range	-20		+10	%	Measured across Pins 8 & 4; Figure C; Figure 27
Output Voltage Remote Sense Range			+10	%	Measured across Pins 8 & 4; Figure 27
Output Over-Voltage Protection	117	122	127	%	Over full temp range; % of nominal Vout
Over-Temperature Shutdown		125		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis		10		°C	
Load Current Scale Factor		1,000			See Output Load Current app. note on our web
RELIABILITY CHARACTERISTICS					
Calculated MTBF (Telcordia)		2.50		10^6 Hrs.	TR-NWT-000332; 80% load,300LFM, 40°C T_a
Calculated MTBF (MIL-217)		1.56		10^6 Hrs.	MIL-HDBK-217F; 80% load, 300LFM, 40°C T_a
Field Demonstrated MTBF				10^6 Hrs.	See our website for details

STANDARDS COMPLIANCE

Parameter	Notes
STANDARDS COMPLIANCE	
UL/cUL 60950-1	File # E194341, Basic insulation & pollution degree 2
EN60950-1	Certified by TUV
72/23/EEC	
93/68/EEC	
Needle Flame Test (IEC 695-2-2)	Test on entire assembly; board & plastic components UL94V-0 compliant
IEC 61000-4-2	ESD test, 8kV - NP, 15kV air - NP (Normal Performance)
GR-1089-CORE	Section 7 - electrical safety, Section 9 - bonding/grounding
Telcordia (Bellcore) GR-513	

- An external input fuse must always be used to meet these safety requirements. Contact SynQor for official safety certificates on new releases or download from the SynQor website.

QUALIFICATION TESTING

Parameter	# Units	Test Conditions
QUALIFICATION TESTING		
Life Test	32	95% rated Vin and load, units at derating point, 1000 hours
Vibration	5	10-55Hz sweep, 0.060" total excursion,1 min./sweep, 120 sweeps for 3 axis
Mechanical Shock	5	100g minimum, 2 drops in x and y axis, 1 drop in z axis
Temperature Cycling	10	-40°C to 100°C, unit temp. ramp 15°C/min., 500 cycles
Power/Thermal Cycling	5	Toperating = min to max, Vin = min to max, full load, 100 cycles
Design Marginality	5	Tmin-10°C to Tmax+10°C, 5°C steps, Vin = min to max, 0-105% load
Humidity	5	85°C, 85% RH, 1000 hours, 2 minutes on and 6 hours off
Solderability	15 pins	MIL-STD-883, method 2003

- Extensive characterization testing of all SynQor products and manufacturing processes is performed to ensure that we supply robust, reliable product. Contact the factory for official product family qualification documents.

OPTIONS

SynQor provides various options for Pin Length and Feature Set for this family of Power Interface Modules. Please consult the last page for information on available options.

PATENTS

SynQor is protected under various patents. Please consult the last page for further information.

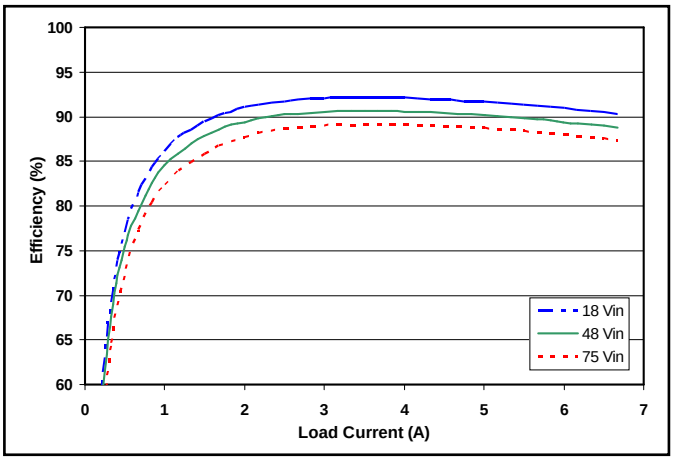


Figure 1: *Efficiency at nominal output voltage vs. load current for 18V, 48V and 75V input at 25°C.*

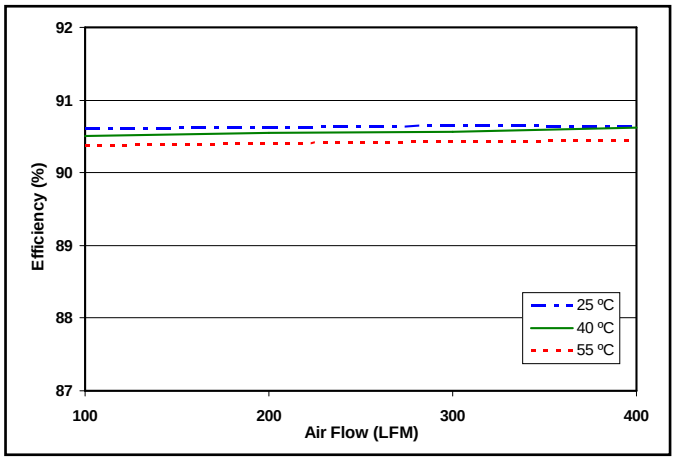


Figure 2: *Efficiency at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C (nominal input voltage, 48V input).*

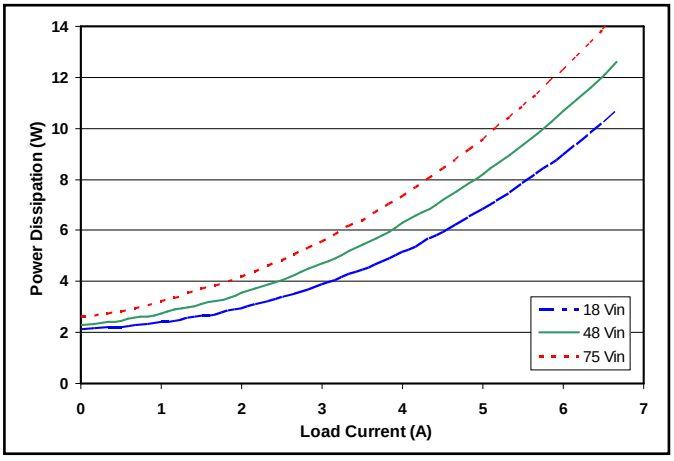


Figure 3: *Power dissipation at nominal output voltage vs. load current for 18V, 48V and 75V input at 25°C.*

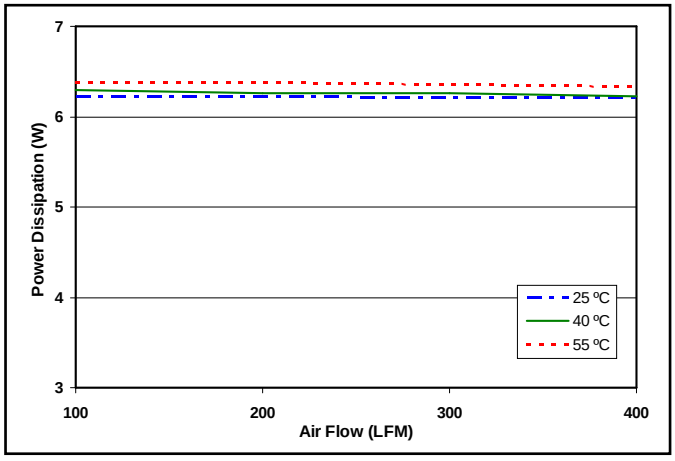


Figure 4: *Power dissipation at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C (nominal input voltage, 48V input).*

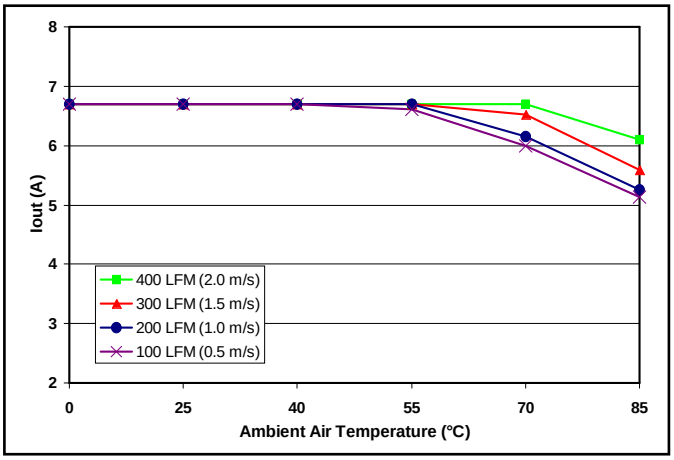


Figure 5: *Maximum output power derating curves vs. ambient air temperature for airflow rates of 100 LFM through 400 LFM with air flowing across the converter from pin 3 to pin 1 (24V input).*

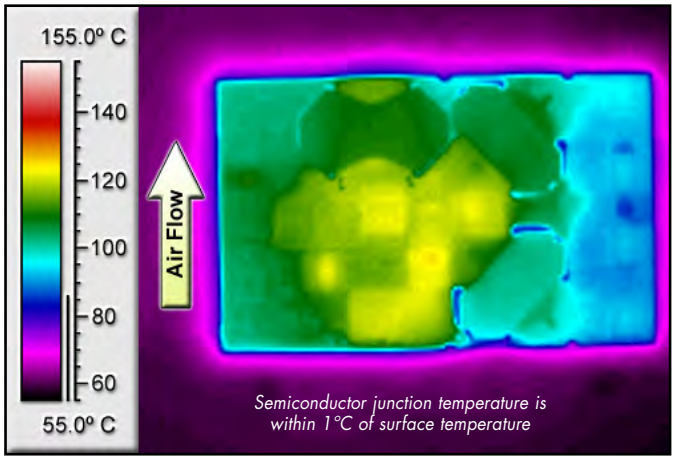


Figure 6: *Thermal plot of converter at 6.7 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across the converter sideways from pin 3 to pin 1 (24V input).*

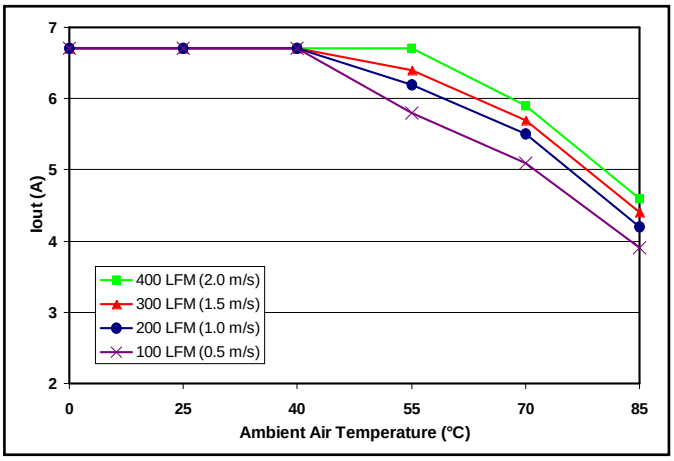


Figure 7: *Maximum output power derating curves vs. ambient air temperature for airflow rates of 100 LFM through 400 LFM with air flowing across the converter from pin 3 to pin 1 (48V input).*

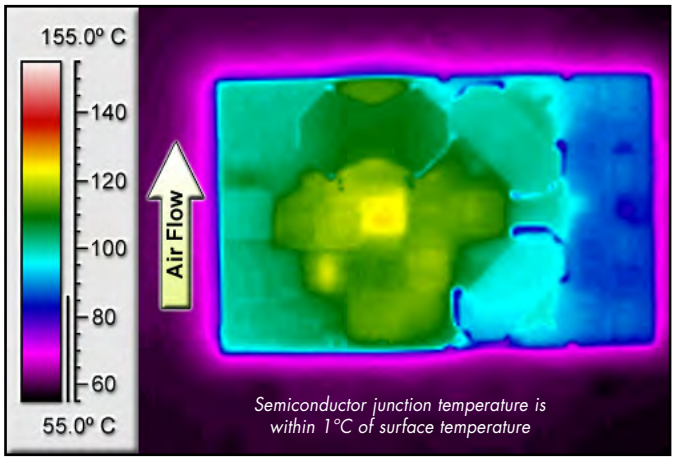


Figure 8: *Thermal plot of converter at 6.2 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across the converter sideways from pin 3 to pin 1 (48V input).*

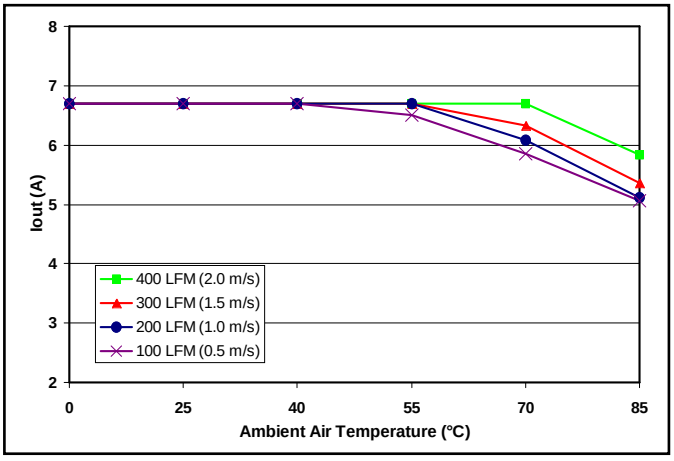


Figure 9: *Maximum output power-derating curves vs. ambient air temperature for airflow rates of 100 LFM through 400 LFM with air flowing from input to output (24V input).*

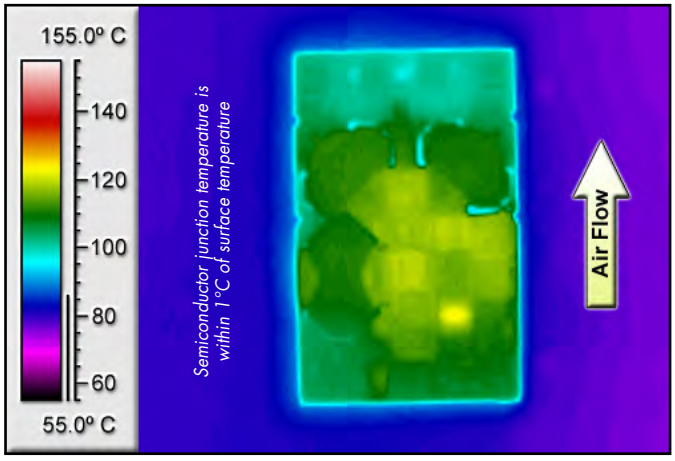


Figure 10: *Thermal plot of converter at 6.7 amp load current with 85°C air flowing at the rate of 200 LFM. Air is flowing across the converter from input to output (24V input).*

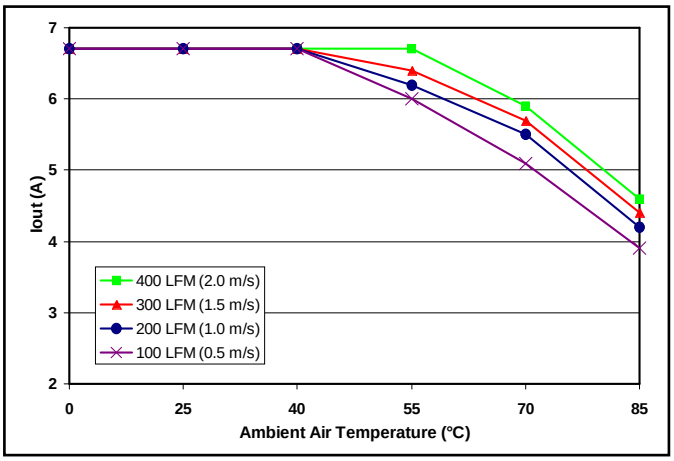


Figure 11: *Maximum output power-derating curves vs. ambient air temperature for airflow rates of 100 LFM through 400 LFM with air flowing from input to output (48V input).*

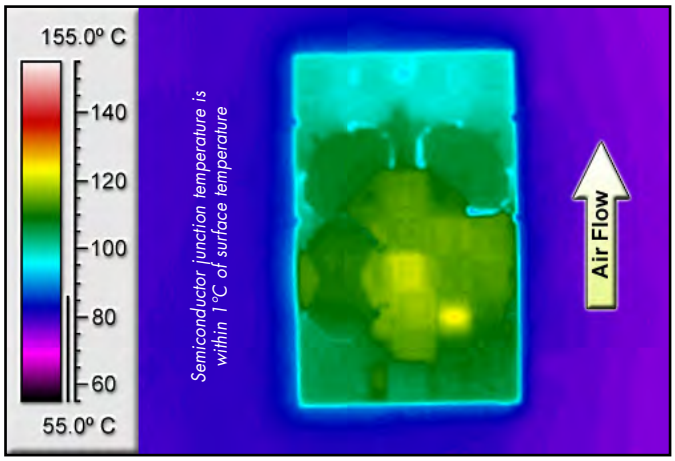


Figure 12: *Thermal plot of converter at 6.2 amp load current with 85°C air flowing at the rate of 200 LFM. Air is flowing across the converter from input to output (48V input).*

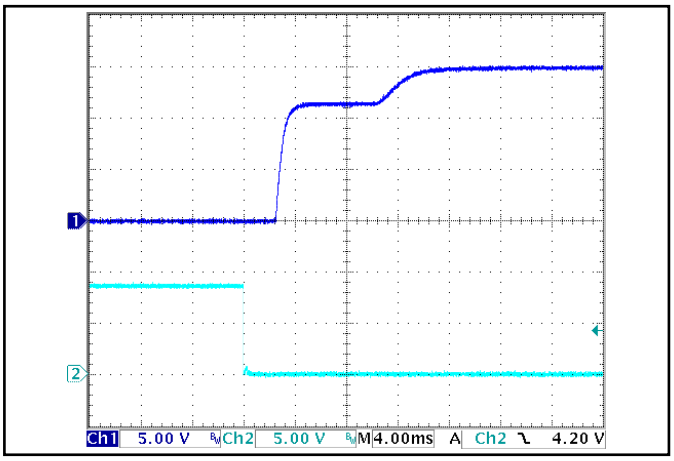


Figure 13: *Turn-on transient at full load (resistive load) (4 ms/div). Input voltage pre-applied. Ch 1: Vout (5V/div). Ch 2: ON/OFF input (5V/div).*

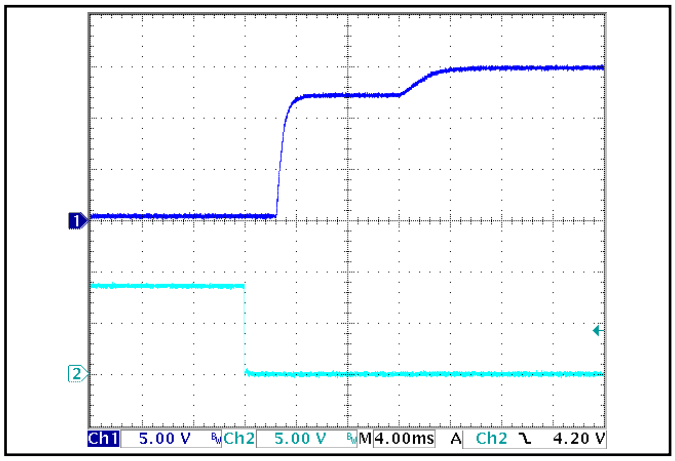


Figure 14: *Turn-on transient at zero load (4 ms/div). Ch 1: Vout (5V/div). Ch 2: ON/OFF input (5V/div).*

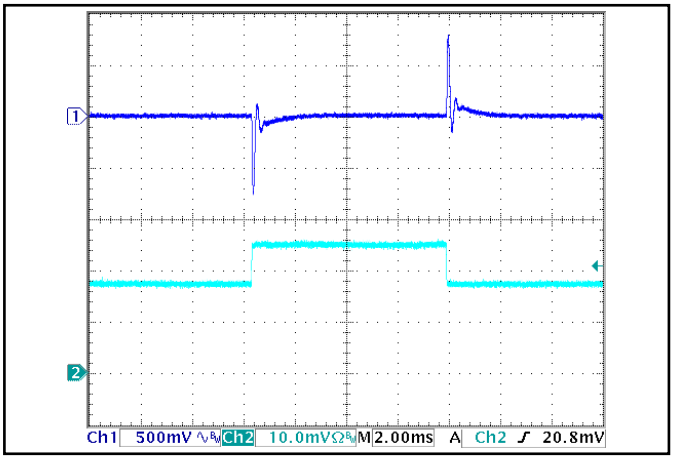


Figure 15: *Output voltage response to step-change in load current (50%-75%-50% of Iout(max); dI/dt = 0.1A/µs). Load cap: 10µF, 100 mΩ ESR tantalum cap and 1µF ceramic cap. Ch 1: Vout (500mV/div), Ch 2: Iout (2A/div).*

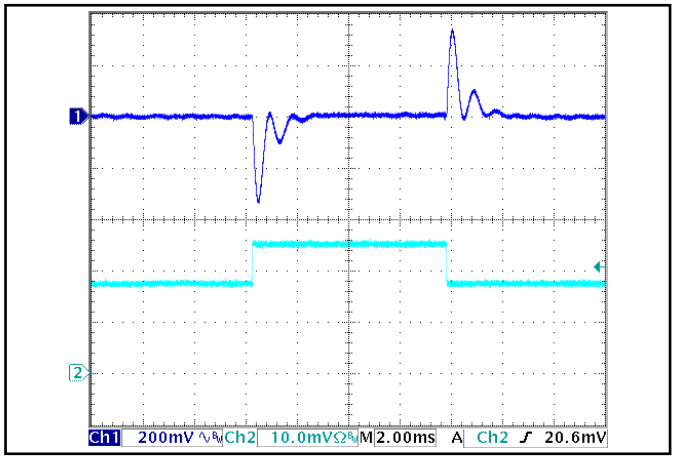


Figure 16: *Output voltage response to step-change in load current (50%-75%-50% of Iout(max): dI/dt = 5A/µs). Load cap: 470µF, 30 mΩ ESR tantalum cap and 1µF ceramic cap. Ch 1: Vout (200mV/div), Ch 2: Iout (2A/div).*

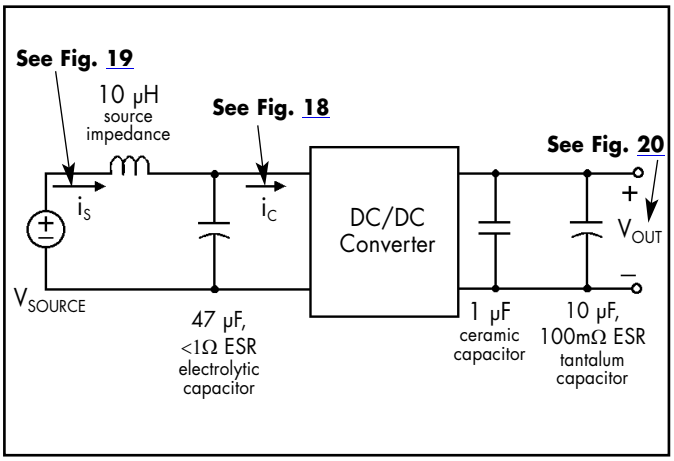


Figure 17: *Test set-up diagram showing measurement points for Input Terminal Ripple Current (Figure 18), Input Reflected Ripple Current (Figure 19) and Output Voltage Ripple (Figure 20).*

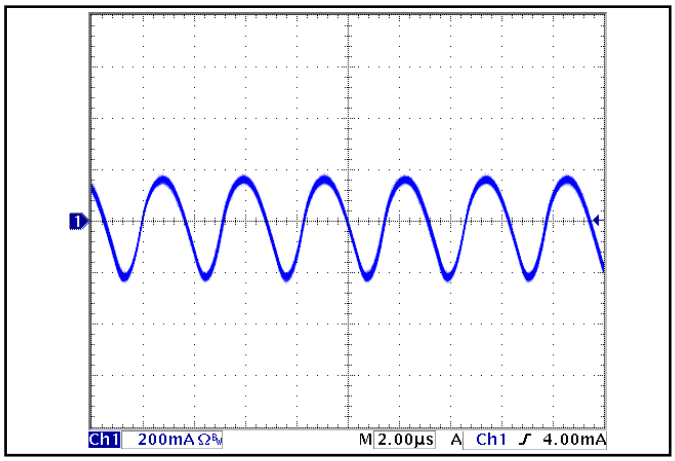


Figure 18: *Input Terminal Ripple Current, i_C, at full rated output current and nominal input voltage with 10µH source impedance and 47µF electrolytic capacitor (200 mA/div). See Figure 17.*

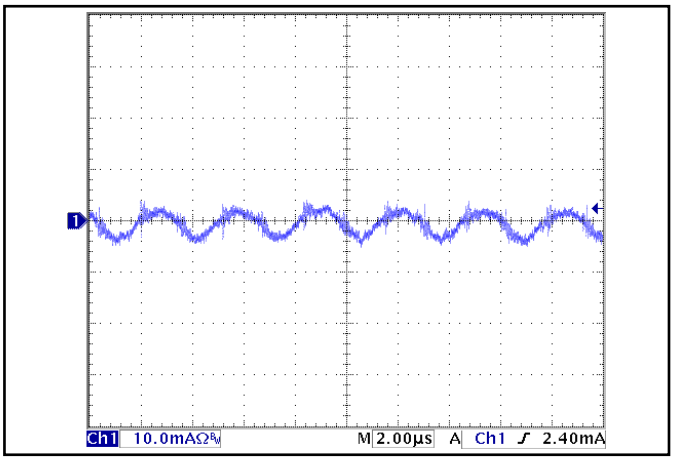


Figure 19: *Input reflected ripple current, i_S, through a 10 µH source inductor at nominal input voltage and rated load current (10 mA/div). See Figure 17.*

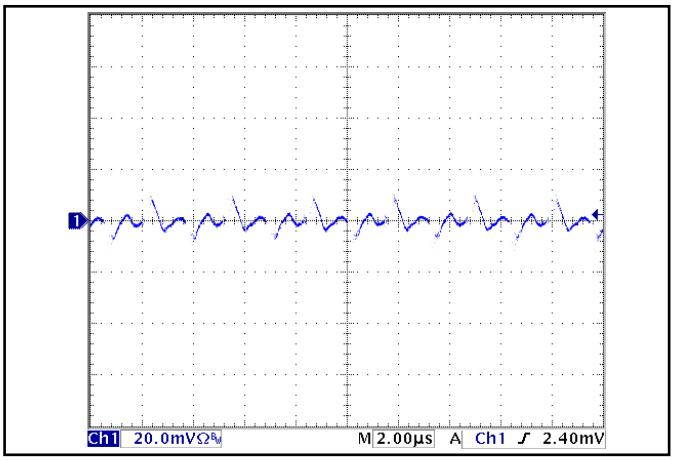


Figure 20: *Output voltage ripple at nominal input voltage and rated load current (20 mV/div). Load capacitance: 1µF ceramic capacitor and 10µF tantalum capacitor. Bandwidth: 20 MHz. See Figure 17.*

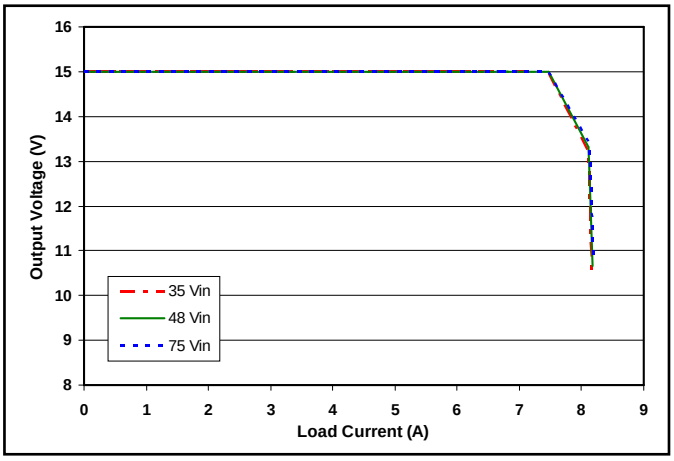


Figure 21: *Output voltage vs. load current showing typical current limit curves and converter shutdown points.*

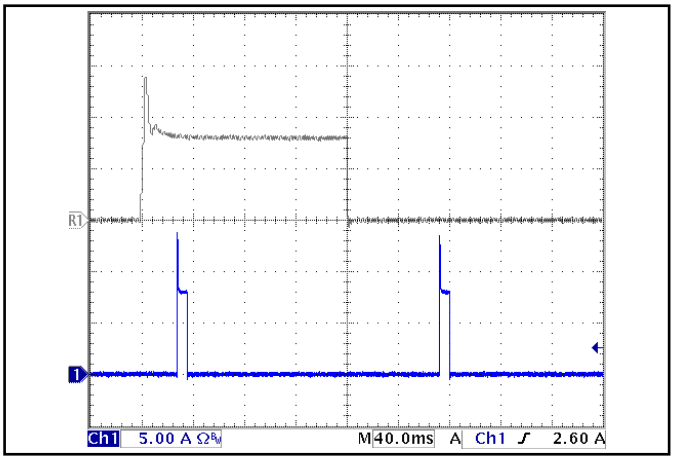


Figure 22: *Load current (5A/div) as a function of time when the converter attempts to turn on into a 10 mΩ short circuit. Top trace (40ms/div) is an expansion of the on-time portion of the bottom trace.*

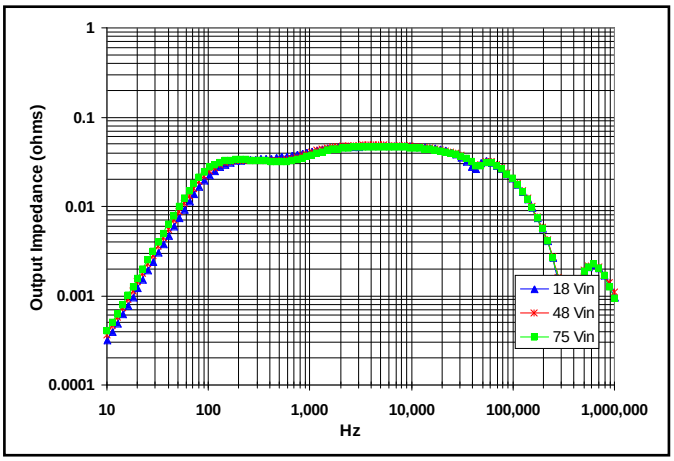


Figure 23: *Magnitude of incremental output impedance (Z_{out} = v_{out}/i_{out}) for minimum, nominal, and maximum input voltage at full rated power.*

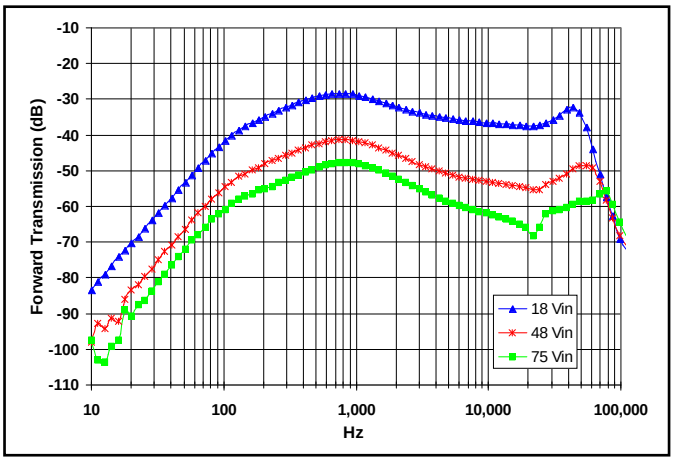


Figure 24: *Magnitude of incremental forward transmission (FT = v_{out}/v_{in}) for minimum, nominal, and maximum input voltage at full rated power.*

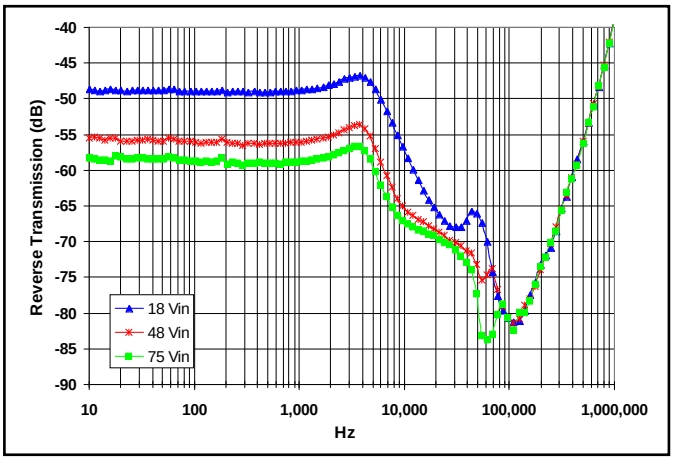


Figure 25: *Magnitude of incremental reverse transmission (RT = i_{in}/i_{out}) for minimum, nominal, and maximum input voltage at full rated power.*

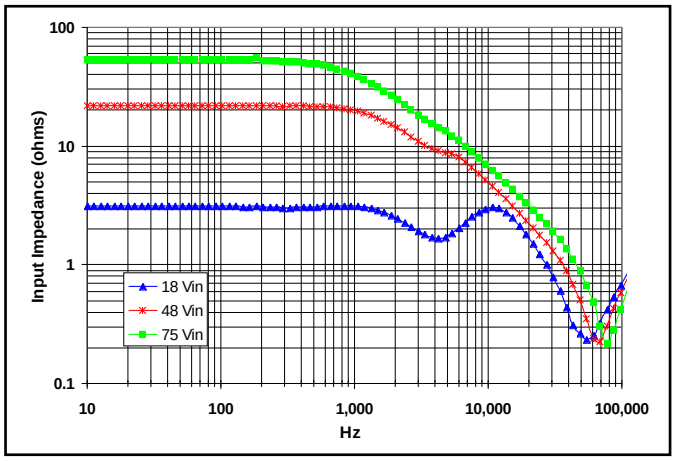


Figure 26: *Magnitude of incremental input impedance ($Z_{in} = v_{in}/i_{in}$) for minimum, nominal, and maximum input voltage at full rated power.*

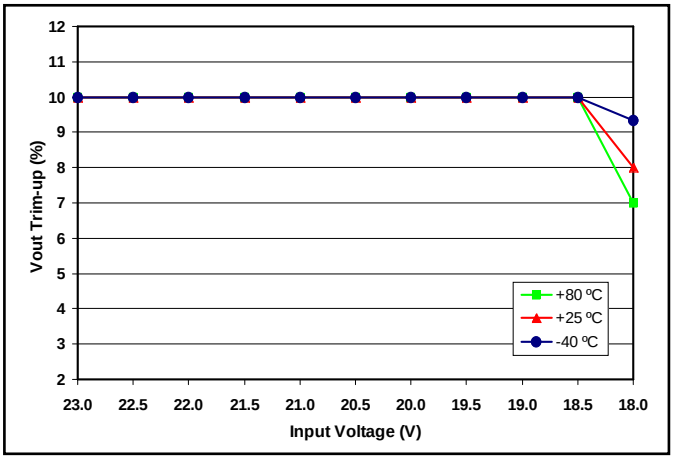


Figure 27: *Achievable trim-up percentage vs. input voltage at +85°C, +25°C and -40°C (all at full load). Full trim-up percentage is achieved at loads of 50% or less across full temperature and input voltage range.*

BASIC OPERATION AND FEATURES

The *Power*Qor series converter uses a two-stage power conversion topology. The first stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second stage uses a transformer to provide the functions of input/output isolation and voltage step-down to achieve the low output voltage required.

Both the first stage and the second stage switch at a fixed frequency for predictable EMI performance. Rectification of the transformer's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low on-state resistance, dissipate far less energy than Schottky diodes. This is the primary reason that the *Power*Qor converter has such high efficiency, even at very low output voltages and very high output currents.

Dissipation throughout the converter is so low that it does not require a heatsink for operation. Since a heatsink is not required, the *Power*Qor converter does not need a metal baseplate or potting material to help conduct the dissipated energy to the heatsink. The *Power*Qor converter can thus be built more simply and reliably using high yield surface mount techniques on a PCB substrate.

The *Power*Qor series of half-brick and quarter-brick converters uses the industry standard footprint and pin-out configuration.

CONTROL FEATURES

REMOTE ON/OFF (Pin 2): The ON/OFF input, Pin 2, permits the user to control when the converter is *on* or *off*. This input is referenced to the return terminal of the input bus, Vin(-). There are two versions of the converter that differ by the sense of the logic used for the ON/OFF input.

In the positive logic version, the ON/OFF input is active high (meaning that a high turns the converter *on*). In the negative logic version, the ON/OFF signal is active low (meaning that a low turns the converter *on*). Figure A details five possible circuits for driving the ON/OFF pin. Figure B is a detailed look of the internal ON/OFF circuitry.

REMOTE SENSE(+) (Pins 7 and 5): The SENSE(+) inputs correct for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 7 should be connected to Vout(+) and Pin 5 should be connected to Vout(-) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

$$[Vout(+) - Vout(-)] - [Vsense(+) - Vsense(-)] \leq \text{Sense Range \% x Vout}$$

Pins 7 and 5 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than its specified value.

Note: the output over-voltage protection circuit senses the voltage across the output (pins 8 and 4) to determine when it should trig-

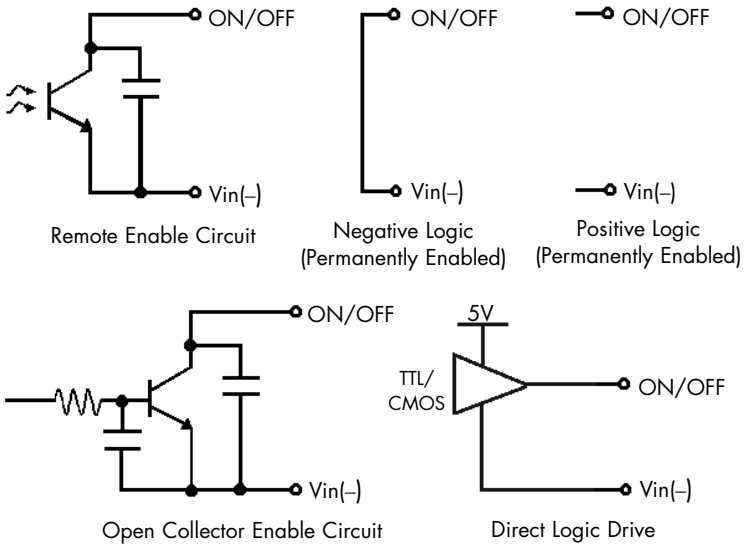


Figure A: *Various circuits for driving the ON/OFF pin.*

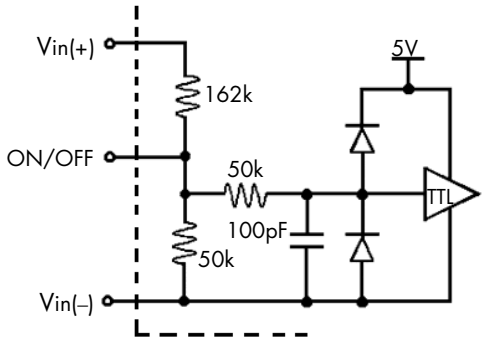


Figure B: *Internal ON/OFF pin circuitry*

ger, not the voltage across the converter's sense leads (pins 7 and 5). Therefore, the resistive drop on the board should be small enough so that output OVP does not trigger, even during load transients.

OUTPUT VOLTAGE TRIM (Pin 6): The TRIM input permits the user to adjust the output voltage across the sense leads up or down according to the trim range specifications.

To decrease the output voltage, the user should connect a resistor between Pin 6 and Pin 5 (SENSE(-) input). For a desired decrease of the nominal output voltage, the value of the resistor should be

$$R_{trim\text{-}down} = \left(\frac{511}{\Delta\%}\right) - 10.22 \quad (k\Omega)$$

where

$$\Delta\% = \left|\frac{Vnominal - Vdesired}{Vnominal}\right| \times 100\%$$

To increase the output voltage, the user should connect a resistor between Pin 6 and Pin 7 (SENSE(+) input). For a desired increase of the nominal output voltage, the value of the resistor should be

$$R_{trim\text{-}up} = \left(\frac{5.11V_{OUT}(100+\Delta\%)}{1.225\Delta\%} - \frac{511}{\Delta\%} - 10.22\right) (k\Omega)$$

where

V_{OUT} = Nominal Output Voltage

Figure C graphs the relationship between the trim resistor value and Rtrim-up and Rtrim-down, showing the total range the output voltage can be trimmed up or down.

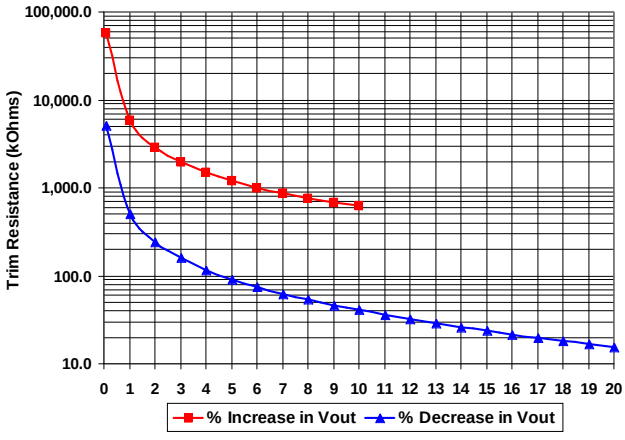


Figure C: *Trim Graph for 8.0Vout module*

Note: the TRIM feature does not affect the voltage at which the output over-voltage protection circuit is triggered. Trimming the output voltage too high may cause the over-voltage protection circuit to engage, particularly during transients.

It is not necessary for the user to add capacitance at the Trim pin. The node is internally bypassed to eliminate noise.

Total DC Variation of Vout: For the converter to meet its full specifications, the maximum variation of the DC value of Vout, due to both trimming and remote load voltage drops, should not be greater than that specified for the output voltage trim range.

PROTECTION FEATURES

Input Under-Voltage Lockout: The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability". The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specification page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

Output Current Limit: The maximum current limit remains constant as the output voltage drops. However, once the impedance of the short across the output is small enough to make the output voltage drop below the specified Output DC Current-Limit Shutdown Voltage, the converter turns off.

The converter then enters a "hiccup mode" where it repeatedly turns on and off at a 5 Hz (nominal) frequency with a 5% duty cycle until the short circuit condition is removed. This prevents excessive heating of the converter or the load board.

Output Over-Voltage Limit: If the voltage across the output pins exceeds the Output Over-Voltage Protection threshold, the converter will immediately stop switching. This prevents damage to the load circuit due to 1) excessive series resistance in output current path from converter output pins to sense point, 2) a release of a short-circuit condition, or 3) a release of a current limit condition. Load capacitance determines exactly how high the output voltage will rise in response to these conditions. After 200 ms the converter will automatically restart.

Over-Temperature Shutdown: A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the Over-Temperature Shutdown value. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the Over-Temperature Shutdown Restart Hysteresis value.

APPLICATION CONSIDERATIONS

Input System Instability: This condition can occur because any DC/DC converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynQor website which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

Application Circuits: Figure D below provides a typical circuit diagram which details the input filtering and voltage trimming.

Input Filtering and External Capacitance: Figure E below provides a diagram showing the internal input filter components. This filter dramatically reduces input terminal ripple current, which otherwise could exceed the rating of an external electrolytic input capacitor. The recommended external input capacitance is specified in the "Input Characteristics" section. More detailed information is available in the application note titled "EMI Characteristics" on the SynQor website.

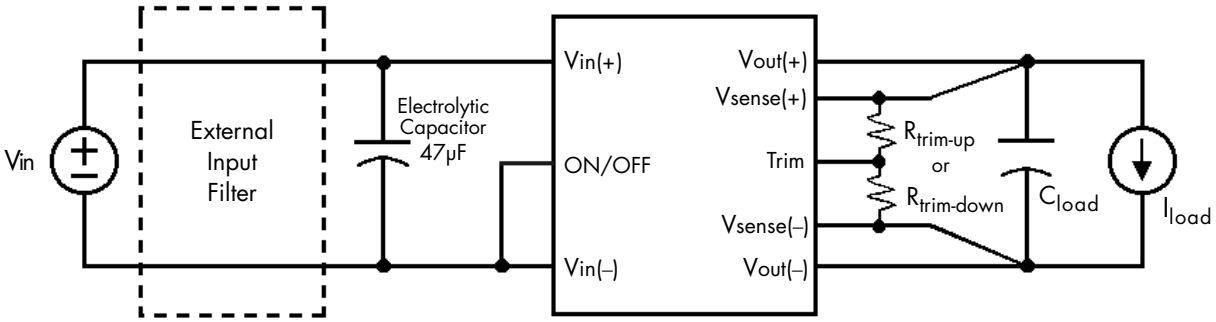


Figure D: *Typical application circuit (negative logic unit, permanently enabled).*

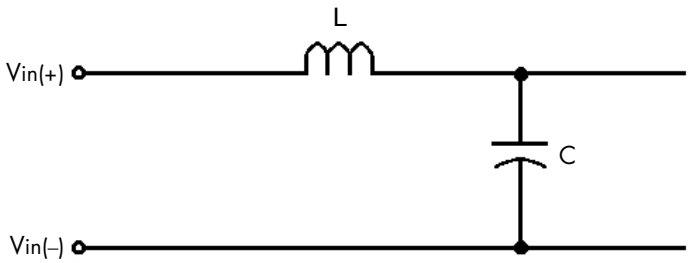


Figure E: *Internal Input Filter Diagram (component values listed on page 3).*

Startup Inhibit Period: The Startup Inhibit Period ensures that the converter will remain off for approximately 200ms when it is shut down for any reason. When an output short is present, this generates a 5Hz "hiccup mode," which prevents the converter from overheating. In all, there are seven ways that the converter can be shut down, initiating a Startup Inhibit Period:

- Input Under-Voltage Lockout
- Input Over-Voltage Shutdown (not present in Quarter-brick)
- Output Over-Voltage Protection
- Over Temperature Shutdown
- Current Limit
- Short Circuit Protection
- Turned off by the ON/OFF input

Figure F shows three turn-on scenarios, where a Startup Inhibit Period is initiated at t_0, t_1, and t_2:

Before time t_0, when the input voltage is below the UVL threshold, the unit is disabled by the Input Under-Voltage Lockout feature. When the input voltage rises above the UVL threshold, the Input Under-Voltage Lockout is released, and a Startup Inhibit Period is initiated. At the end of this delay, the ON/OFF pin is evaluated, and since it is active, the unit turns on.

At time t_1, the unit is disabled by the ON/OFF pin, and it cannot be enabled again until the Startup Inhibit Period has elapsed.

When the ON/OFF pin goes high after t_2, the Startup Inhibit Period has elapsed, and the output turns on within the typical Turn-On Time.

RoHS Compliance: The EU led RoHS (Restriction of Hazardous Substances) Directive bans the use of Lead, Cadmium, Hexavalent Chromium, Mercury, Polybrominated Biphenyls (PBB), and Polybrominated Diphenyl Ether (PBDE) in Electrical and Electronic Equipment. This SynQor product is 6/6 RoHS compliant. For more information please refer to SynQor's RoHS addendum available at our RoHS Compliance / Lead Free Initiative web page or e-mail us at rohs@synqor.com.

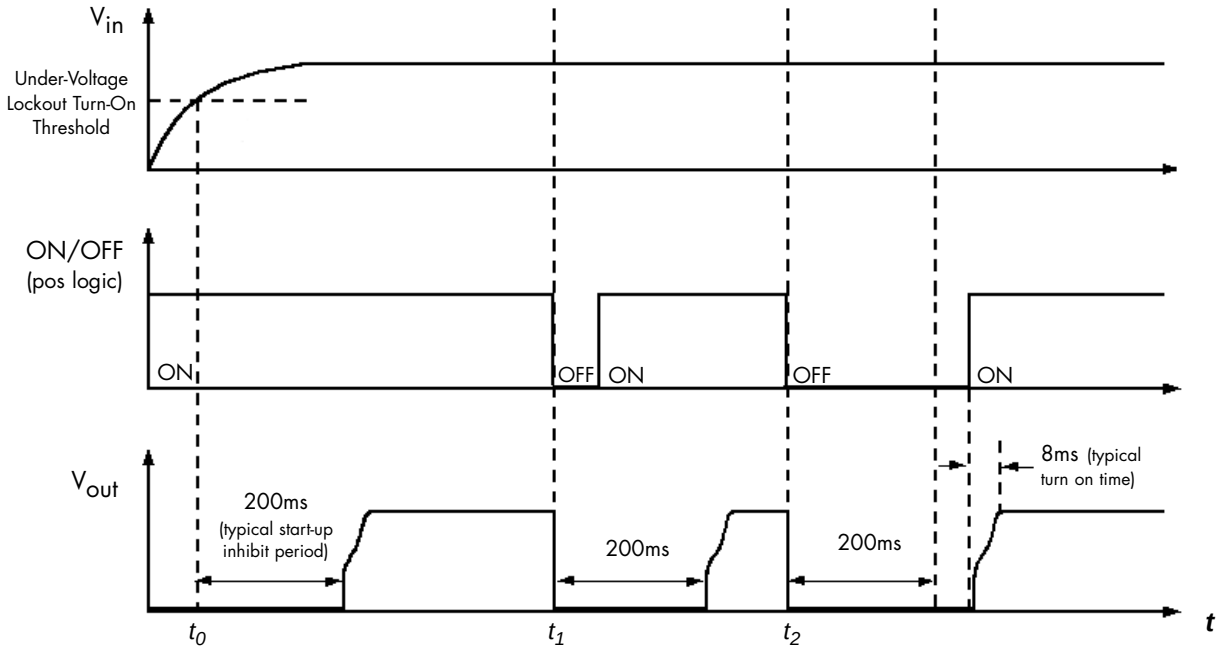
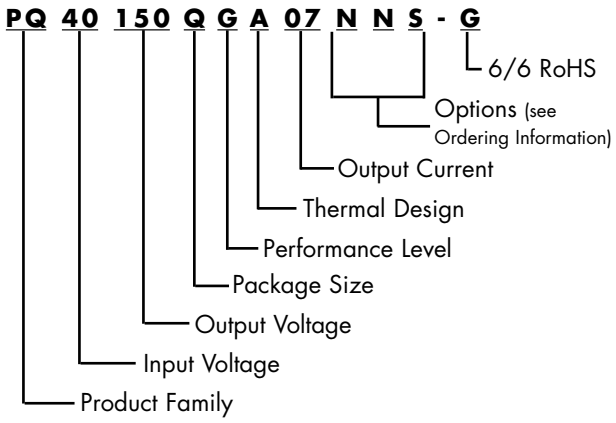


Figure F: *Startup Inhibit Period (turn-on time not to scale)*

PART NUMBERING SYSTEM

The part numbering system for SynQor's *Power*Qor DC/DC converters follows the format shown in the example below.



The first 12 characters comprise the base part number and the last 3 characters indicate available options. Although there are no default values for enable logic and pin length, the most common options are negative logic and 0.145" pins. These part numbers are more likely to be readily available in stock for evaluation and prototype quantities. The "-G" suffix indicates 6/6 RoHS compliance.

Application Notes

A variety of application notes and technical white papers can be downloaded in pdf format from our website.

PATENTS (additional patent applications may be filed)

SynQor holds the following patents, one or more of which might apply to this product:

5,999,417	6,222,742	6,545,890	6,577,109
6,594,159	6,731,520	6,894,468	6,896,526
6,927,987	7,050,309	7,072,190	7,085,146

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Boxborough, MA 01719
USA

ORDERING INFORMATION

The tables below show the valid model numbers and ordering options for converters in this product family. When ordering SynQor converters, please ensure that you use the complete 15 character part number consisting of the 12 character base part number and the additional 3 characters for options. A "-G" suffix indicates the product is 6/6 RoHS compliant.

Model Number	Input Voltage	Output Voltage	Max Output Current
PQ40033QGA25*xyz*	18 - 75 V	3.3 V	25 A
PQ40050QGA25*xyz*	18 - 75 V	5.0 V	20 A
PQ40080QGA09*xyz*	18 - 75 V	8.0 V	9 A
PQ40120QGA08*xyz*	18 - 75 V	12.0 V	8.33 A
PQ40150QGA07*xyz*	18 - 75 V	15.0 V	6.67 A

The following option choices must be included in place of the *x y z* spaces in the model numbers listed above.

Options Description: *x y z*		
Enable Logic	**Pin Length**	**Feature Set**
P - Positive N - Negative	K - 0.110" N - 0.145" R - 0.180" Y - 0.250"	S - Standard

Warranty
SynQor offers a three (3) year limited warranty. Complete warranty information is listed on our website or is available upon request from SynQor.

Information furnished by SynQor is believed to be accurate and reliable. However, no responsibility is assumed by SynQor for its use, nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of SynQor.