IXYS
INTEGRATED CIRCUITS DIVISION



LDA101
Optocoupler, Unidirectional Input Single-Transistor Output

Parameter	Rating	Units
Breakdown Voltage BV_{CEO}	30	V_P
Current Transfer Ratio (Typical)	300	%
Saturation Voltage	0.5	V
Input Control Current	1	mA

Features

- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 6-Pin Package, Thru-Hole or Surface Mount
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available

Applications

- Telecom Switching
- Tip/Ring Circuits
- Modem Switching (Laptop, Notebook, Pocket Size)
- Loop Detect
- Ringing Detect
- Current Sensing

Description

The LDA101 is a unidirectional-input optocoupler with a single-transistor output. Optically coupled technology provides a 3750V_{rms} isolation barrier between the input and the output.

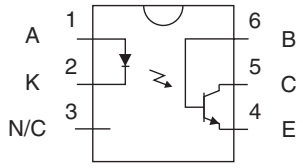
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 006

Ordering Information

Part Number	Description
LDA101	6-Pin DIP (50/Tube)
LDA101S	6-Pin Surface Mount (50/Tube)
LDA101STR	6-Pin Surface Mount (1000/Reel)

Pin Configuration



Absolute Maximum Ratings @ 25ºC

Parameter	Ratings	Units
Breakdown Voltage	30	V_P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Power Dissipation		
Input [1]	150	mW
Phototransistors [2]	150	
Isolation Voltage, Input to Output	3750	V_{rms}
Operational Temperature	-40 to +85	ºC
Storage Temperature	-40 to +125	ºC

[1] Derate linearly 1.33mW / ºC
[2] Derate linearly 2mW / ºC

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

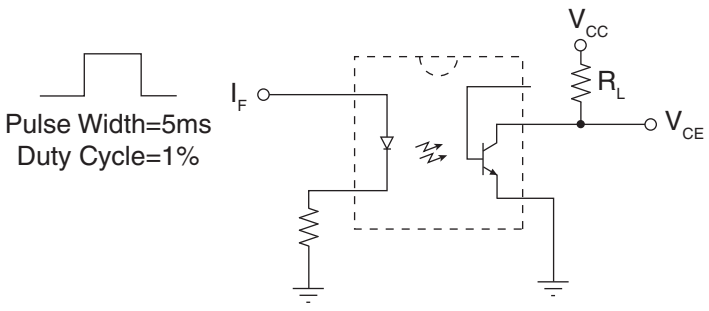
Electrical Characteristics @ 25ºC (Unless Otherwise Noted)

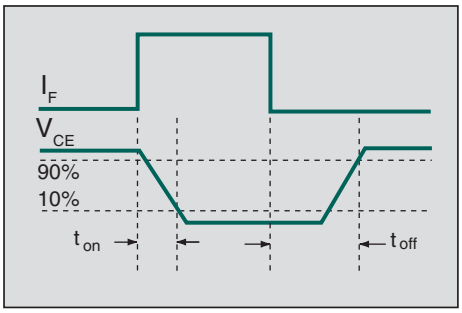
Parameters	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Phototransistor Breakdown Voltage	I_C=10µA	BV_{CEO}	30	-	-	V
Phototransistor Dark Current	V_{CE}=5V, I_F=0mA	I_{CEO}	-	10	500	nA
Saturation Voltage	I_C=2mA, I_F=1mA	$V_{CE(sat)}$	-	-	0.5	V
Current Transfer Ratio	I_F=1mA, V_{CE}=0.5V	CTR	33	300	-	%
Output Capacitance	25V, f=1MHz	C_{OUT}	-	6	-	pF
Input Characteristics						
Input Control Current	I_C=0.33mA, V_{CE}=0.5V	I_F	-	-	1	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.2	1.4	V
Common Characteristics						
Input to Output Capacitance	-	$C_{I/O}$	-	3	-	pF

Switching Characteristics @ 25ºC

Characteristic	Symbol	Test Condition	Typ	Units
Turn-On Time	t_{on}	V_{CC}=5V, I_F=2mA, R_L=1KΩ	7	µs
Turn-Off Time	t_{off}		20	

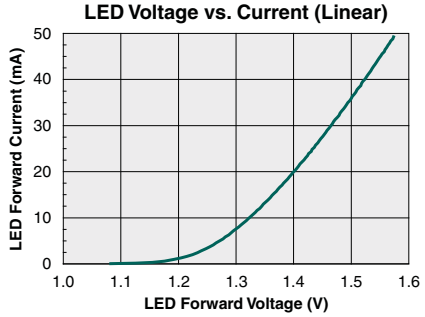
Switching Time Test Circuit



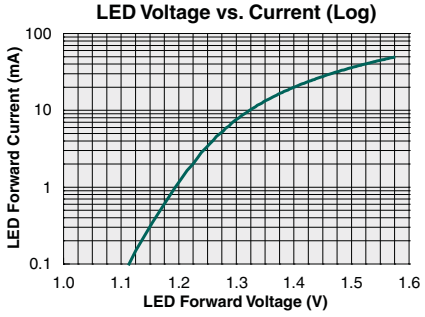


PERFORMANCE DATA @25ºC (Unless Otherwise Noted)*

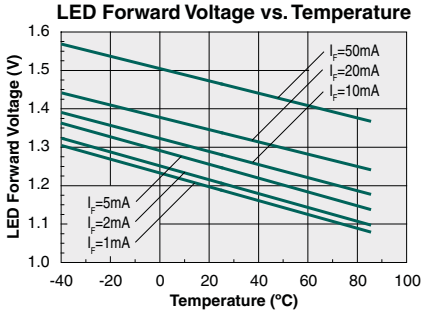
LED Voltage vs. Current (Linear)



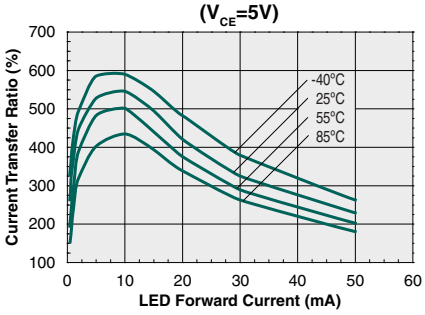
LED Voltage vs. Current (Log)



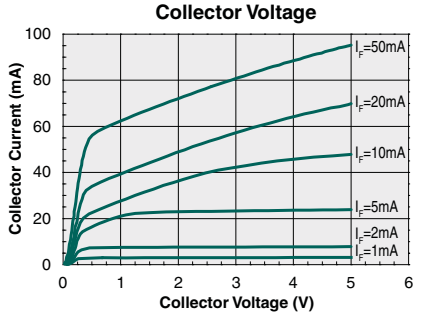
LED Forward Voltage vs. Temperature



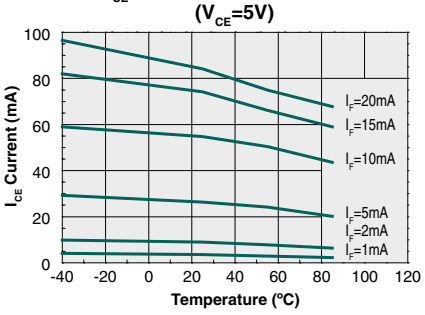
Typical CTR vs. LED Forward Current (V_{CE}=5V)



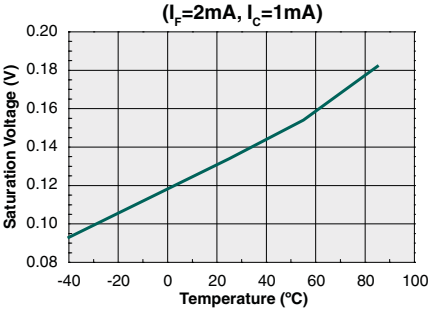
Typical Collector Current vs. Collector Voltage



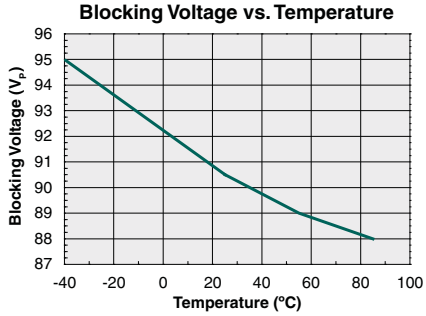
I_{CE} Current vs. Temperature (V_{CE}=5V)



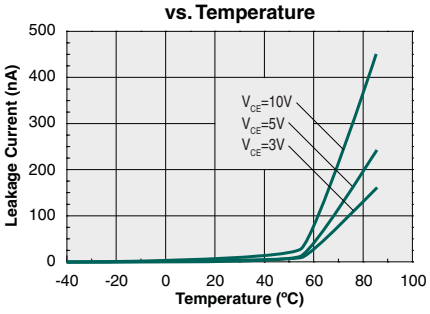
Saturation Voltage vs. Temperature (I_F=2mA, I_C=1mA)



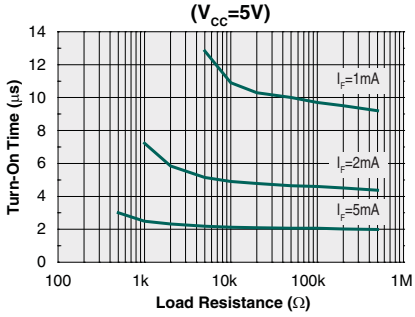
Blocking Voltage vs. Temperature



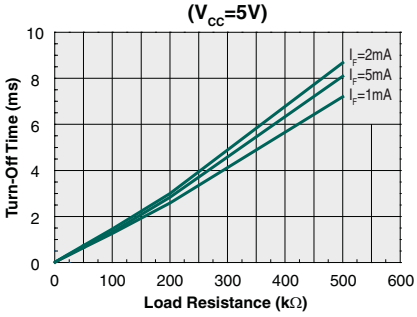
Collector Leakage Current vs. Temperature



Turn-On Time vs. Load Resistance (V_{CC}=5V)



Turn-Off Time vs. Load Resistance (V_{CC}=5V)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
LDA101 / LDA101S	MSL 1

ESD Sensitivity

This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

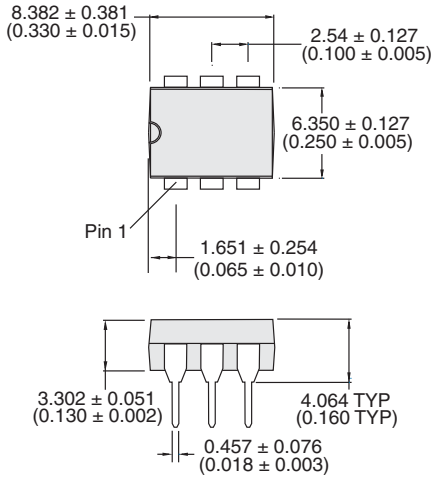
Device	Maximum Temperature x Time
LDA101 / LDA101S	250°C for 30 seconds

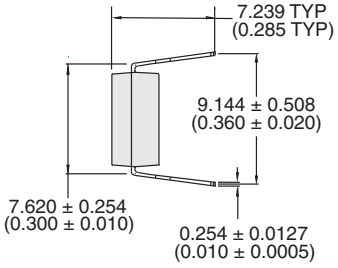
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

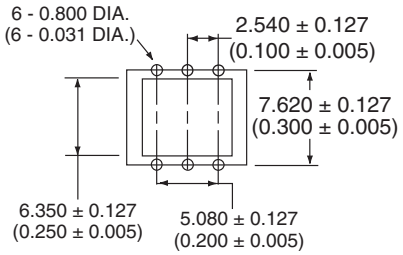
Mechanical Dimensions

LDA101



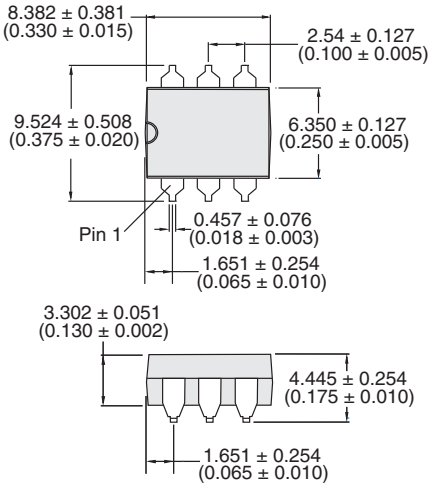


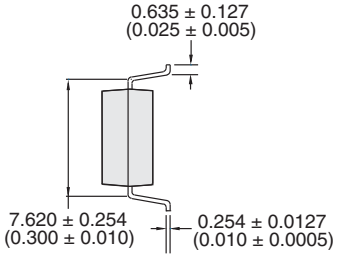
PCB Hole Pattern



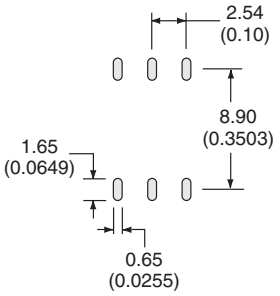
Dimensions
mm
(inches)

LDA101S



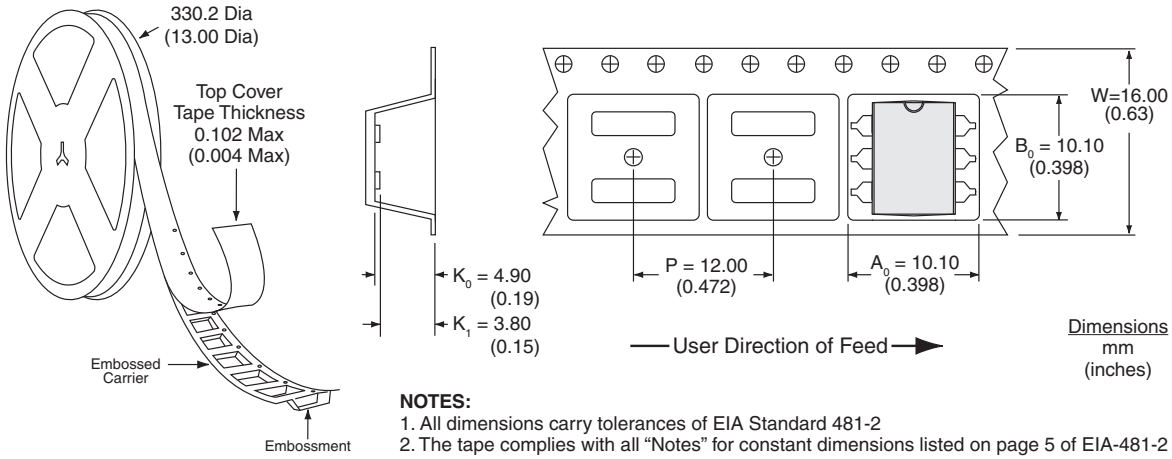


PCB Land Pattern



Dimensions
mm
(inches)

LDA101STR Tape & Reel



NOTES:
1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

For additional information please visit our website at: www.ixysic.com

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