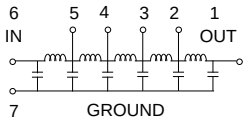
7 Pin Single-in-Line Package Passive Delay Lines

Zo OHMS ±10%	DELAY nS ±5% or ±2 nS†	TAP DELAYS ±5% or ±2 nS†	RISE TIME nS Max.	DCR OHMS Max.	PCA PART NUMBER	Zo OHMS ±10%	DELAY nS ±5% or ±2 nS †	TAP DELAYS ±5% or ±2 nS†	RISE TIME nS Max.	DCR OHMS Max.	PCA PART NUMBER
50	5	1	2	0.3	EPA572-5A	100	5	1	2	0.5	EPA572-5B
50	10	2	3	0.5	EPA572-10A	100	10	2	3.3	1.0	EPA572-10B
50	20	4	6	0.8	EPA572-20A	100	20	4	6	1.5	EPA572-20B
50	30	6	9	1.2	EPA572-30A	100	30	6	9	2.5	EPA572-30B
50	40	8	12	1.8	EPA572-40A	100	40	8	11	4.0	EPA572-40B
50	50	10	15	2.3	EPA572-50A	100	50	10	14	5.0	EPA572-50B
50	60	12	18	2.5	EPA572-60A	100	60	12	17	5.5	EPA572-60B
50	70	14	22	3.5	EPA572-70A	100	70	14	21	6.0	EPA572-70B
50	80	16	24	4.2	EPA572-80A	100	80	16	23	6.5	EPA572-80B
50	90	18	27	4.5	EPA572-90A	100	90	18	25	7.0	EPA572-90B
50	100	20	28	5.2	EPA572-100A	100	100	20	28	7.5	EPA572-100B

†Whichever is greater.

DC Electrical Characteristics	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay		100	PPM/°C
Insulation Resistance @ 100 Vdc	1K		Meg Ohms
Dielectric Strength		100	Vdc

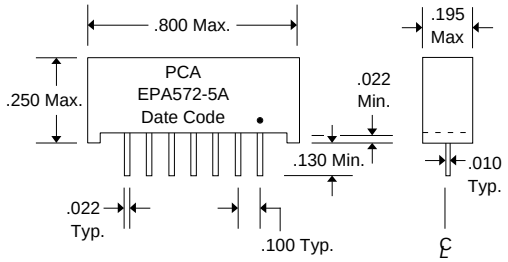
Schematic



Recommended Operating Conditions		Min	Max	Unit
P_W*	Pulse Width % of Total Delay	200		%
D*	Duty Cycle		40	%
T_A	Operating Free Air Temperature	0	70	°C

**These two values are inter-dependent.*

Package Dimensions



Input Pulse Test Conditions @ 25°C		
V_{IN}	Pulse Input Voltage	3 Volts
P_W	Pulse Width % of Total Delay	300 %
T_{RI}	Input Rise Time (10 - 90%)	2.0 nS
P_{RR}	Pulse Repetition Rate @ Td ≤ 150 nS	1.0 MHz
	Pulse Repetition Rate @ Td > 150 nS	300 KHz

Unless Otherwise Noted Dimensions in Inches
Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010

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