

N-Channel Enhancement Mode Power MOSFET

Description

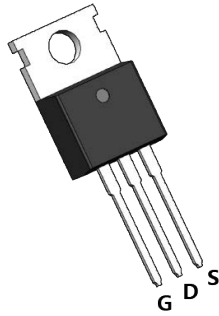
The HLDP50N06 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Application

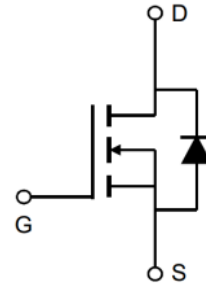
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Features

- $V_{DS} = 60V, I_D = 50A$
- $R_{DS(ON)} < 20m\Omega @ V_{GS} = 10V$
- Low gate charge.
- Green device available.
- Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- Excellent package for good heat dissipation.



Marking and pin assignment



N-Channel MOSFET

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	50	A
Drain Current-Continuous ($T_C = 100^\circ C$)	$I_D (100^\circ C)$	35.4	A
Pulsed Drain Current	I_{DM}	200	A
Maximum Power Dissipation	P_D	85	W
Derating factor		0.57	$W/^\circ C$
Single pulse avalanche energy ^(Note 5)	E_{AS}	300	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	1.8	$^\circ C/W$
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Package Marking and Ordering Information

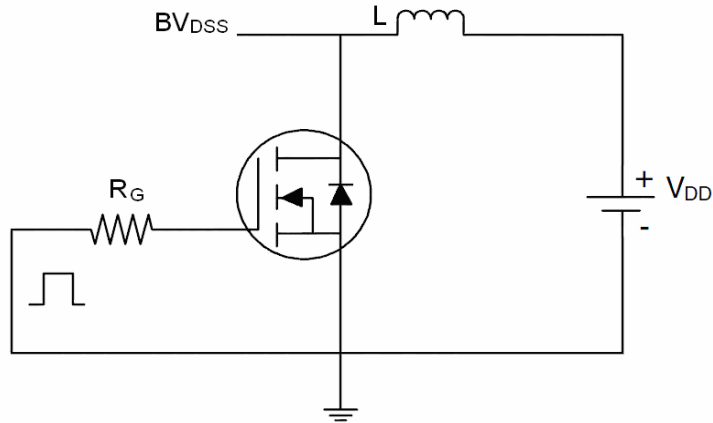
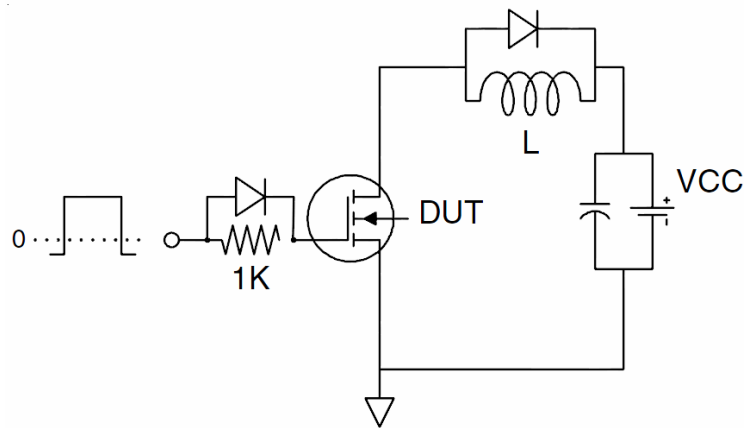
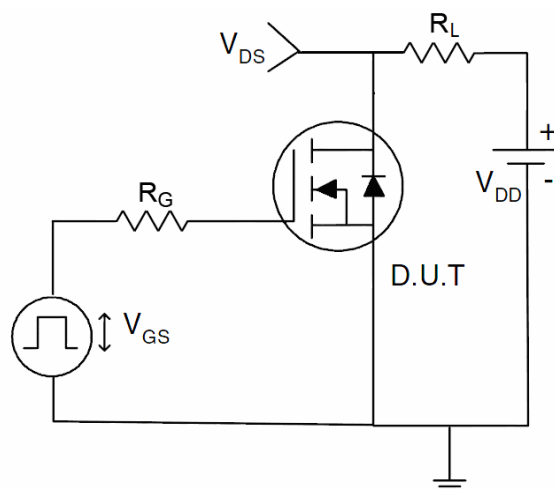
Part NO.	Marking	Package
HLDP50N06	P50N06	TO-220

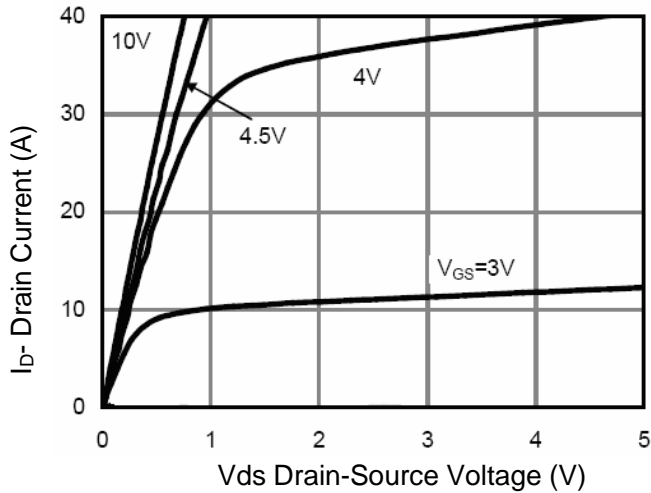
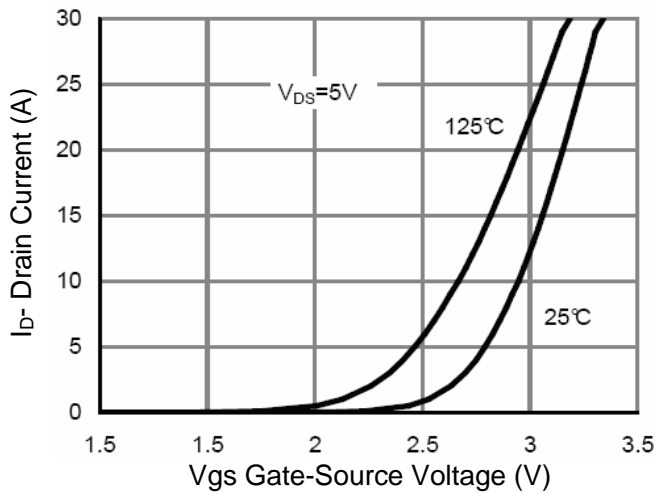
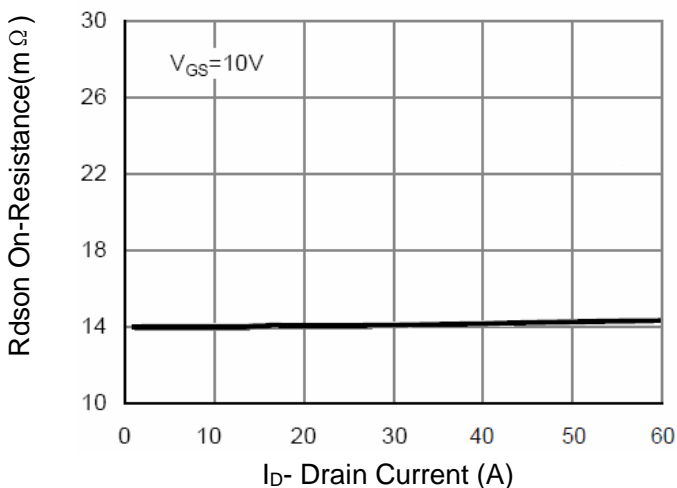
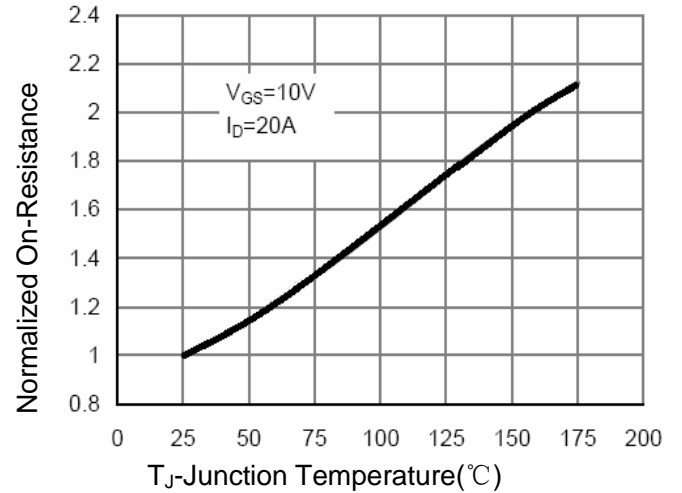
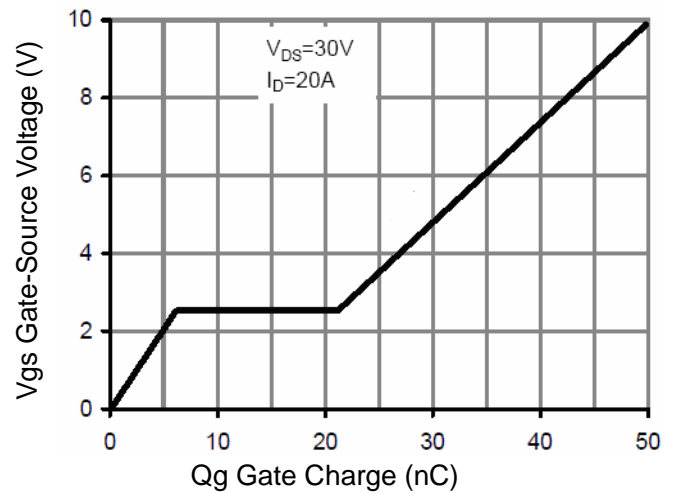
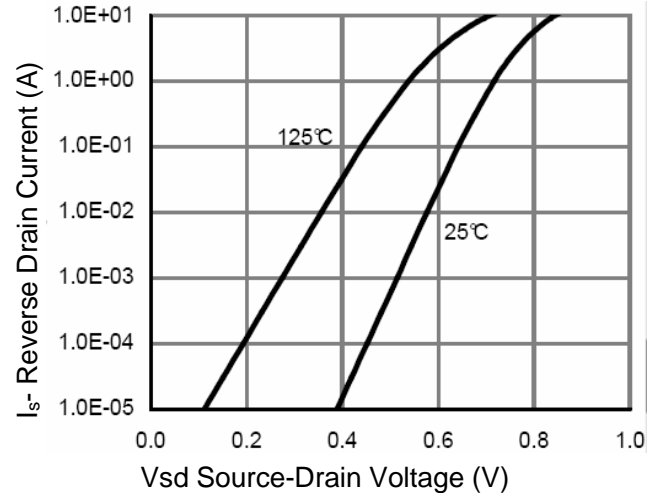
Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.4	1.9	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	14	20	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =20A	18	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C _{ISS}	V _{DS} =30V, V _{GS} =0V, F=1.0MHz	-	2050	-	PF
Output Capacitance	C _{OSS}		-	158	-	PF
Reverse Transfer Capacitance	C _{RSS}		-	120	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =30V, R _L =6.7Ω V _{GS} =10V, R _G =3Ω	-	7.4	-	nS
Turn-on Rise Time	t _r		-	5.1	-	nS
Turn-Off Delay Time	t _{d(off)}		-	28.2	-	nS
Turn-Off Fall Time	t _f		-	5.5	-	nS
Total Gate Charge	Q _g	V _{DS} =30V, I _D =20A, V _{GS} =10V	-	50	-	nC
Gate-Source Charge	Q _{gs}		-	6	-	nC
Gate-Drain Charge	Q _{gd}		-	15	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	50	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =20A di/dt = 100A/μs (Note3)	-	28	-	nS
Reverse Recovery Charge	Q _{rr}		-	40	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 se c.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2% .
4. Guaranteed by design, not subject to production
5. EAS condition : T_j=25°C, V_{DD}=30V, V_G=10V, L=0.5mH, R_G=25

Test circuit
1) E_{AS}TestCircuit

2) Gate Charge TestCircuit

3) Switch Time TestCircuit


Typical Electrical and Thermal Characteristics (Curves)

Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 Rdson- Drain Current

Figure 4 Rdson-Junction Temperature

Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward

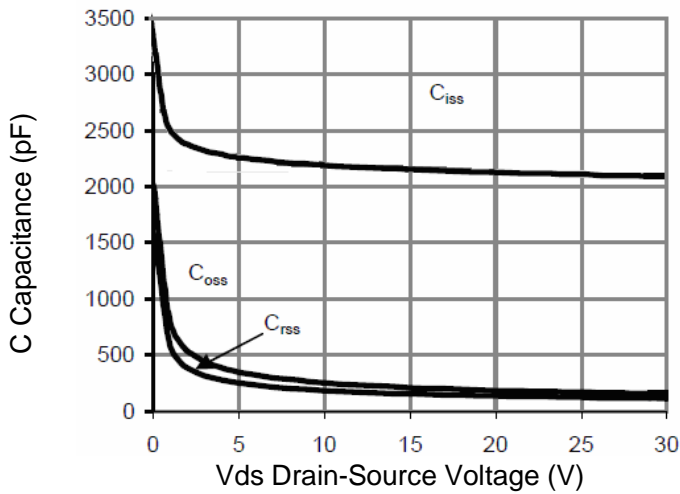


Figure 7 Capacitance vs Vds

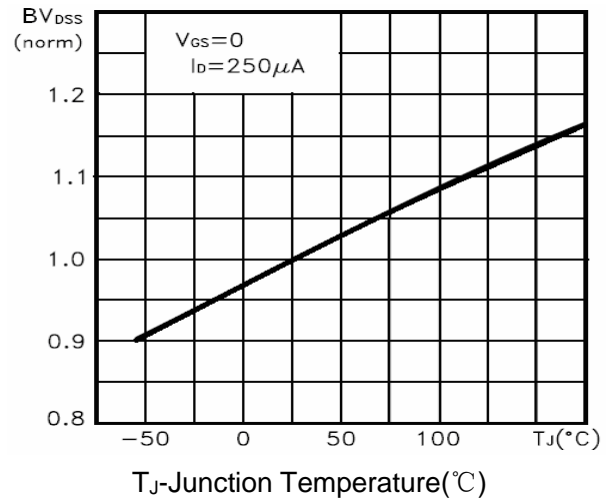


Figure 9 BV_{DSS} vs Junction Temperature

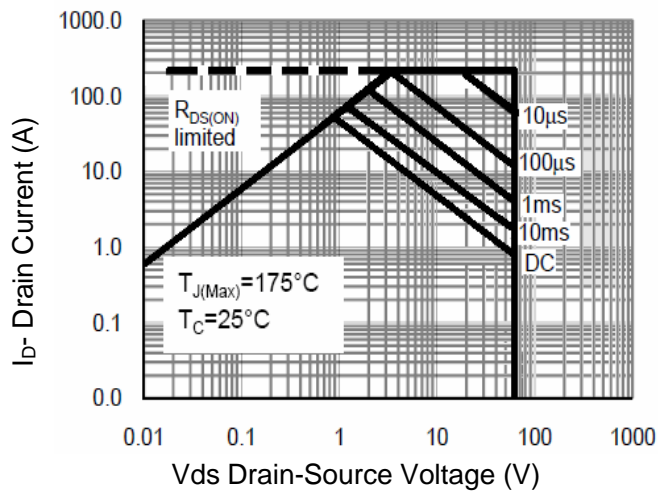


Figure 8 Safe Operation Area

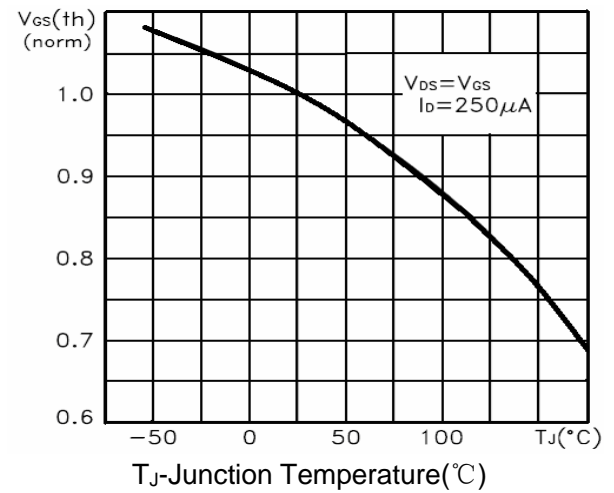


Figure 10 V_{GS(th)} vs Junction Temperature

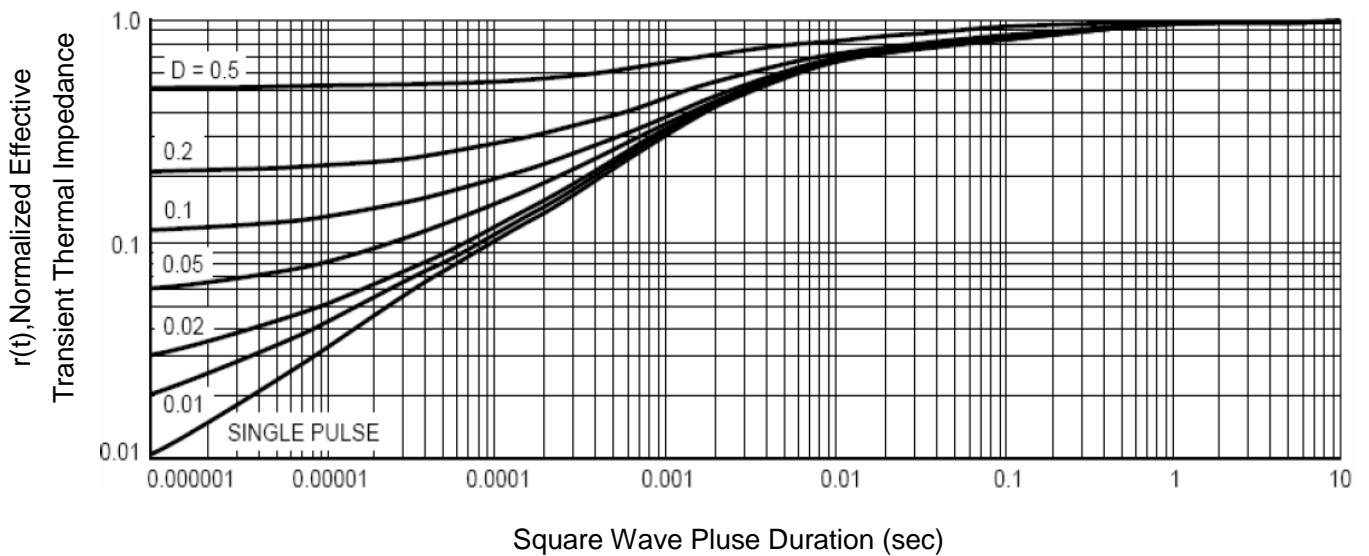
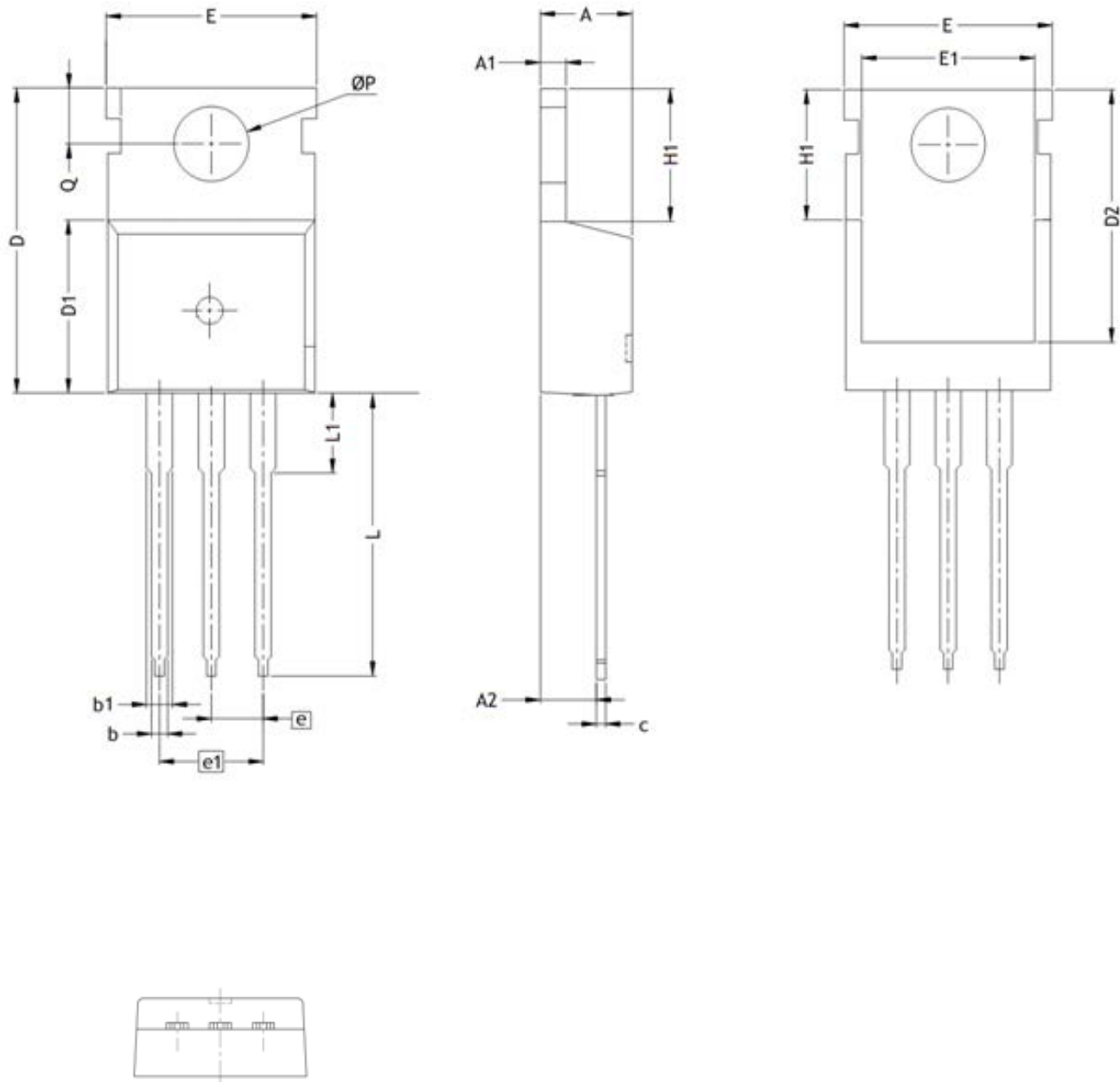


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220 Package Information


UNIT: mm

SYMBOLS	A	A1	A2	b	b1	c	D	D1	D2	E	E1	e
MIN	4.25	1.25	2.35	0.7	1.15	0.45	14.35	8.80	13.05	9.90	7.85	2.540
MAX	4.65	1.35	2.55	0.9	1.75	0.60	15.95	9.50	13.65	10.35	8.85	BSC
SYMBOLS	e1	H1	L	L1	Q	ØP						
MIN	5.080	6.30	12.85	2.85	2.70	3.50						
MAX	BSC	6.65	13.50	3.25	2.90	3.70						