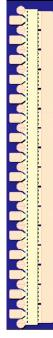


# **Applications**

- Defense & Aerospace
- Broadband Wireless



# **Product Features**

- Frequency Range: DC 18 GHz
- 50.5 dBm Nominal P<sub>SAT</sub> at 3 GHz
- 70.5% Maximum PAE
- 19.2 dB Nominal Power Gain at 3 GHz
- Bias: V<sub>D</sub> = 12 32 V, I<sub>DQ</sub> = 400 2000 mA
- Technology: TQGaN25 on SiC
- Chip Dimensions: 0.82 x 4.56 x 0.10 mm

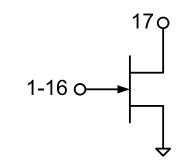
### **General Description**

The TriQuint TGF2023-2-20 is a discrete 20 mm GaN on SiC HEMT which operates from DC-18 GHz. The TGF2023-2-20 is designed using TriQuint's proven TQGaN25 production process. This process features advanced field plate techniques to optimize microwave power and efficiency at high drain bias operating conditions.

The TGF2023-2-20 typically provides 50.5 dBm of saturated output power with power gain of 19.2 dB at 3 GHz. The maximum power added efficiency is 70.5 % which makes the TGF2023-2-20 appropriate for high efficiency applications.

#### Lead-free and RoHS compliant





#### **Pad Configuration**

Pad No.	Symbol		
1-16	V <sub>G</sub> / RF IN		
17	V <sub>D</sub> / RF OUT		
Backside	Source / Ground		

Ordering Information					
Part	ECCN	Description			
TGF2023-2-20	3A001b.3.b	90 Watt GaN HEMT			

# **Absolute Maximum Ratings**

Parameter	Value
Drain to Gate Voltage (V <sub>DG</sub> )	100 V
Drain Voltage (V <sub>D</sub> )	40 V
Gate Voltage Range (V <sub>G</sub> )	–50 to 0 V
Drain Current (I <sub>D</sub> )	20 A
Gate Current (I <sub>G</sub> )	–20 to 56 mA
Power Dissipation (P <sub>D</sub> )	See graph on pg.3.
CW Input Power (P <sub>IN</sub> )	+43 dBm
Channel Temperature (T <sub>CH</sub> )	275 <i>°</i> C
Mounting Temperature (30 Sec.)	320 <i>°</i> C
Storage Temperature	–65 to 150 <i>°</i> C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

### **Recommended Operating Conditions**

Parameter	Value
Drain Voltage Range (V <sub>D</sub> )	12 – 32 V
Drain Quiescent Current (I <sub>DQ</sub> )	1 A
Drain Current Under RF Drive (I <sub>D</sub> )	6 A (Typ.)
Gate Voltage (V <sub>G</sub> )	–3.0 V (Typ.)
Channel Temperature (T <sub>CH</sub> )	225℃ (Max.)

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

# **RF Characterization – Model Optimum Power Tune**

Test conditions unless otherwise noted: T = 25  $^{\circ}$ C, Bond wires not included

Parameter	,	Typical Value					Units		
Frequency (F)	3			6				GHz	
Drain Voltage (V <sub>D</sub> )	12	12	28	28	12	12	28	28	V
Bias Current (I <sub>DQ</sub> )	400	1000	400	1000	400	1000	400	1000	mA
Output P3dB (P <sub>3dB</sub> )	46.3	46.4	50.5	50.3	46.5	46.2	50.4	50.2	dBm
PAE @ P <sub>3dB</sub> (PAE <sub>3dB</sub> )	54.9	55.6	62.1	62.1	52.8	51.6	56.0	58.1	%
Gain @ P3dB (G <sub>3dB</sub> )	15.6	16.6	16.6	19.6	9.8	11.4	10.9	13.7	dB
Parallel Resistance <sup>(1)</sup> (R <sub>p</sub> )	23.0	23.2	66.6	65.7	21.5	20.5	62.2	60.4	Ω·mm
Parallel Capacitance <sup>(1)</sup> (C <sub>p</sub> )	0.63	0.54	0.25	0.28	0.32	0.31	0.27	0.29	pF/mm
Load Reflection Coefficient $^{(2)}(\Gamma_L)$	0.58∠171°	0.57∠172°	0.19∠128°	0.21∠128°	0.60∠172°	0.61∠173°	0.36∠129°	0.38∠131°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 4  $\Omega$ .

# **RF Characterization – Model Optimum Efficiency Tune**

Test conditions unless otherwise noted:  $T = 25 \,^{\circ}C$ , Bond wires not included

Parameter	Typical Value						Units		
Frequency (F)	3			6				GHz	
Drain Voltage (V <sub>D</sub> )	12	12	28	28	12	12	28	28	V
Bias Current (I <sub>DQ</sub> )	400	1000	400	1000	400	1000	400	1000	mA
Output P3dB (P <sub>3dB</sub> )	43.9	44.2	48.9	48.9	44.1	44.0	49.4	49.1	dBm
PAE @ P <sub>3dB</sub> (PAE <sub>3dB</sub> )	70.3	69.8	70.5	69.5	66.1	64.8	63.2	64.8	%
Gain @ P3dB (G <sub>3dB</sub> )	16.0	16.2	18.5	20.7	12.1	13.6	11.9	14.6	dB
Parallel Resistance <sup>(1)</sup> (R <sub>p</sub> )	76.9	73.4	131	125	61.1	58.7	102	102	Ω·mm
Parallel Capacitance <sup>(1)</sup> (Cp)	0.52	0.51	0.42	0.40	0.48	0.46	0.38	0.38	pF/mm
Load Reflection Coefficient $^{(2)}$ ( $\Gamma$ L)	0.36∠114°	0.35∠117°	0.43∠80°	0.40∠79°	0.54∠134°	0.52∠135°	0.55∠112°	0.55∠112°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 4  $\Omega$ .

# TriQuint 🕋

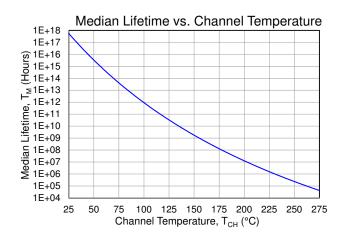
# Thermal and Reliability Information (1)

Parameter	Test Conditions	Value	Units
Thermal Resistance, $\theta_{JC}$ (No RF Drive)		1.52	°C/W
Channel Temperature, T <sub>CH</sub> (No RF Drive)	$V_D = 28 V, I_D = 2 A,$ $P_D = 56 W, Tbaseplate = 70 °C$	155	C°
Median Lifetime, $T_M$ (No RF Drive)	1	1.02 x 10^9	Hrs
Thermal Resistance, $\theta_{JC}$ (Under RF Drive)	$V_{\rm D} = 28 \text{ V}, I_{\rm D} = 5.92 \text{ A},$	1.67	°C/W
Channel Temperature, T <sub>CH</sub> (Under RF Drive)	$P_{D} = 79.3 \text{ W}, P_{OUT} = 49.6 \text{ dBm},$	202	C
Median Lifetime, T <sub>M</sub> (Under RF Drive)	Tbaseplate = 70 ℃	1.06 x 10^7	Hrs

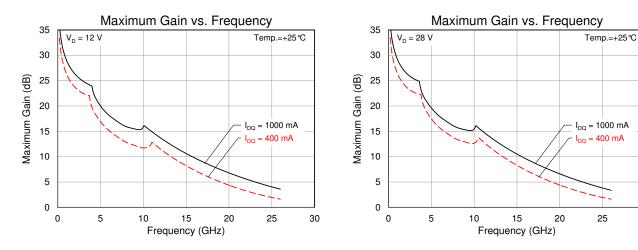
Notes:

1. Assumes eutectic attach using 1mil thick 80/20 AuSn mounted to a 10 mil CuMo Carrier Plate.

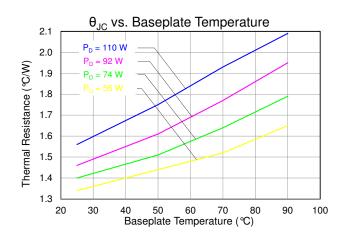
## **Median Lifetime**



# **Model Maximum Gain Performance**



# **Thermal Resistance**



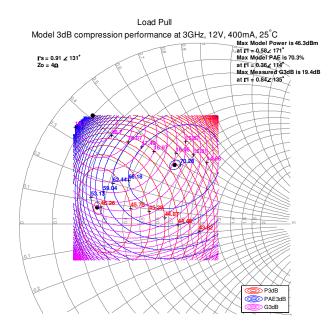
30

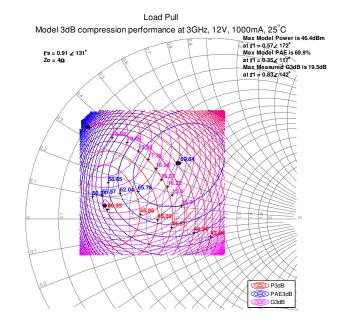
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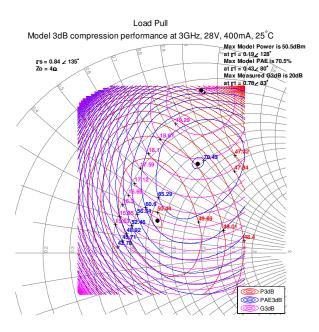


# **Model Load Pull Contours**

Simulated signal: 10% pulses. Bond wires not included.







Model 3dB compression performance at 3GHz, 28V, 1000mA, 25<sup>°</sup>C Max Model Power is 03.dBm st F1° 04.2128<sup>°</sup> Max Model PAE is 09.5% ITT 104.279<sup>°</sup> Max Model is 22.2dB at 11° 02.270<sup>°</sup> At 10° 02.270<sup>°</sup> At 10°

Load Pull

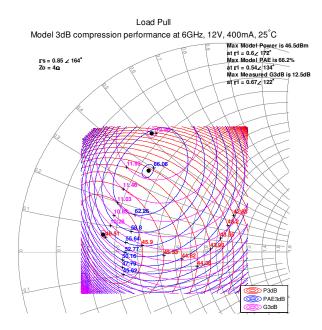
PAE3dB

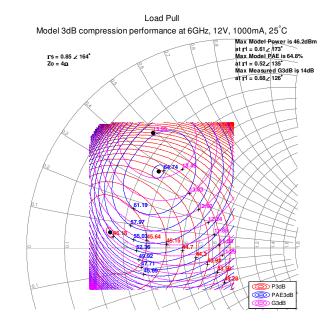
G3dB

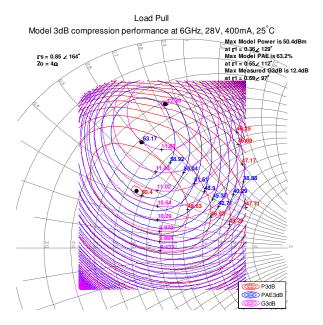


# **Model Load Pull Contours**

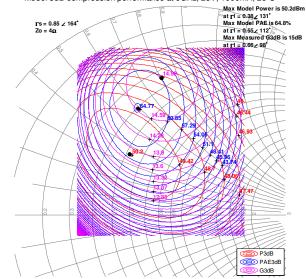
Simulated signal: 10% pulses. Bond wires not included.





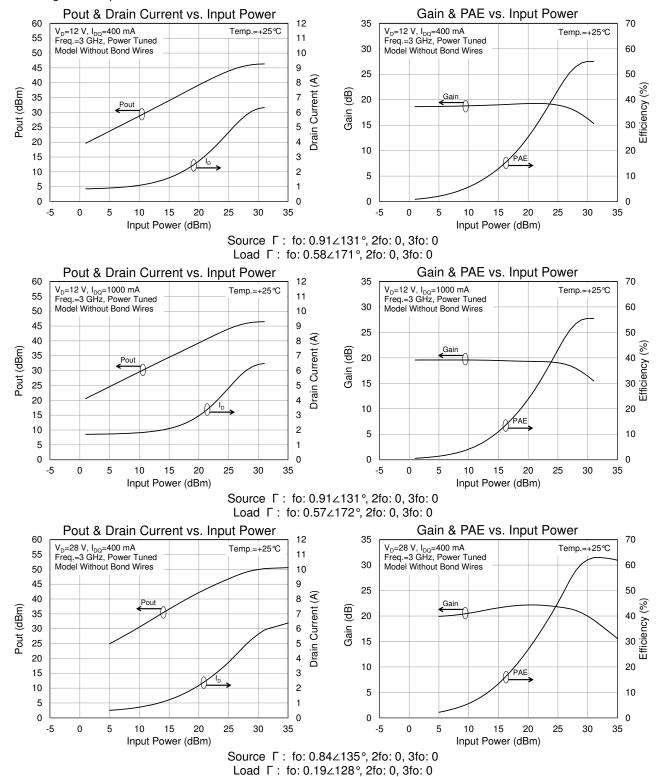


Load Pull Model 3dB compression performance at 6GHz, 28V, 1000mA, 25°C



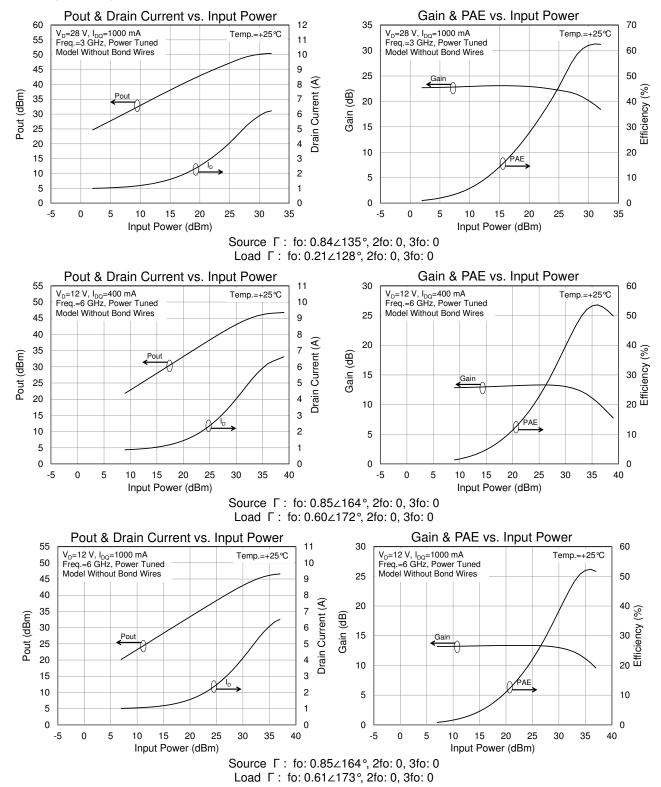


# Model Power Tuned Data





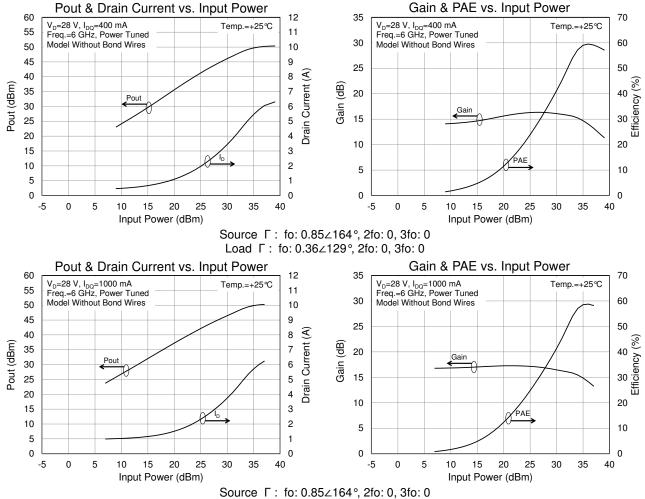
### Model Power Tuned Data





# Model Power Tuned Data

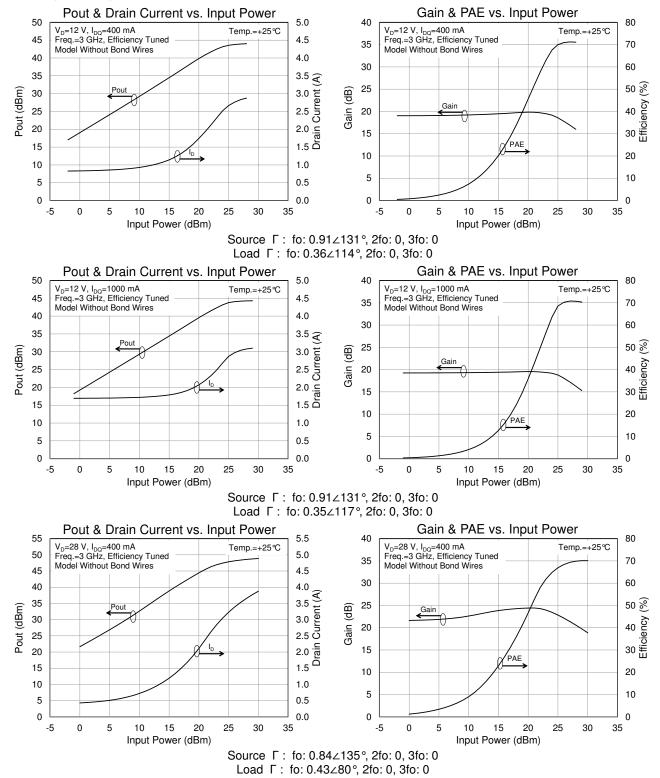
#### Simulated signal: 10% pulses.



Load Γ : fo: 0.38∠131°, 2fo: 0, 3fo: 0

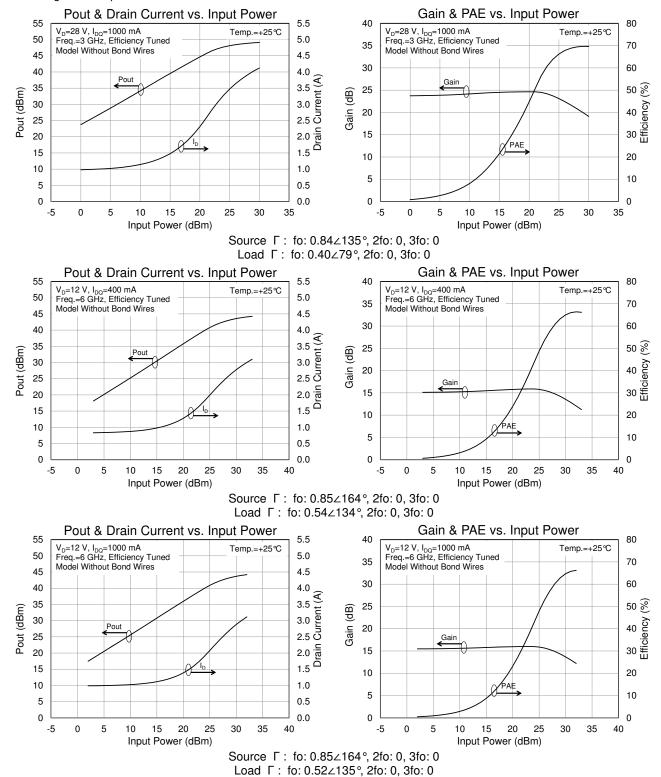


## **Model Efficiency Tuned Data**





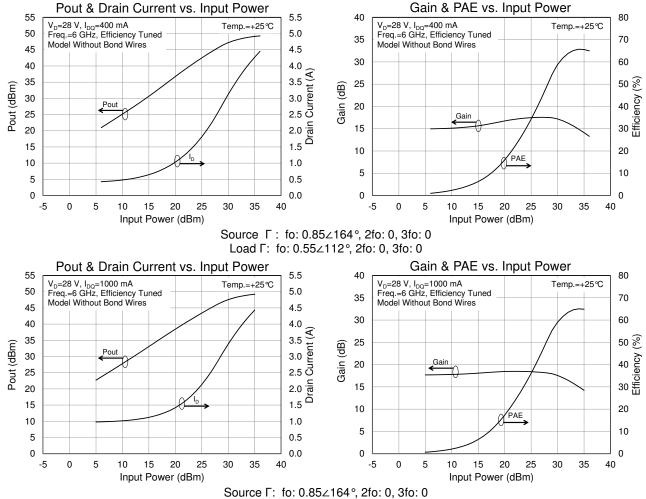
## **Model Efficiency Tuned Data**





# **Model Efficiency Tuned Data**

#### Simulated signal: 10% pulses.



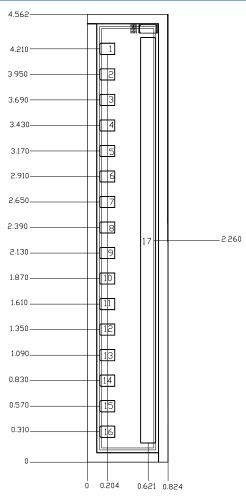
Load Γ: fo: 0.55∠112°, 2fo: 0, 3fo: 0

# TriQuint 🏠

### Model

A model is available for download from Modelithics (at <u>http://www.modelithics.com/mvp/Triquint&tab=3</u>) by approved TriQuint customers. The model is compatible with the industry's most popular design software including Agilent ADS and National Instruments/AWR applications. Once on the Modelithics web page, the user will need to register for a free license before being granted the download.

# Mechanical Drawing



# Bond Pads

Pad No.	Description	Dimensions
1-16	Gate	0.154 x 0.115
2	Drain	0.154 x 4.130
Die Backside	Source / Ground	0.824 x 4.562

#### Notes:

- 1. Units: millimeters
- 2. Thickness: 0.100 mm
- 3. Die x,y size tolerance: ± 0.050 mm



# **Assembly Notes**

Component placement and adhesive attachment assembly notes:

- Vacuum pencils and/or vacuum collets are the preferred method of pick up.
- Air bridges must be avoided during placement.
- The force impact is critical during auto placement.
- Organic attachment (i.e. epoxy) not recommended.

Reflow process assembly notes:

- Use AuSn (80/20) solder and limit exposure to temperatures above 300°C to 3-4 minutes, maximum.
- An alloy station or conveyor furnace with reducing atmosphere should be used.
- Do not use any kind of flux.
- Coefficient of thermal expansion matching is critical for long-term reliability.
- Devices must be stored in a dry nitrogen atmosphere.

Interconnect process assembly notes:

- Ball bonding is the preferred interconnect technique, except where noted on the assembly diagram.
- Force, time, and ultrasonics are critical bonding parameters.
- Aluminum wire should not be used.
- Devices with small pad sizes should be bonded with 0.0007-inch wire.

## Disclaimer

GaN/SiC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

#### **Bias-up Procedure**

- 1.  $V_G$  set to -5 V.
- 2.  $V_D$  set to 28 V.
- 3. Adjust  $V_G$  more positive until quiescent  $I_D$  is 2 A.
- 4. Apply RF signal.

# **Bias-down Procedure**

- 1. Turn off RF signal.
- 2. Turn off  $V_{\text{D}}$  and wait 1 second to allow drain capacitor dissipation.
- 3. Turn off  $V_G$ .



# **Product Compliance Information**

# **ESD Sensitivity Ratings**



Caution! ESD-Sensitive Device

ESD Rating: TBD Value: TBD Test: TBD Standard: TBD

# Solderability

Compatible with gold/tin (320°C maximum reflow temperature) soldering processes.

# **RoHs Compliance**

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>0<sub>2</sub>) Free
- PFOS Free
- SVHC Free

## **Contact Information**

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

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Email: info-products@triquint.com

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