

CMLT8099M

**SURFACE MOUNT SILICON
DUAL, MATCHED
NPN TRANSISTOR**



SOT-563 CASE

• Device is **Halogen Free** by design

APPLICATIONS:

- Small signal general purpose amplifiers

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT8099M consists of two individual, isolated 8099 NPN silicon transistors with matched $V_{BE(ON)}$ characteristics. This device is manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package.

MARKING CODE: 8CM

FEATURES:

- Transistor pair matched for $V_{BE(ON)}$

SYMBOL		UNITS
V_{CBO}	80	V
V_{CEO}	80	V
V_{EBO}	6.0	V
I_C	500	mA
P_D	350	mW
T_J, T_{stg}	-65 to +150	$^{\circ}\text{C}$
Θ_{JA}	357	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{CBO}	$V_{CB}=80\text{V}$		0.1	μA
I_{EBO}	$V_{BE}=6.0\text{V}$		0.1	μA
BV_{CBO}	$I_C=100\mu\text{A}$	80		V
BV_{CEO}	$I_C=10\text{mA}$	80		V
BV_{EBO}	$I_E=10\mu\text{A}$	6.0		V
$V_{CE(SAT)}$	$I_C=100\text{mA}, I_B=5.0\text{mA}$		0.4	V
$V_{CE(SAT)}$	$I_C=100\text{mA}, I_B=10\text{mA}$		0.3	V
$V_{BE(ON)}$	$V_{CE}=5.0\text{V}, I_C=10\text{mA}$	0.6	0.8	V
h_{FE}	$V_{CE}=5.0\text{V}, I_C=1.0\text{mA}$	100	300	
h_{FE}	$V_{CE}=5.0\text{V}, I_C=10\text{mA}$	100		
h_{FE}	$V_{CE}=5.0\text{V}, I_C=100\text{mA}$	75		
f_T	$V_{CE}=5.0\text{V}, I_C=10\text{mA}, f=100\text{MHz}$	150		MHz
C_{ob}	$V_{CB}=10\text{V}, I_E=0, f=1.0\text{MHz}$		6.0	pF
C_{ib}	$V_{BE}=0.5\text{V}, I_C=0, f=1.0\text{MHz}$		25	pF

MATCHING CHARACTERISTICS:

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$ V_{BE1}-V_{BE2} $	$V_{CE}=5.0\text{V}, I_C=1.0\mu\text{A}$		10	mV
$ V_{BE1}-V_{BE2} $	$V_{CE}=5.0\text{V}, I_C=5.0\mu\text{A}$		10	mV
$ V_{BE1}-V_{BE2} $	$V_{CE}=5.0\text{V}, I_C=10\mu\text{A}$		10	mV
$ V_{BE1}-V_{BE2} $	$V_{CE}=5.0\text{V}, I_C=100\mu\text{A}$		10	mV

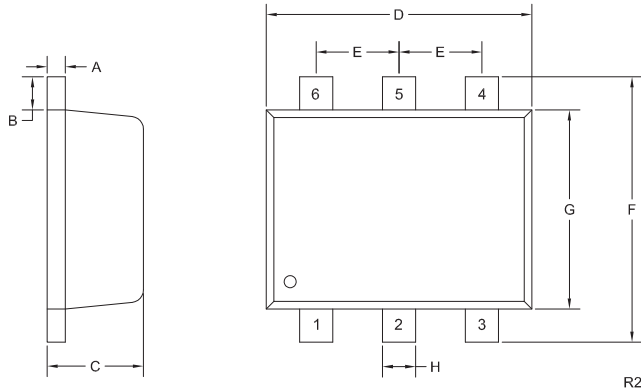
R3 (29-June 2015)

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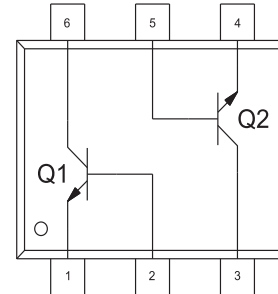
SOT-563 CASE - MECHANICAL OUTLINE



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.0027	0.007	0.07	0.18
B	0.008		0.20	
C	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R2)

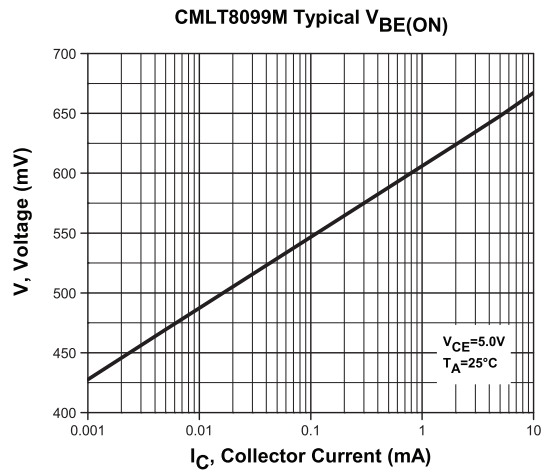
PIN CONFIGURATION



LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

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