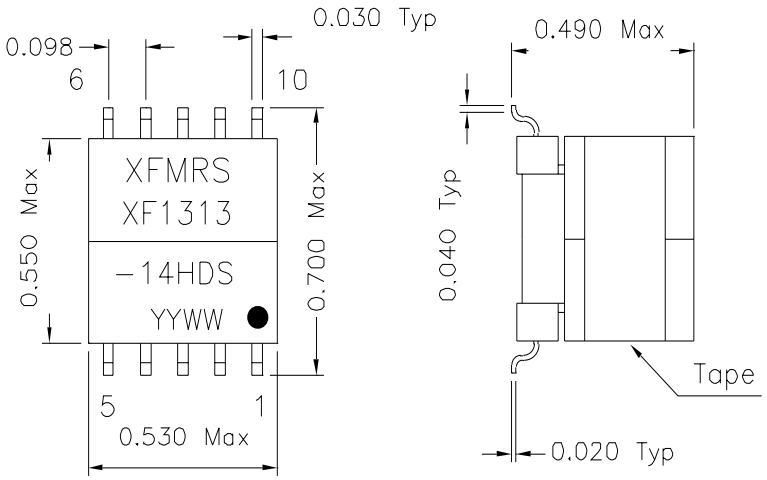
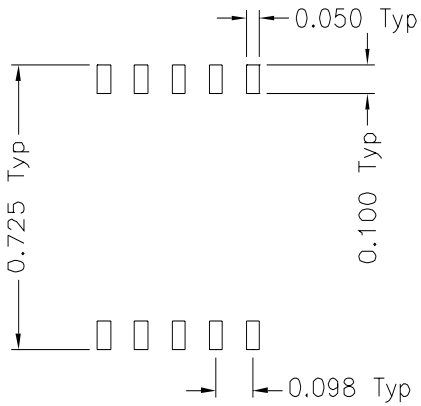
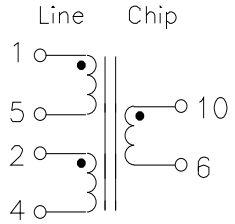
## 1. Dimensions:




Suggested PCB Layout

## 2.Schematic:




## 3.Electrical Specifications: @25°C

PIN1-4 OCL: 3.0mH ±10% @10KHz 0.1V 0/160mAdc
(CONNECT PIN2-5)

PIN1-4 LL: 20uH Max @100KHz 100mV
(CONNECT PIN 2+5, 10+6)

TURNS RATIO: (1-4):(10-6) = 2:1±1% tie(2+5)

DC Res.: Pins 1-5 1.91 Ohms±15%

DC Res.: Pins 2-4 1.91 Ohms±15%

DC Res.: Pins 10-6 1.69 Ohms±15%

THD: -70dB Max @40KHz, 1-4(tie 2+5)
+14dBm, 32 Ohm Load on 10-6, 135Ohm input

ISOLATION VOLTAGE: 2500Vrms (Chip to Line)

Return Loss: 20dB Min, 40K-200KHz, 32 Ohm Load

Freq. Resp.: ±2dB, 40K-200KHz

Insertion Loss: 0.4dB Max 299KHz, 32 Ohm Load

OPERATING TEMP: -40 TO 85°C

Note: Designed to meet the specifications of UL 1950 for supplementary insulation for a primary circuit with 250 working volts, Reinforced at 150V.

DOC. REV A/1

| | | | |
|---|---|---|---|
| XFMRS Inc | Title: HDSL TRANSFORMER | | |
| UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH | P/N: XF1313-14HDS | | REV. A |
| | DWN. | 莫玉坤 | Mar-30-00 |
| | CHK. | 李清儿 | Mar-30-00 |
| SHEET 1 OF 1 | APP. | Joe Huff | Mar-30-00 |