NTC thermistors for temperature measurement

SMD NTC thermistors,
case size 0603 (1608), automotive series

Series/Type: **B573**V5**

Date: January 2016

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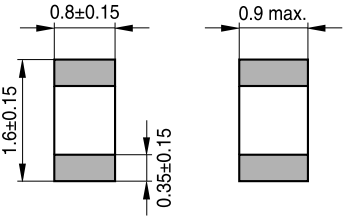
Applications
- Temperature measurement and compensation

Features
- Qualification based on AEC-Q200 Rev-D
- Multilayer SMD NTC with inner electrodes
- Nickel barrier termination
- For temperature measurement up to 150 °C
- Excellent long-term aging stability in high temperature and high humidity environment
- UL approval (E69802)

Options
- Alternative resistance ratings, resistance tolerances and B value tolerances available on request.

Delivery mode
Cardboard tape, 180-mm reel

Dimensional drawing



Dimensions in mm
Approx. weight 6 mg

General technical data

Operating temperature range		T_{op}	−40 ... 150	°C
Max. power	(at 25 °C, on PCB)	P_{25} [1)]	180	mW
Resistance tolerance		$\Delta R_R/R_R$	±1, ±3, ±5	%
Rated temperature		T_R	25	°C
Dissipation factor	(on PCB)	δ_{th} [1)]	approx. 3	mW/K
Thermal cooling time constant	(on PCB)	τ_c [1)]	approx. 4	s
Heat capacity		C_{th} [1)]	approx. 12	mJ/K

1) Depends on mounting situation

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Electrical specification and ordering codes

R_{25} Ω	$\Delta R_R/R_R$ %	No. of R/T characteristic	$B_{25/50}$ K	$B_{25/85}$ K	$B_{25/100}$ K	Ordering code
10 k	±1	8509	3380	3435	3455 ±1%	B57332V5103F360
10 k	±3, ±5	8509	3380	3435	3455 ±1%	B57332V5103+360
10 k	±3, ±5	8500	3590	3635	3650 ±3%	B57342V5103+060
10 k	±3, ±5	8502	3940	3980	4000 ±3%	B57351V5103+060
10 k	±3, ±5	8507	4386	4455	4480 ±3%	B57352V5103+060
22 k	±3, ±5	8502	3940	3980	4000 ±3%	B57351V5223+060
22 k	±3, ±5	8507	4386	4455	4480 ±3%	B57352V5223+060
47 k	±3, ±5	8507	4386	4455	4480 ±3%	B57352V5473+060
100 k	±1	8507	4386	4455	4480 ±1%	B57352V5104F360
100 k	±3, ±5	8507	4386	4455	4480 ±1%	B57352V5104+360
100 k	±3, ±5	8507	4386	4455	4480 ±3%	B57352V5104+060

+ = Resistance tolerance

F = ±1% (only for type B57332V5103F360 and B57352V5104F360)

H = ±3%

J = ±5%

Reliability data

Tests of SMD NTC thermistors are based on AEC-Q200 Rev-D. The parts are mounted on standardized PCB.

Test	Standard	Test conditions	$\Delta R_{25}/R_{25}$ (typical)	Remarks
Pre- and post-stress electrical test		Resistance at: 25 °C and 100 °C		
High temperature exposure (storage)	MIL-STD-202, method 108	Test temperature: 150 °C Duration: 1000 h Unpowered	< 5%[1)]	
Temperature cycling	JESD22, method JA-104	Lower test temperature: −40 °C Upper test temperature: 150 °C Number of cycles: 1000 Transfer time: < 10 s Dwell time: 15 min Air − Air	< 5%[2)]	
Biased humidity	MIL-STD-202, method 103	Test temperature: 85 °C Rel. humidity of air: 85% Duration: 1000 h Test voltage: V_{NTC} = 0.3 V DC	< 5%[2)]	
Operational life	MIL-STD-202, method 108	Test temperature: 150 °C P_{max} = 0.35 mW Duration: 1000 h	< 5%[2)]	
External visual	MIL-STD-883E, method 2009	Visual inspection		
Physical dimensions	JESD22, method JB-100	Measured with calibers		Within the specified values
Resistance to solvents	MIL-STD-202, method 215	Not applicable for SMD thermistors (component has no marking, color coding or coating)		
Mechanical shock	MIL-STD-202, method 213	Peak value: 1500 *g* Half sine Condition F	< 5%[1)]	
Vibration	MIL-STD-202, method 204	Acceleration: 5 *g* Sweep time: 20 min Frequency range: 10 ... 2000 Hz 3 × 12 cycles	< 5%[1)]	

1) Except B57332V5103+360 $\Delta R_{25}/R_{25}$ (typical) < 1%
2) Except B57332V5103+360 $\Delta R_{25}/R_{25}$ (typical) < 3%

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Test	Standard	Test conditions	$\Delta R_{25}/R_{25}$ (typical)	Remarks
Resistance to soldering heat	MIL-STD-202, method 210	Dip: 260 °C; 10 s 1 heat cycle	< 3%[3]	
ESD	AEC-Q200-002, method -002	Discharge capacitance: 150 pF Discharge resistance: 2 kΩ Charging voltage: 6 kV Contact discharge 2 pulses in each polarity	< 5%[4]	
Solderability	J-STD-002	a) Dip: 235 °C; 5 s: aging 4 h @ 155 °C b) Dip: 215 °C; 5 s: steam aging 8 h @ 92 °C c) Dip: 260 °C; 7 s: steam aging 8 h @ 92 °C		95% of termination wetted
Electrical characterization		R(25 °C), R(100 °C), B(25/100)		Within the specified values
Flammability	UL-94 V-0 or V-1	Not applicable for SMD thermistors (component is not coated or encapsulated with plastic materials)		
Board flex	AEC-Q200-005, method -005	Max. bending: 2 mm Duration @ max. bending: 60 s	< 5%[5]	
Terminal strength	AEC-Q200-006, method -006	Max. F: 10 N	< 5%[5]	
Resistance drift after soldering		Reflow soldering profile Wave soldering profile	< 1%	

3) Except B57332V5103+360 $\Delta R_{25}/R_{25}$ (typical) < 1%
4) Except B57332V5103+360 $\Delta R_{25}/R_{25}$ (typical) < 3%
5) Except B57332V5103+360 $\Delta R_{25}/R_{25}$ (typical) < 2%

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R/T characteristics

R/T No.	8500		8502		8507	
T (°C)	$B_{25/100}$ = 3650 K		$B_{25/100}$ = 4000 K		$B_{25/100}$ = 4480 K	
	R_T/R_{25}	α (%/K)	R_T/R_{25}	α (%/K)	R_T/R_{25}	α (%/K)
−55.0	63.917	6.8	96.158	7.4	142.71	7.9
−50.0	45.889	6.5	66.892	7.1	96.913	7.6
−45.0	33.344	6.3	47.127	6.9	66.637	7.4
−40.0	24.504	6.1	33.606	6.6	46.366	7.1
−35.0	18.201	5.8	24.243	6.4	32.629	6.9
−30.0	13.657	5.6	17.681	6.2	23.213	6.7
−25.0	10.347	5.5	13.032	6.0	16.686	6.5
−20.0	7.9114	5.3	9.702	5.8	12.115	6.3
−15.0	6.1019	5.1	7.2923	5.6	8.8803	6.1
−10.0	4.7454	4.9	5.5314	5.4	6.5692	5.9
−5.0	3.7198	4.8	4.2325	5.3	4.9025	5.8
0.0	2.938	4.6	3.2657	5.1	3.6896	5.6
5.0	2.3372	4.5	2.54	4.9	2.7994	5.4
10.0	1.8722	4.4	1.9907	4.8	2.1406	5.3
15.0	1.5096	4.2	1.5716	4.7	1.6492	5.1
20.0	1.2249	4.1	1.2494	4.5	1.2798	5.0
25.0	1.0000	4.0	1.0000	4.4	1.0000	4.9
30.0	0.82111	3.9	0.80552	4.3	0.78663	4.7
35.0	0.67798	3.8	0.65288	4.1	0.62277	4.6
40.0	0.56279	3.7	0.53229	4.0	0.4961	4.5
45.0	0.46958	3.6	0.43645	3.9	0.39757	4.4
50.0	0.39374	3.5	0.35981	3.8	0.32044	4.3
55.0	0.33171	3.4	0.29819	3.7	0.2597	4.1
60.0	0.28073	3.3	0.24837	3.6	0.21161	4.0
65.0	0.23863	3.2	0.20787	3.5	0.17331	3.9
70.0	0.2037	3.1	0.17479	3.4	0.14265	3.8
75.0	0.17459	3.0	0.14763	3.3	0.11799	3.8
80.0	0.15022	3.0	0.12523	3.2	0.098035	3.7
85.0	0.12975	2.9	0.10667	3.2	0.081823	3.6
90.0	0.11247	2.8	0.091227	3.1	0.068589	3.5
95.0	0.097838	2.8	0.078319	3.0	0.057735	3.4
100.0	0.085396	2.7	0.067488	2.9	0.048796	3.3
105.0	0.074781	2.6	0.058363	2.9	0.041403	3.2
110.0	0.065691	2.6	0.050647	2.8	0.035263	3.2
115.0	0.057883	2.5	0.044098	2.7	0.030143	3.1
120.0	0.051153	2.4	0.03852	2.7	0.025858	3.0
125.0	0.045335	2.4	0.033752	2.6	0.022258	3.0
130.0	0.040289	2.3	0.029663	2.6	0.019223	2.9
135.0	0.0359	2.3	0.026146	2.5	0.016655	2.8
140.0	0.032071	2.2	0.023111	2.4	0.014476	2.8
145.0	0.028723	2.2	0.020484	2.4	0.012619	2.7
150.0	0.025786	2.1	0.018203	2.3	0.011033	2.7

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R/T characteristics

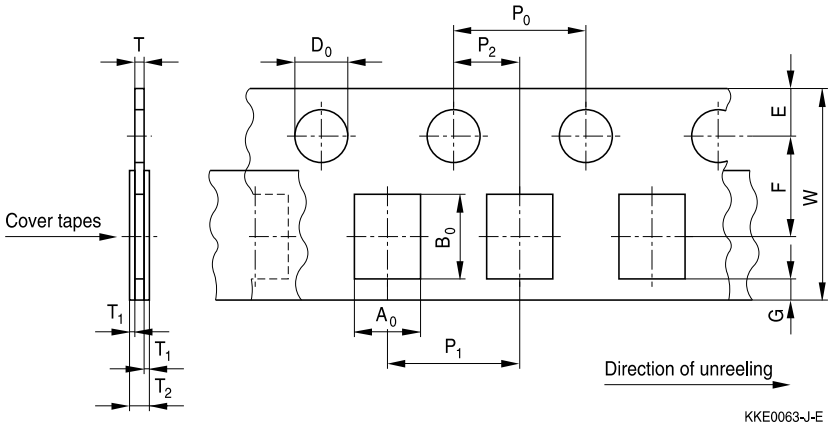
R/T No.	**8509**							
T (°C)	$B_{25/100}$ = 3455 K		T (°C)	$B_{25/100}$ = 3455 K		T (°C)	$B_{25/100}$ = 3455 K	
	R_T/R_{25}	α (%/K)		R_T/R_{25}	α (%/K)		R_T/R_{25}	α (%/K)
−55.0	44.605	5.9	15.0	1.4703	4.0	85.0	0.1451	2.8
−50.0	33.281	5.8	20.0	1.2093	3.9	90.0	0.12663	2.7
−45.0	25.044	5.6	25.0	1.0000	3.7	95.0	0.11088	2.6
−40.0	19.003	5.4	30.0	0.83113	3.6	100.0	0.097381	2.6
−35.0	14.536	5.3	35.0	0.69418	3.6	105.0	0.085788	2.5
−30.0	11.206	5.1	40.0	0.58255	3.5	110.0	0.075795	2.4
−25.0	8.7041	5.0	45.0	0.49112	3.4	115.0	0.067155	2.4
−20.0	6.8104	4.8	50.0	0.41587	3.3	120.0	0.059663	2.3
−15.0	5.3665	4.7	55.0	0.35365	3.2	125.0	0.053146	2.3
−10.0	4.2576	4.6	60.0	0.30197	3.1	130.0	0.047463	2.2
−5.0	3.4001	4.4	65.0	0.25888	3.0	135.0	0.042493	2.2
0.0	2.7326	4.3	70.0	0.22278	3.0	140.0	0.038134	2.1
5.0	2.2096	4.2	75.0	0.19243	2.9	145.0	0.034302	2.1
10.0	1.7973	4.1	80.0	0.16681	2.8	150.0	0.030925	2.1

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Taping and packing

1 Taping of SMD NTC thermistors

1.1 Cardboard tape for case size 0402 and 0603 (taping to IEC 60286-3)

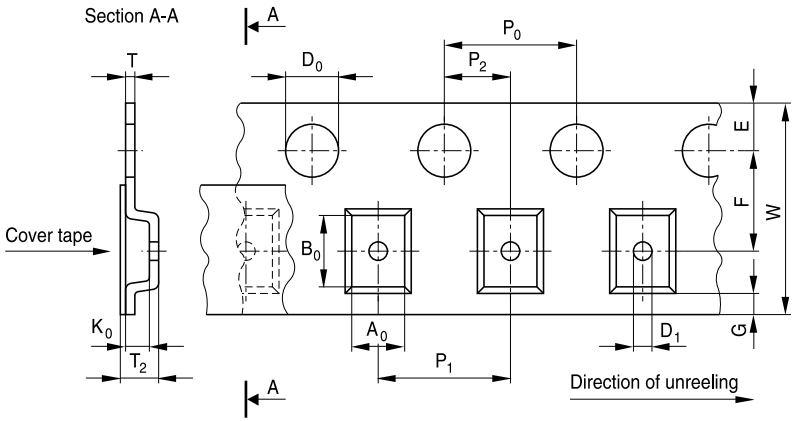


Dimensions (mm)

	Case size 0402 (8-mm tape)	Case size 0603 (8-mm tape)	Tolerance
$A_0 \times B_0$	0.60 × 1.15	0.95 × 1.80	±0.2
T_2	0.70	1.10	
T	0.60	0.95	max.
D_0	1.50	1.50	±0.10
P_0	4.00	4.00	±0.10[1)]
P_2	2.00	2.00	±0.05
P_1	2.00	4.00	±0.10
W	8.00	8.00	±0.30
E	1.75	1.75	±0.10
F	3.50	3.50	±0.05
G	0.75	0.75	min.

1) ≤0.2 mm over 10 sprocket holes.

1.2 Blister tape for case size 0805 and 1206 (taping to IEC 60286-3)

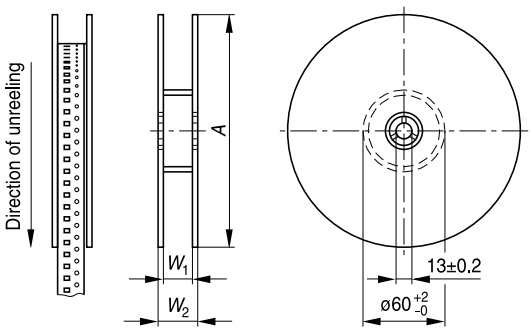


Dimensions (mm)

	Case size 0805 (8-mm tape)	Case size 1206 (8-mm tape)	Tolerance
$A_0 \times B_0$	1.50 × 2.30	1.80 × 3.40	±0.2
K_0	1.40	1.40	max.
T_2	2.5	2.5	max.
D_0	1.50	1.50	+0.10/–0
D_1	0.30	0.30	min.
P_0	4.00	4.00	±0.10[2)]
P_2	2.00	2.00	±0.05
P_1	4.00	4.00	±0.10
W	8.00	8.00	±0.30
E	1.75	1.75	±0.10
F	3.50	3.50	±0.05
G	0.75	0.75	min.

2) ≤0.2 mm over 10 sprocket holes.

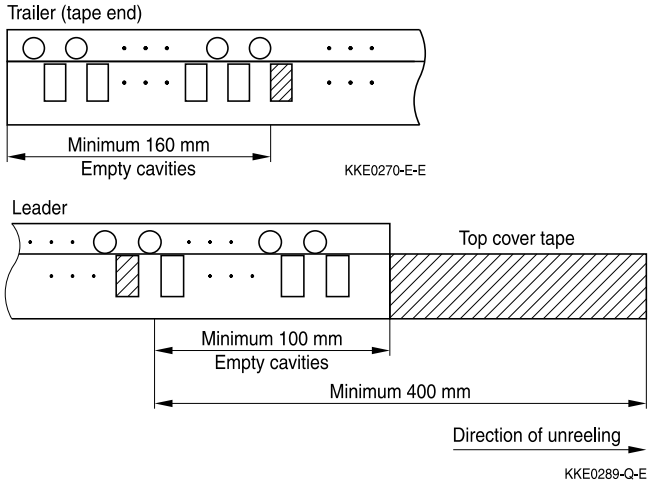
1.3 Reel packing



Dimensions in mm

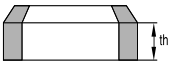
	8-mm tape	
	180-mm reel	330-mm reel
A	180 +0/−3	330 +0/−2.0
W_1	8.4 +1.5/−0	8.4 +1.5/−0
W_2	14.4 max.	14.4 max.

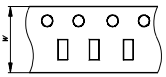
Leader, trailer



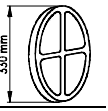
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1.4 Packing units for discrete chip and array chip









Case size inch/mm	Chip thickness th	Cardboard tape W	Blister tape W	∅ 180-mm reel pcs.	∅ 330-mm reel pcs.
0402/1005	0.5 mm	8 mm	–	10000	50000
0603/1608	0.8 mm	8 mm	8 mm	4000	16000
0805/2012	0.8 mm	–	8 mm	4000	16000
	1.2 mm	–	8 mm	3000	12000
1206/3216	0.8 mm	–	8 mm	3000	12000
	1.2 mm	–	8 mm	3000	12000

2 Packing codes

The last two digits of the complete ordering code state the packing mode:

Last two digits			
60	SMD	Cardboard tape	180-mm reel packing
62	SMD	Blister tape	180-mm reel packing
70	SMD	Cardboard tape	330-mm reel packing
72	SMD	Blister tape	330-mm reel packing

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Mounting instructions

1 Soldering

1.1 SMD NTC thermistors

SMD NTC thermistors can be provided with a nickel barrier termination or on special request with silver-palladium termination. The usage of mild, non-activated fluxes for soldering is recommended as well as a proper cleaning of the PCB.

The nickel barrier layer of the silver/nickel/tin termination (see figure 1) prevents leaching of the silver base metalization layer. This allows great flexibility in the selection of soldering parameters.

The tin prevents the nickel layer from oxidizing and thus ensures better wetting by the solder. The nickel barrier termination is suitable for all commonly-used soldering methods.

Note: SMD NTCs with AgPd termination are not approved for lead-free soldering.

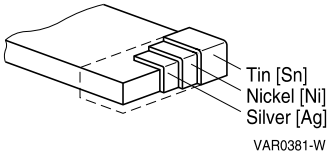


Figure 1
SMD NTC thermistors, structure of nickel barrier termination

1.1.1 Solderability (test to IEC 60068-2-58)

Preconditioning: Immersion into flux F-SW 32.
Evaluation criterion: Wetting of soldering areas ≥95%.

Solder	Bath temperature (°C)	Dwell time (s)
SnPb 60/40	215 ±3	3 ±0.3
SnAg (3.0 ... 4.0), Cu (0.5 ... 0.9)	245 ±3	3 ±0.3

1.1.2 Resistance to soldering heat (test to IEC 60068-2-58)

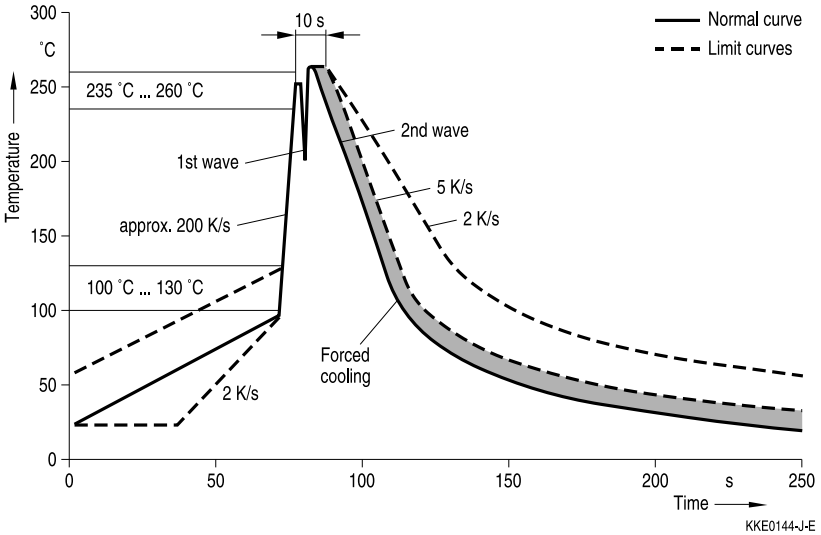
Preconditioning: Immersion into flux F-SW 32.
Evaluation criterion: Leaching of side edges ≤1/3.

Solder	Bath temperature (°C)	Dwell time (s)
SnPb 60/40	260 ±5	10 ±1
SnAg (3.0 ... 4.0), Cu (0.5 ... 0.9)	260 ±5	10 ±1

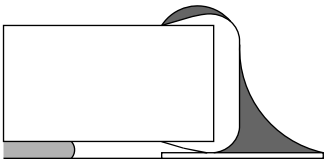
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Wave soldering

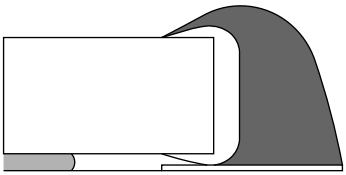
Temperature characteristic at component terminal with dual wave soldering



Solder joint profiles for silver/nickel/tin terminations



Good solder joint KKE0287-9-E

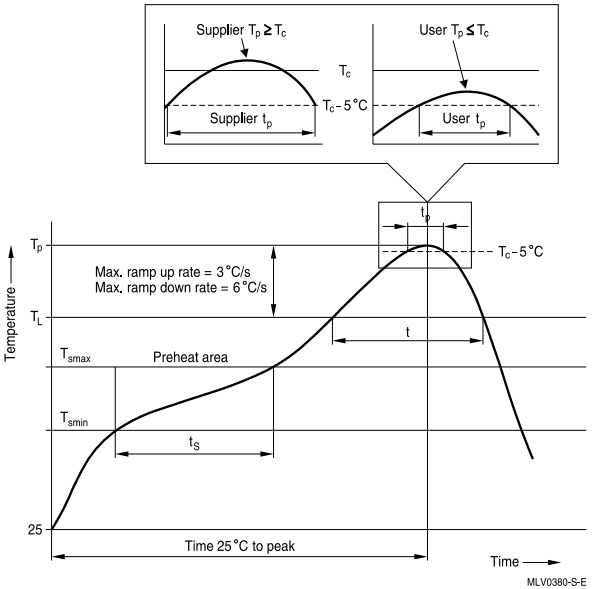


Too much solder
Pad geometry too large,
not soldered in preferred direction KKE0288-H-E

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Reflow soldering

Recommended temperature characteristic for reflow soldering following JEDEC J-STD-020D

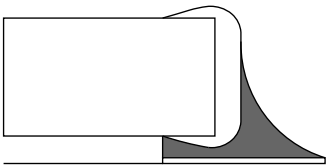


Profile feature		Sn-Pb eutectic assembly	Pb-free assembly
Preheat and soak			
- Temperature min	T_{smin}	100 °C	150 °C
- Temperature max	T_{smax}	150 °C	200 °C
- Time	t_{smin} to t_{smax}	60 ... 120 s	60 ... 180 s
Average ramp-up rate	T_{smax} to T_p	3 °C/ s max.	3 °C/ s max.
Liquidous temperature	T_L	183 °C	217 °C
Time at liquidous	t_L	60 ... 150 s	60 ... 150 s
Peak package body temperature	T_p [1)]	220 °C ... 235 °C [2)]	245 °C ... 260 °C [2)]
Time (t_P) [3)] within 5 °C of specified classification temperature (T_c)		20 s [3)]	30 s [3)]
Average ramp-down rate	T_p to T_{smax}	6 °C/ s max.	6 °C/ s max.
Time 25 °C to peak temperature		maximum 6 min	maximum 8 min

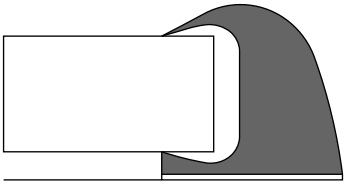
1) Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.
2) Depending on package thickness. For details please refer to JEDEC J-STD-020D.
3) Tolerance for time at peak profile temperature (t_P) is defined as a supplier minimum and a user maximum.

Note: All temperatures refer to topside of the package, measured on the package body surface. Number of reflow cycles: 3

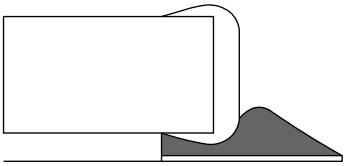
Solder joint profiles for silver/nickel/tin terminations



Good solder joint TNT0565-G-E

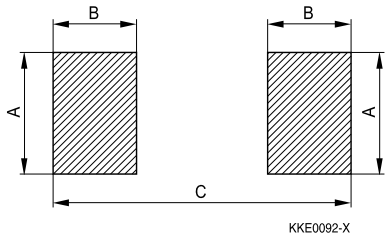


Too much solder
Pad geometry too large KKE0071-A-E



Poor wetting KKE0072-I-E

1.1.3 Recommended geometry of solder pads



KKE0092-X

Recommended maximum dimensions (mm)

Case size inch/mm	A	B	C
0402/1005	0.6	0.6	1.7
0603/1608	1.0	1.0	3.0
0805/2012	1.3	1.2	3.4
1206/3216	1.8	1.2	4.5

1.1.4 Notes

Iron soldering should be avoided, hot air methods are recommended for repair purposes.

2 Conductive adhesion

An alternative to soldering is the gluing of thermistors with conductive adhesives. The benefit of this method is that it involves no thermal stress. The adhesives used must be chemically inert.

3 Clamp contacting

Pressure contacting by means of clamps is particularly suitable for applications involving frequent switching and high turn-on powers.

4 Sealing and potting

When thermistors are sealed, potted or overmolded, there must be no mechanical stress caused by thermal expansion during the production process (curing / overmolding process) and during later operation. The upper category temperature of the thermistor must not be exceeded. Ensure that the materials used (sealing / potting compound and plastic material) are chemically neutral.

5 Cleaning

If cleaning is necessary, mild cleaning agents such as ethyl alcohol and cleaning gasoline are recommended. Cleaning agents based on water are not allowed. Ultrasonic cleaning methods are permissible.

6 Storage

In order to maintain their solderability, thermistors must be stored in a non-corrosive atmosphere. Humidity, temperature and container materials are critical factors.

Do not store SMDs where they are exposed to heat or direct sunlight. Otherwise, the packing material may be deformed or SMDs may stick together, causing problems during mounting. After opening the factory seals, such as polyvinyl-sealed packages, use the SMDs as soon as possible.

The components should be left in the original packing. Touching the metallization of unsoldered thermistors may change their soldering properties.

Storage temperature:	−25 °C up to 45 °C
Relative humidity (without condensation):	≤75% annual mean
	<95%, maximum 30 days per annum

Solder the thermistors listed in this data book after shipment from EPCOS within the time specified:

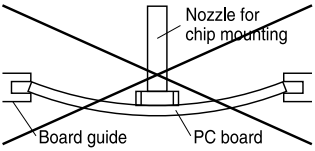
SMDs:	12 months for Ni-barrier termination
	6 months for AgPd termination

SMD

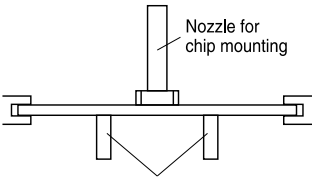
7 Placement and orientation of SMD NTC thermistors on PCB

a) Component placement

Incorrect



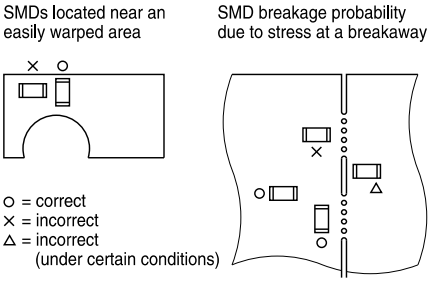
Correct



KKE0267-U-E

It is recommended that the PC board should be held by means of some adequate supporting pins such as shown left to prevent the SMDs from being damaged or cracked.

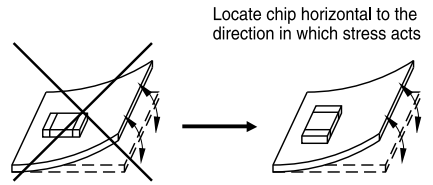
b) Cracks



KKE0268-3-E

When placing a component near an area which is apt to bend or a grid groove on the PC board, it is advisable to have both electrodes subjected to uniform stress, or to position the component's electrodes at right angles to the grid groove or bending line (see c) Component orientation).

c) Component orientation



KKE0269-B-E

Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.

SMD

Cautions and warnings

General

See "Important notes".

Storage

- Store thermistors only in original packaging. Do not open the package prior to processing.
- Storage conditions in original packaging: storage temperature −25 °C ... +45 °C, relative humidity ≤75% annual mean, <95% maximum 30 days per annum, dew precipitation is inadmissible.
- Do not store thermistors where they are exposed to heat or direct sunlight. Otherwise, the packing material may be deformed or components may stick together, causing problems during mounting.
- Avoid contamination of thermistor surface during storage, handling and processing.
- Avoid storage of thermistors in harmful environments like corrosive gases (SO_x, Cl etc).
- Use the components as soon as possible after opening the factory seals, i.e. the polyvinyl-sealed packages.
- Solder SMD NTC thermistors within the time specified after shipment from EPCOS. For SMD components with nickel barrier termination 12 months, for SMD components with AgPd termination 6 months.

Handling

- NTC thermistors must not be dropped. Chip-offs or any other damage must not be caused during handling of NTCs.
- Do not touch components with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.
- Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

Soldering

- Use resin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

SMD

Mounting

- Ensure that no thermo-mechanical stress occurs due to production processes (curing or overmolding processes) when thermistors are sealed, potted or overmolded or during their subsequent operation. The maximum temperature of the thermistor must not be exceeded. Ensure that the materials used (sealing/potting compound and plastic material) are chemically neutral.
- Electrodes/contacts must not be scratched or damaged before/during/after the mounting process.
- Contacts and housing used for assembly with the thermistor must be clean before mounting.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of the thermistor. Be sure that surrounding parts and materials can withstand the temperature.
- Avoid contamination of the thermistor surface during processing.
- The connections of sensors (e.g. cable end, wire end, plug terminal) may only be exposed to an environment with normal atmospheric conditions.
- Avoid using chemical substances as mounting aids. It must be ensured that no water or other liquids enter the NTC thermistors (e.g. through plug terminals). In particular, water based substances (e.g. soap suds) must not be used as mounting aids for sensors.

Operation

- Use thermistors only within the specified operating temperature range.
- Use thermistors only within the specified power range.
- Environmental conditions must not harm the thermistors. Only use the thermistors under normal atmospheric conditions or within the specified conditions.
- Contact of NTC thermistors with any liquids and solvents should be prevented. It must be ensured that no water enters the NTC thermistors (e.g. through plug terminals). For measurement purposes (checking the specified resistance vs. temperature), the component must not be immersed in water but in suitable liquids (e.g. perfluoropolyethers such as Galden).
- Avoid dewing and condensation unless thermistor is specified for these conditions.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by malfunction.

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.

Symbols and terms

Symbol	English	German
A	Area	Fläche
B	B value	B-Wert
$B_{25/100}$	B value determined by resistance measurement at 25 °C and 100 °C	B-Wert, ermittelt durch Widerstands-messungen bei 25 °C und 100 °C
C_{th}	Heat capacitance	Wärmekapazität
I	Current	Strom
N	Number (integer)	Anzahl (ganzzahliger Wert)
P_{25}	Maximum power at 25 °C	Maximale Leistung bei 25 °C
P_{diss}	Power dissipation	Verlustleistung
P_{el}	Electrical power	Elektrische Leistung
P_{max}	Maximum power within stated temperature range	Maximale Leistung im angegebenenTemperaturbereich
$\Delta R_B/R_B$	Resistance tolerance caused by spread of B value	Widerstandstoleranz, die durch die Streuung des B-Wertes verursacht wird
R_{ins}	Insulation resistance	Isolationswiderstand
R_P	Parallel resistance	Parallelwiderstand
R_R	Rated resistance	Nennwiderstand
$\Delta R_R/R_R$	Resistance tolerance	Widerstandstoleranz
R_S	Series resistance	Serienwiderstand
R_T	Resistance at temperature T (e.g. R_{25} = resistance at 25 °C)	Widerstand bei Temperatur T (z.B. R_{25} = Widerstand bei 25 °C)
T	Temperature	Temperatur
ΔT	Temperature tolerance	Temperaturtoleranz
t	Time	Zeit
T_A	Ambient temperature	Umgebungstemperatur
T_{max}	Upper category temperature	Obere Grenztemperatur (Kategorietemperatur)
T_{min}	Lower category temperature	Untere Grenztemperatur (Kategorietemperatur)
T_{op}	Operating temperature	Betriebstemperatur
T_R	Rated temperature	Nenntemperatur
T_{surf}	Surface temperature	Oberflächentemperatur
V	Voltage	Spannung
V_{ins}	Insulation test voltage	Isolationsprüfspannung
V_{op}	Operating voltage	Betriebsspannung
V_{test}	Test voltage	Prüfspannung

SMD

Symbol	English	German
α	Temperature coefficient	Temperaturkoeffizient
Δ	Tolerance, change	Toleranz, Änderung
δ_{th}	Dissipation factor	Wärmeleitwert
τ_c	Thermal cooling time constant	Thermische Abkühlzeitkonstante
τ_a	Thermal time constant	Thermische Zeitkonstante

Abbreviations / Notes

Symbol	English	German
SMD	Surface-mounted devices	Oberflächenmontierbares Bauelement
*	To be replaced by a number in ordering codes, type designations etc.	Platzhalter für Zahl im Bestellnummern-code oder für die Typenbezeichnung.
+	To be replaced by a letter.	Platzhalter für einen Buchstaben.
	All dimensions are given in mm.	Alle Maße sind in mm angegeben.
	The commas used in numerical values denote decimal points.	Verwendete Kommas in Zahlenwerten bezeichnen Dezimalpunkte.

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
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