SPS-93100WG / SPS-93100BWG / SPS-93100AWG (RoHS Compliant)

3.3V / 1550 nm / 2.5 Gbps Digital Diagnostic SFP LC SINGLE-MODE TRANSCEIVER

FEATURES

- Hot-Pluggable SFP Footprint LC Optical Transceiver
- Small Form-Factor Pluggable (SFP) MSA compatible
- Compliant with SONET OC-48 ELR / SDH STM-16
- Compliant with Fibre Channel 1x/2x SM-LC-L FC-PI
- Compliant with IEEE 802.3z 1000BASE
- SFF-8472 Digital Diagnostic Function
- 1550 nm DFB LD Transmitter
- APD High Sensitivity Receiver
- 28 dB Power Budget at Least
- Distance up to 100 km
- AC/AC Coupling according to MSA
- Single +3.3 V Power Supply
- RoHS Compliant
- 0 to 70°C Operation: SPS-93100WG
- -10 to 85°C Operation: SPS-93100BWG
- -40 to 85°C Operation: SPS-93100AWG
- Class 1 Laser International Safety Standard IEC-60825 Compliant

APPLICATIONS

- ATM Switches and Routers
- SONET / SDH Switch Infrastructure
- XDSL Applications
- Metro Edge Switching

DESCRIPTION

The SPS-93100WG series single mode transceivers is small form factor pluggable module for bi-directional serial optical data communications such as SONET OC-48 / SDH STM-16, Gigabit Ethernet 1000BASE-LX and Fibre Channel 1x/2x SM-LC-L FC-PI. It is with the SFP 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I^2C. This module is designed for single mode fiber and operates at a nominal wavelength of 1550 nm. A guaranteed minimum optical link budget of 28 dB is offered which can correspond to a link distance of over 100 km (assuming worst case fiber loss of 0.22 dB/km). The transmitter section uses a multiple quantum well 1550 nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	SONET /SDH	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
SPS-93100WG	2.488	ELR	100	1550 DFB	LC SFP with DMI	0 to 70	5 to 0	-28	Yes
SPS-93100BWG	2.488	ELR	100	1550 DFB	LC SFP with DMI	-10 to 85	5 to 0	-28	Yes
SPS-93100AWG	2.488	ELR	100	1550 DFB	LC SFP with DMI	-40 to 85	5 to 0	-28	Yes

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Case Temperature	Topr	0 -10 -40	70 85 85	°C	SPS-93100WG SPS-93100BWG SPS-93100AWG
Power Supply Voltage	Vcc	-0.5	3.6	V	

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Operating Case Temperature	Topr	0 -10 -40		70 85 85	°C / SPS-93100WG °C / SPS-93100BWG °C / SPS-93100AWG
Power Supply Current	$I_{CC\ (TX+RX)}$		230	300	mA
Data Rate		622	2488	2670	Mb/s / 1

1. OC-48, Gigabit Ethernet and 1x/2x Fibre Channel compliant.

OPTOWAY TECHNOLOGY INC. No.38, Kuang Fu S. Road, Hu Kou, Hsin Chu Industrial Park, Hsin Chu, Taiwan 303
Tel: 886-3-5979798 Fax: 886-3-5979737
E-mail: sales@optoway.com.tw http: // www.optoway.com.tw

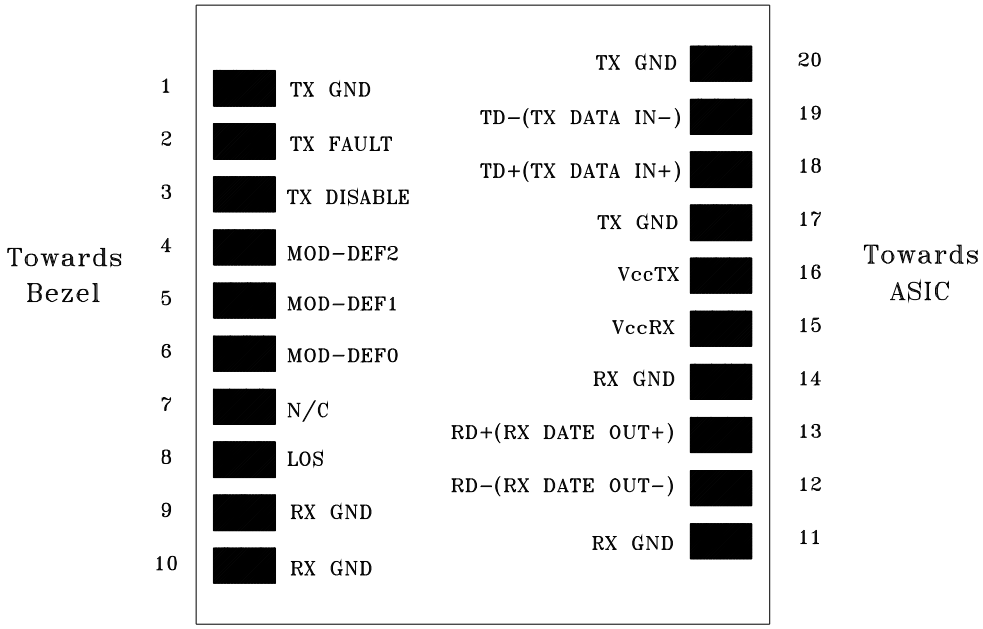
Transmitter Specifications (0^oC < Topr < 70^oC, 3.13V < Vcc < 3.47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Optical Transmit Power	Po	0	---	5	dBm	1
Output Center Wavelength	λ	1500		1580	nm	
Output Spectrum Width	Δλ	---	---	1	nm	-20 dB Width
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	E_R	8.2	---	---	dB	
Output Eye	Compliant with Telecordia GR-253-GORE and ITU-T Recommendation G.957					
Optical Rise Time	t_r			150	ps	20 % to 80% Values
Optical Fall Time	t_f			150	ps	20 % to 80% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Dispersion Penalty				2	dB	2
Electrical						
Data Input Current – Low	I_{IL}	-350			μA	
Data Input Current – High	I_{IH}			350	μA	
Differential Input Voltage	V_{IH} - V_{IL}	0.5		2.4	V	Peak-to-Peak
TX Disable Input Voltage – Low	$T_{DIS, L}$	0		0.5	V	3
TX Disable Input Voltage – High	$T_{DIS, H}$	2.0		Vcc	V	3
TX Disable Assert Time	T_{ASSERT}			10	μs	
TX Disable Deassert Time	$T_{DEASSERT}$			1	ms	
TX Fault Output Voltage -- Low	T_{FaultL}	0		0.5	V	4
TX Fault Output Voltage -- High	T_{FaultH}	2.0		Vcc+0.3	V	4

1. Output power is power coupled into a 9/125 μm single mode fiber.
2. Specified at 2000 ps/nm dispersion over G.652/G.654 fiber with center wavelength range of 1500 to 1580 nm.
3. There is an internal 4.7K to 10K ohm pull-up resistor to VccTX.
4. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

Receiver Specifications (0^oC < Topr < 70^oC, 3.13V < Vcc < 3.47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Sensitivity	Sens			-28	dBm	5,6
Maximum Input Power	Pin	-9			dBm	5,6
Signal Detect -- Asserted	Pa	---		-28	dBm	Transition: low to high
Signal Detect -- Deasserted	Pd	-40	---	---	dBm	Transition: high to low
Signal detect -- Hysteresis		1.0	---		dB	
Wavelength of Operation		1100	---	1600	nm	
Electrical						
Differential Output Voltage	$V_{OH} - V_{OL}$	0.6		2.0	V	
Output LOS Voltage -- Low	V_{OL}	0		0.5	V	7
Output LOS Voltage -- High	V_{OH}	2.0		Vcc+0.3	V	7

5. Measured at 2^{23}-1 PRBS at BER 1E-10.
6. Measured at 2^7-1 PRBS at BER 1E-12 for 2.125 Gb/s, 1.25Gb/s, and 1.063 Gb/s.
7. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

CONNECTION DIAGRAM



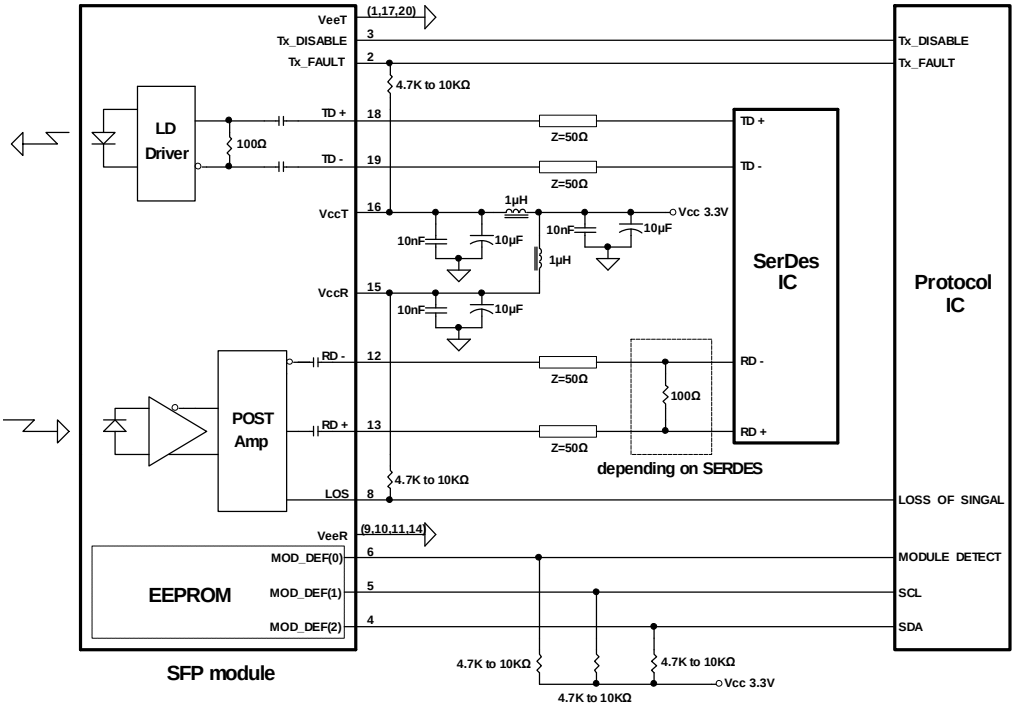
PIN	Signal Name	Description	PIN	Signal Name	Description
1	TX GND	Transmitter Ground	11	RX GND	Receiver Ground
2	TX Fault	Transmitter Fault Indication	12	RX DATA OUT-	Inverse Receiver Data Out
3	TX Disable	Transmitter Disable (Module disables on high or open)	13	RX DATA OUT+	Receiver Data Out
4	MOD-DFE2	Modulation Definition 2 – Two wires serial ID Interface	14	RX GND	Receiver Ground
5	MOD-DEF1	Modulation Definition 1 – Two wires serial ID Interface	15	Vcc RX	Receiver Power – 3.3V±5%
6	MOD-DEF0	Modulation Definition 0 – Ground in Module	16	Vcc TX	Transmitter Power – 3.3V±5%
7	N/C	Not Connected	17	TX GND	Transmitter Ground
8	LOS	Loss of Signal	18	TX DATA IN+	Transmitter Data In
9	RX GND	Receiver Ground	19	TX DATA IN-	Inverse Transmitter Data In
10	RX GND	Receiver Ground	20	TX GND	Transmitter Ground

Module Definition

Module Definition	MOD-DEF2 PIN 4	MOD-DEF1 PIN 5	MOD-DEF0 PIN 6	Interpretation by Host
4	SDA	SCL	LV-TTL Low	Serial module definition protocol

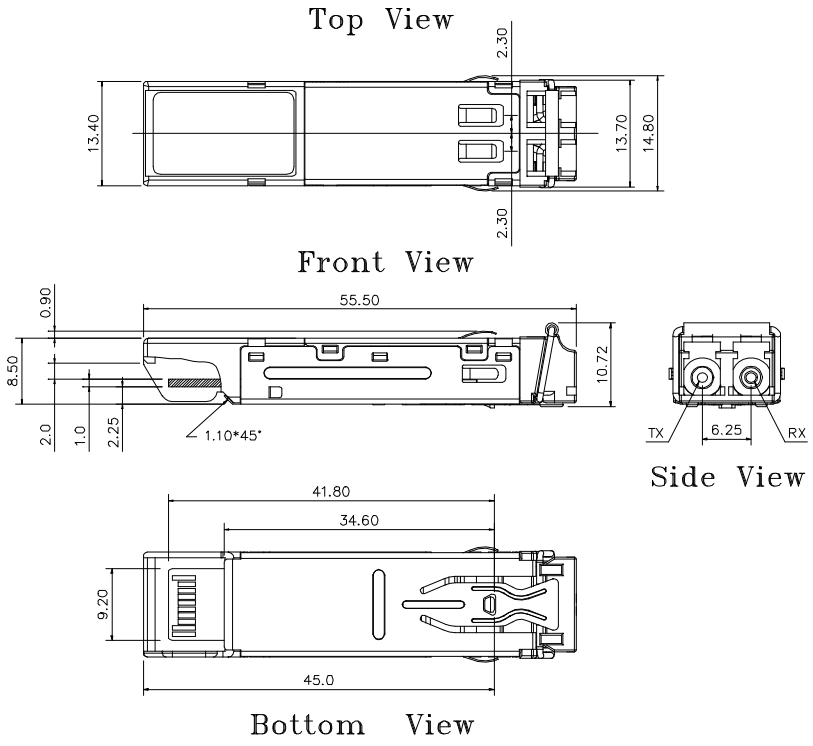
Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E^2PROM protocol of the ATMEL AT24C01A/02/04 family of components.

RECOMMENDED CIRCUIT SCHEMATIC



PACKAGE DIAGRAM

Units in mm



Note: Specifications subject to change without notice.

Revision History

Version	Subject	Release Date
1.0	Initial datasheet	2005/12/1
2.0	Revise package diagram for high port density use	2008/1/1