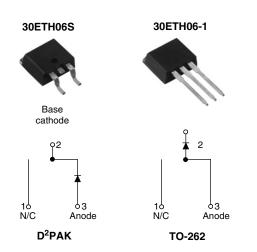


Vishay High Power Products

Hyperfast Rectifier, 30 A FRED Pt[™]



PRODUCT SUMMARY				
t _{rr} (typical)	28 ns			
I _{F(AV)}	30 A			
V _R	600 V			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 125 °C operating junction temperature
- Dual diode center tap
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 103 °C	30	А
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	200	A
Operating junction and storage temperatures	T_J,T_Stg		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage V _F	N	I _F = 30 A	-	2.0	2.6	V	
	۷F	I _F = 30 A, T _J = 150 °C	-	1.34	1.75		
Reverse leakage current I _R	$V_R = V_R$ rated	-	0.3	50			
	'R	$T_J = 150 \ ^{\circ}C, \ V_R = V_R \text{ rated}$	-	60	500	μΑ	
Junction capacitance	CT	V _R = 600 V -		33	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -			-	nH	

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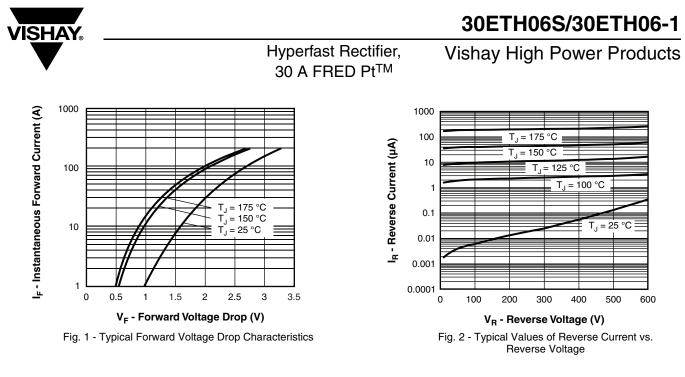
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	28	35	
Reverse recovery time t _{rr}	T _J = 25 °C		-	31	-	ns	
		T _J = 125 °C		-	77	-	
Peak recovery current I _{RRM}		T _J = 25 °C	I _F = 30 A dI _F /dt = 200 A/μs V _R = 200 V	-	3.5	-	A
	IRRM	T _J = 125 °C		-	7.7	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	65	-	nC
	T _J = 125 °C		-	345	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.7	1.1	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Maiaht			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Mandaha a danaha a		Case style D ² PAK	30ETH06S			
Marking device		Case style TO-262	30ETH06-1			



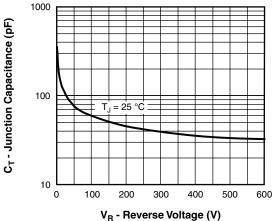


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

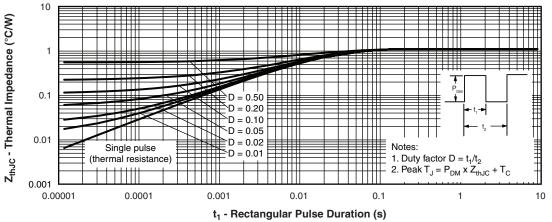
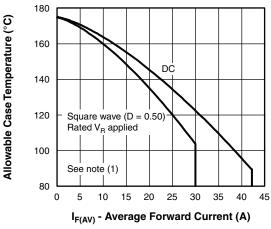


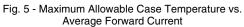
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

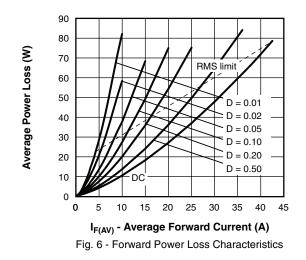
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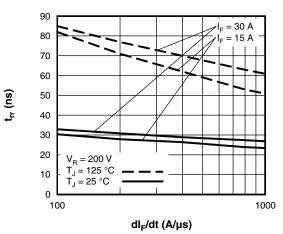






Note

 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ Forward power loss } = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \mbox{ Inverse power loss } = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = \ Rated \ V_R$



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Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

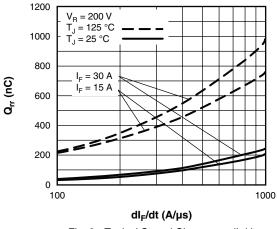


Fig. 8 - Typical Stored Charge vs. dl_F/dt



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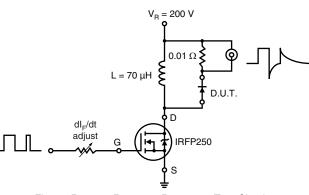


Fig. 9 - Reverse Recovery Parameter Test Circuit

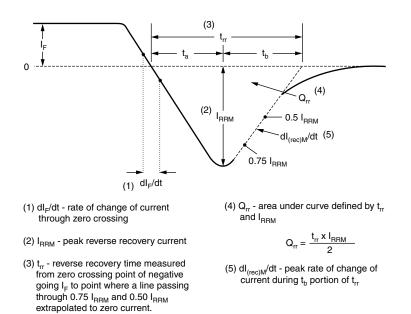
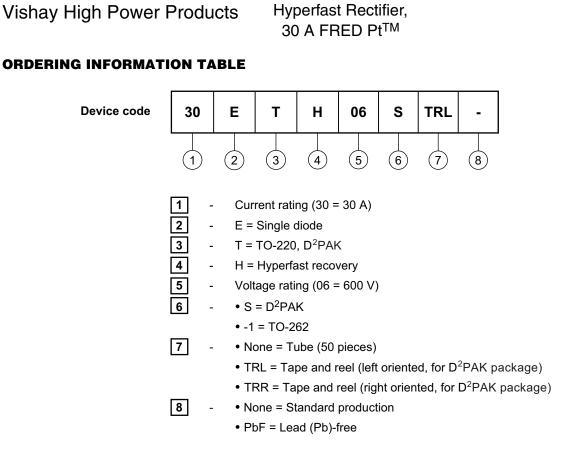


Fig. 10 - Reverse Recovery Waveform and Definitions

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LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			



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