ROHS Compliant

承 認 書

SPECIFICATION FOR APPROVA

CUSTOMER:		
CUSTOMER P/N:		
CUSTOMER PART NO:		
DESCRIPTION:	SMD INDUCTOR	
PRODUCTS NO:	BCIH11750HC-R33M	
FIRST DATE:	2019-10-22	BC REV: X1
DATE:	2019-10-22	

PURCHASER CONFIRMED		
APPROVAL BY	CHECK BY	DRAWN BY

REMARK

PROVIDER ENGINEER DEPT.		
APPROVAL BY	CHECK BY	DRAWN BY
Ouyang weijun	**Xuqiuyue**	**chenlinli**

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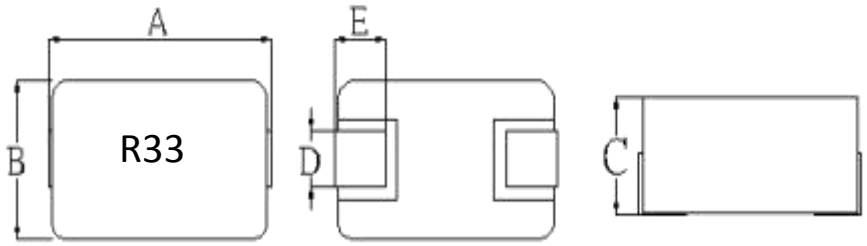
SPECIFICATION FOR APPROVAL

DATE: 2019-10-22

CUSTOMER:	CUSTOMER PART NO:	FIRST DATE: 2019-10-22
DESCRIPTION: SMD INDUCTOR	PRODUCTS NO: BCIH11750HC-R33M	BC REV: X1

1.CONFIGURATION DIMENSIONS

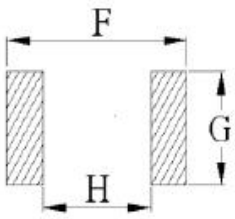


Marking：喷印黑色

DIMENSIONS（UNIT:mm）

A:	10.50	±0.8
B:	6.90	±0.5
C:	5.00	Max
D:	2.80	±0.5
E:	2.00	±0.5

2.RECOMMEND LAND PATTERN DIMENSIONS

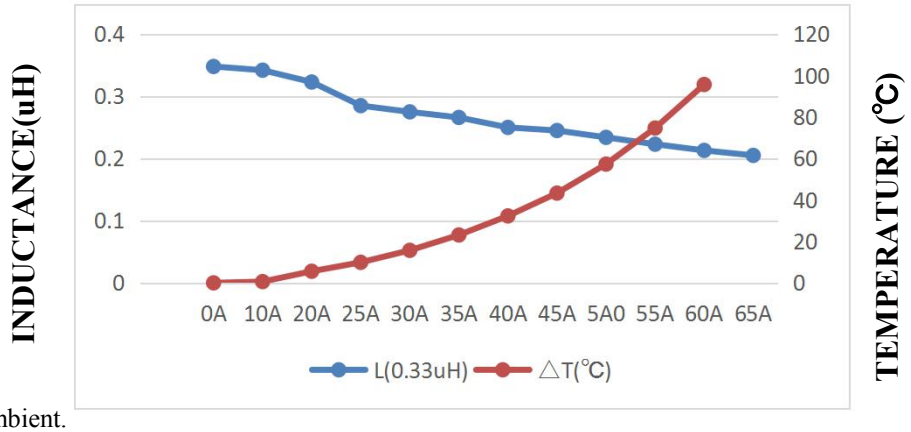


F:	12.50
G:	3.50
H:	6.50

3.ELECTRICAL CHARACTER

Part Number	Inductance (uH)	R_{dc} (mΩ)	(5) (Irms)(A) Heat Rating Current DC Amps		(6) (Isat)(A) Saturation Current DC Amps	
	100kHz/0.25v/0A.	±10%	Typical		Typical	
BCIH11750HC-R33M	0.33 ± 20%	1.05	32.0	△T≦40℃	60.0	△L≦30%

Adc	0A	10A	20A	25A	30A	35A	40A	45A	5A0	55A	60A	65A
L(0.33uH)	**0.348**	**0.342**	**0.323**	**0.285**	**0.275**	**0.266**	**0.250**	**0.245**	**0.234**	**0.223**	**0.213**	**0.205**
△T(℃)	**0.00**	**0.60**	**5.60**	**9.90**	**15.70**	**23.10**	**32.30**	**43.30**	**57.30**	**74.70**	**95.70**	



(1) Tolerance of Inductance: M=±20%.

(2) All test data is referenced to 25℃ ambient.

(3) Inductance is measured 100kHz/0.25v/0A .25℃ ambient.

(4) Operating Temperature Range -40℃ to +125℃.

(5) DC current (Irms) (A) that will cause an Approxim △T≦40℃

(6) DC current (Isat) (A) that will cause L0 to drop approximate △L≦30%

(7) The part Temperature (ambient + temp rise) should not exceed 125℃ under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature Part temperature should be verified in the end application.

※Irms : Heat Rating Current DC Amps.

※Isat : Saturation Current DC Amps.

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SPECIFICATION FOR APPROVAL

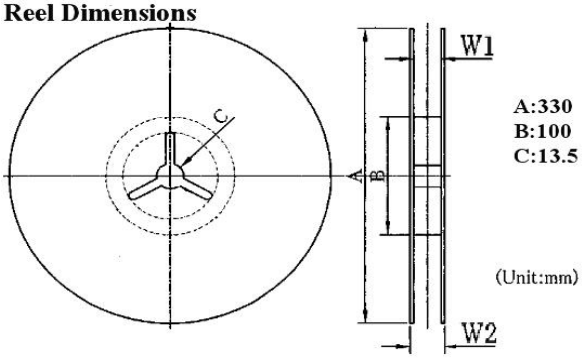
DATE: 2019-10-22

CUSTOMER:	CUSTOMER PART NO:	FIRST DATE: 2019-10-22
DESCRIPTION: SMD INDUCTOR	PRODUCTS NO: BCIH11750HC-R33M	BC REV: X1

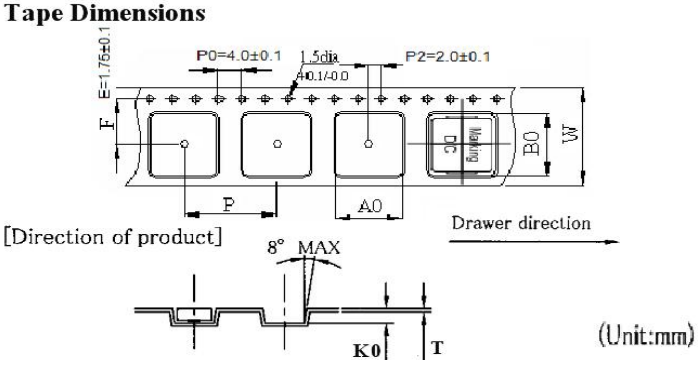
4.PACKAGING INFORMATION

(1) Tape Dimension / Packaging Quantity

Reel Dimensions



Tape Dimensions



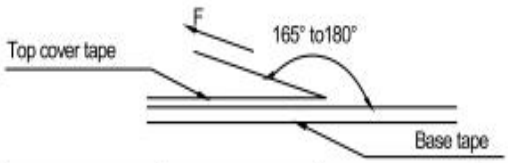
DIMENSIONS（UNIT:mm）

A: 330.0 ±2.00
B: 100.0 ±0.50
C: 13.5 ±0.50
W1: 24.80 ±0.30
W2: 29.00 ±0.50
Q`TY: 1,000 PCS

DIMENSIONS（UNIT:mm）

W: 24.00 ±0.30
A0: 7.60 ±0.10
B0: 12.00 ±0.10
K0: 5.40 ±0.10
P: 12.00 ±0.10
F: 11.50 ±0.10
T: 0.40 ±0.05

(2) Tearing Off Force



Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

The force tearing off cover 10 to 130 grams (0.1N to 1.3N) in the arrow direction under the following conditions.

●Storage conditions/Note things

(1) Storage temperature and humidity conditions :

1. Product packing with Carrier tape: +5℃~+40℃ and less than 60% RH.
2. Product alone: -20℃~+60℃ and less than 60% RH.

(2) Products should be used within 6 months.

(3) The packaging material should be kept where no chlorine or sulfur exists in the air.

(4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability

(5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.

(6) Bulk handling should ensure that abrasion and mechanical shock are minimized.

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SAMPLE ACKNOWLEDGE CHANGE RESUME

DATE: 2019-10-22

CUSTOMER:	CUSTOMER PART NO:	FIRST DATE: 2019-10-22
DESCRIPTION: SMD INDUCTOR	PRODUCTS NO: BCIH11750HC-R33M	BC REV: X1

REV	Change content		Change reason	Modify	Date
X1	00	首次	首次送样	chenlinli	2019-10-22

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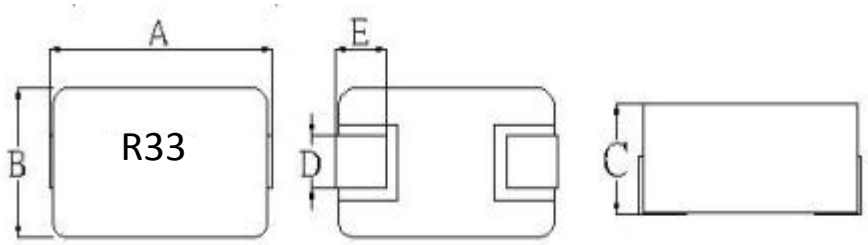
TEST DATA

DATE: 2019-10-22

CUSTOMER:	CUSTOMER PART NO:	FIRST DATE: 2019-10-22
DESCRIPTION: SMD INDUCTOR	PRODUCTS NO: BCIH11750HC-R33M	BC REV: X1

(1) TEST DATA FOR SAMPLE



LOT NO.		
DIMENSIONS（UNIT:mm）		
A	10.50	±0.8
B	6.90	±0.5
C	5.00	Max
D	2.80	±0.5
E	2.00	±0.5

TEST ITEM		SPEC	TEST CONDITION
1	Inductance (uH)	0.33 ± 20%	100kHz/0.25v/0A.
2	Rdc (mΩ)	1.05 ± 10%	
3	(6) (Isat)(A)Saturation Current DC Amps	60.00 △L≦30%	
4	(5) (Irms)(A)Heat Rating Current DC Amps	32.00 △T≦40℃	

MEAS ITEM	A	B	C	D	E			1	2	3
SUGGEST	10.50	6.90	5.00	2.80	2.00			0.33	1.05	60.00
	±0.8	±0.5	Max	±0.5	±0.5			± 20%	±5%	△L≦30%
1	10.95	7.26	4.85	2.75	2.0			0.289	1.32	73%
2	10.96	7.28	4.83	2.75	2.0			0.356	1.29	73%
3	10.94	7.25	4.82	2.75	2.0			0.298	1.31	75%
4	10.98	7.26	4.86	2.75	2.0			0.324	1.29	74%
5	10.97	7.28	4.85	2.75	2.0			0.320	1.33	75%
6	10.98	7.25	4.87	2.75	2.0			0.348	1.32	72%
7	10.96	7.27	4.86	2.75	2.0			0.291	1.28	74%
8	10.97	7.26	4.85	2.75	2.0			0.328	1.33	73%
9	10.98	7.25	4.87	2.75	2.0			0.342	1.30	72%
10	10.97	7.26	4.86	2.75	2.0			0.351	1.29	74%
11										
12										
13										
14										
15										
max	10.98	7.28	4.87					0.36	1.33	75.0%
min	10.94	7.25	4.82					0.29	1.28	72.0%
σ	0.013	0.011	0.015					0.024	0.017	0.010
X	10.97	7.26	4.85					0.32	1.31	73.5%
Cpk	12.13	11.20	3.21					1.00	4.89	1927.89

2.TEST CONDITION

TEMP. 25℃ R.H. 65%

3.TEST INSTRUMENTS

☐HP-4284A METER ☐CH-3305 METER

■HP-4285A METER ☐CD1068+CD1320 METER

☐HP-4191A METER ☐VR113+VR712 METER

☐ CH101 LCR,METER ☐WK3260B+WK3265B METER

■ VR116+VR7220 METI ☐VR562 METER

☐CH-3200 METER ■CH-502B DCR METER

☐CH-310 METER

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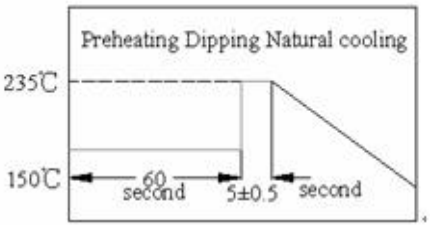
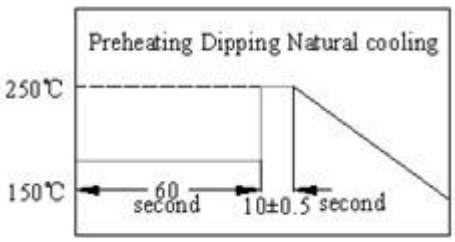
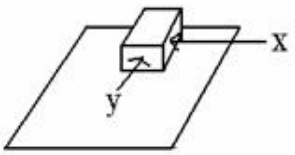
Xuqiuyue
DRAWN BY
chenlinli

■GENERAL CHARACTERISTICS page. 1	
Operation Temperature	-40℃ to +125℃ (Includes temperature when the coil is heated)
External Appearance	On visual inspection, the coil has no external defects.
Solder Ability Test	More than 90% of terminal electrode should be covered with solder. l After fluxing, component shall be dipped in a dipped in a melted. Solder:bath at 235℃±5℃ for 5 ± 0.5senonds 
Heat endurance of Solderin	1.Components should have not evidence of electrical and mechanical damage. 2.Inductance: within±10% of initial value. 3.Impedance: within±10% of initial value. ●Preheat:150±5℃ 60seconds. ●Solder temperature: 250±5℃. ●Flux: rosin. ●Dip time:10±0.5seconds. 
Terminal Strength	After soldering of X,Y withstanding at below conditions .The terminal should not Peel off. (Refer to figure at below) 5N:6 
Insulating Resistance	Over 100MΩ at 100V D.C. between coil and core.
Dielectric Strength	No dielectric breakdown at 30V D.C. for 1 minute between coil and core.
VibrationTest	Inductance deviation within +10% after vibration for 1 hour. In each of three orientations at sweep vibration(10-~55-~10HZ)with 1.5mmP-P amplitudes
Drop test	Inductance deviation within +10% after being dropped once with 981m/s2 (100G) shock Attitude upon a rubber block method shock testing machine, in three different orientations

⑤ Application Notice/Handling

1. Storage Conditions

1. Storage Conditions

To maintain the solder ability of terminal electrodes:

(1) Temperature and humidity conditions: less than 40℃ and 70% RH.

(2) Products should be used within 6 months.

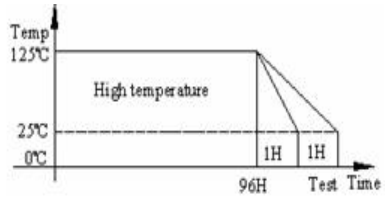
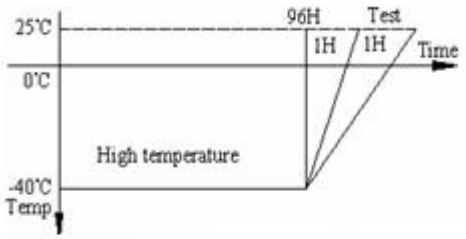
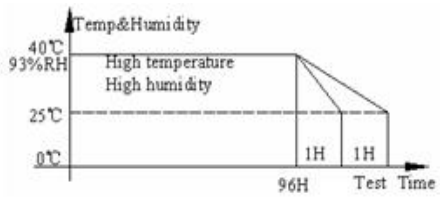
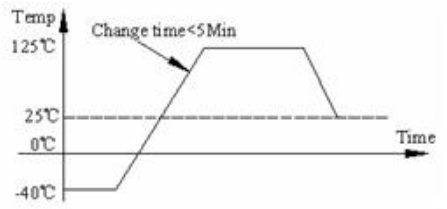
(3) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Handling

(1) Do not touch the electrodes(soldering terminals)with fingers as this may lead to deterioration of solderability.

(2) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.

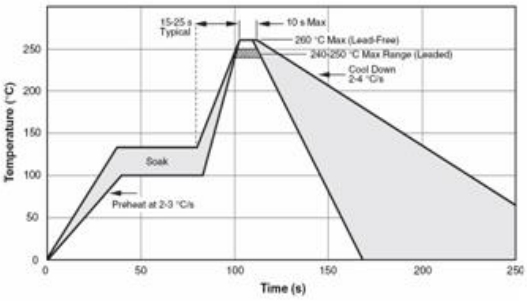
(3) Bulk handling should ensure that abrasion and mechanical shock are minimized.

■GENERAL CHARACTERISTICS		page. 2
TEST	Required Characteristics	Test Method/Condition
High Temperature StorageTest Reference documents: MIL-STD-202G Method108A	1. No case deformation or change in appearance 2. △L/L≦10% 3. △Q/Q≦30% 4. △DCR/DCR≦10%	 Temperature:125℃±2℃ Time:96±2 hours. Tested not less than 1 hour, nor more than 2 hours at room.
Low Temperature Storage Test Reference documents: IEC 68-2-1A 6.1 6.2	1. No case deformation or change in appearance 2. △L/L≦10% 3. △Q/Q≦30% 4. △DCR/DCR≦10%	 T Tested not less than 1 hour, nor more than 2 hours at room.
Humidity Test Reference documents: MIL-STD-202G Method103B	1. No case deformation or change in appearance 2. △L/L≦10% 3. △Q/Q≦30% 4. △DCR/DCR≦10%	 1. Dry oven at a temperature of 40℃±2℃ for 96hours 2. Measurements At the end of this period 3. Exposure: Temperature: 40℃±2℃. Humidity:93±2hoyrs. 4. Tested while the chamber. 5. Tested not less than 1 hour. Nor more than 2 hours at room temperature.
Thermal Shock Test Reference documents: MIL-STD-202G Method107G	1. No case deformation or change in appearance 2. △L/L≦10% 3. △Q/Q≦30% 4. △DCR/DCR≦10%	 First–40℃ for 30 Minutes, last 125℃ for 30 Minutes as 1 cycle. Go through 20 cycles.

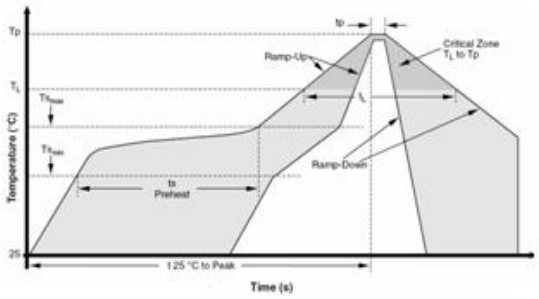
■Application Notice/Handling

(1) Temperature and humidity conditions : less than 40℃ and 70% RH.
(2) Products should be used within 6 months.
(3) The packaging material should be kept where no chlorine or sulfur exists in the air.
(4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability
(5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.
(6) Bulk handling should ensure that abrasion and mechanical shock are minimized.

TYPICAL WAVE SOLDER PROFILE FOR LEADED AND LEAD -FREE THROUGH-HOLE PACKAGES



TYPICAL IR REFLOW PROFILE FOR LEADED AND LEAD -FREE SURFACE MOUNT PACKAGES



IPC/JEDEC J-STD-020C, Figure 5-1

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average Ramp-Up Rate (Ts_{max} to Tp)	3 °C/second max.	3 °C/second max.
Preheat ± Temperature Min (Ts_{min}) ± Temperature Max (Ts_{max}) ± Time (ts_{min} to ts_{max})	 100 °C 150 °C 60-120 seconds	 150 °C 200 °C 60-180 seconds
Time maintained above: ± Temperature (T_L) ± Time (t_L)	 183 °C 60-150 seconds	 217 °C 60-150 seconds
Peak/Classification Temperature (Tp)	See Table 4.1	See Table 4.2
Time within 5 °C of actual Peak Temperature (tp)	10-30 seconds	20-40 seconds
Ramp-Down Rate	6 °C/second max.	6 °C/second max.
Time 25 °C to Peak Temperature	6 minutes max.	8 minutes max.

Table 4. Classification Reflow Profiles (per IPC/JEDEC J-STD-020C, Table 5.2)

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

Package Thickness	Volume mm^3 <350	Volume mm^3 ≥350
<2.5 mm	240 +0/-5 °C	225 +0/-5 °C
≥2.5 mm	225 +0/-5 °C	225 +0/-5 °C

Table 5. SnPb Eutectic Process – Package Peak Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.1)

Package Thickness	Volume mm^3 <350	Volume mm^3 350-2000	Volume mm^3 >2000
<1.6 mm	260 + 0 °C *	260 + 0 °C *	260 + 0 °C *
1.6 mm - 2.5 mm	260 + 0 °C *	250 + 0 °C *	245 + 0 °C *
≥2.5 mm	250 + 0 °C *	245 + 0 °C *	245 + 0 °C *
* Tolerance: Process compatibility is up to and including the stated classification temperature (this means Peak reflow temperature + 0 °C. For example 260 °C + 0 °C) at the rated MSL level.			

Table 6. Pb-free Process – Package Classification Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.2)

Note 1: The profiling tolerance is + 0 °C, -X °C (based on machine variation capability) whatever is required to control the profile process but at no time will it exceed -5 °C. Process compatibility at the peak reflow profile temperatures as defined in Table 4.2.
Note 2: Package volume excludes external terminals (balls, bumps, lands, leads) and/or nonintegral heat sinks.
Note 3: The maximum component temperature reached during reflow depends on package thickness and volume. The use of convection reflow processes reduces the thermal gradients between packages. However, thermal gradients due to differences in thermal mass of SMD packages may still exist.
Note 4: Components intended for use in a "lead-free" assembly process shall be evaluated using the "lead-free" classification temperatures and profiles defined in Tables 4.1, 4.2 and 5.2 whether or not lead free.