

FEATURES

- Four Matched PNP Transistors
- Low noise — $0.75 \text{ nV}/\sqrt{\text{Hz}}$
- High Speed — 325 MHz f_t
- Excellent Matching - 500 μV (typ)
- Dielectrically Isolated
- -25 V V_{CEO}

APPLICATIONS

- Microphone Preamplifiers
- Tape Head Preamplifiers
- Current Sources
- Current Mirrors
- Log/Antilog Amplifiers
- Multipliers

DESCRIPTION

THAT120 is a quad, large-geometry monolithic PNP transistor array which combines low noise, high speed and excellent parametric matching. The large geometry typically results in 25Ω base spreading resistance, producing $0.75 \text{ nV}/\sqrt{\text{Hz}}$ voltage noise. This makes these parts an excellent choice for low-noise amplifier input stages.

Fabricated on a Complementary Bipolar Dielectrically Isolated process, all four transistors are electrically isolated from each other by a layer of oxide.

The resulting low collector-to-substrate capacitance produces a typical f_t of 325 MHz, for AC performance similar to 2N3906-class devices. The dielectric isolation also minimizes crosstalk and provides complete DC isolation.

Substrate biasing is not required for normal operation, though the substrate should be grounded to optimize speed. The one-chip construction assures excellent parameter matching and tracking over temperature.

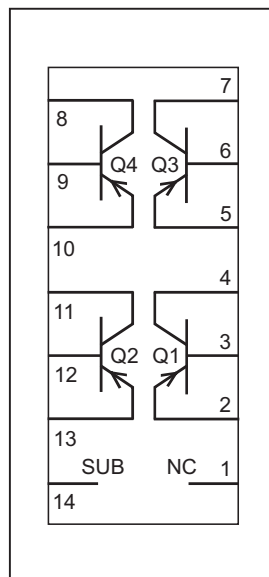


Figure 1. Pin Configuration

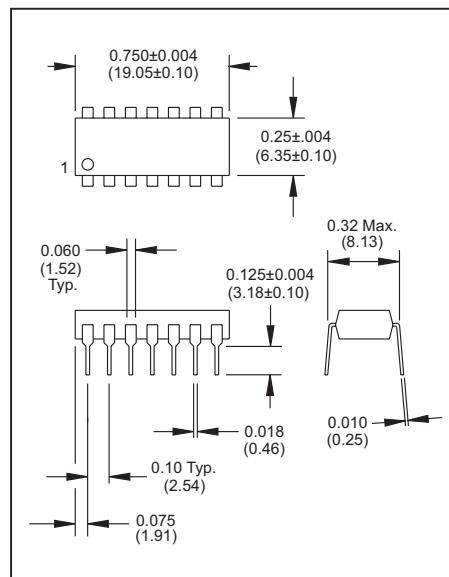


Figure 2. Dual-In-Line Package Outline

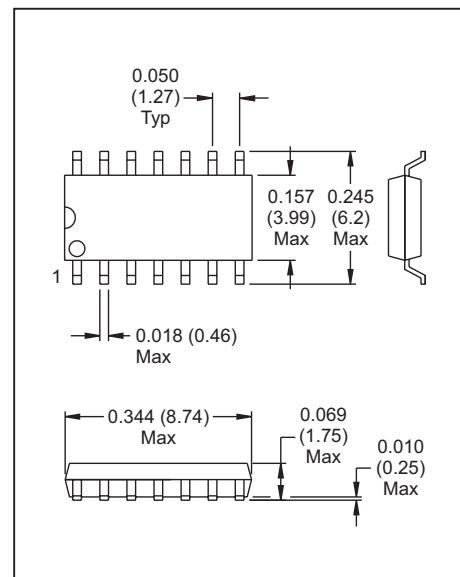


Figure 3. Surface Mount Package Outline

SPECIFICATIONS¹

Maximum Ratings (T_A = 25°C)						
Parameter	Symbol	Conditions	Min	Typ	Max	Units
Collector-Emitter Voltage	BV _{CEO}	I _C = 1 mA, I _B = 0	-25	-40	—	V
Collector-Base Voltage	BV _{CBO}	I _C = 10 μA, I _E = 0	-25	40	—	V
Emitter-Base Voltage	BV _{EBO}	I _E = 10 μA, I _C = 0	-5	—	—	V
Collector-Collector Voltage	BV _{CC}		±100	±200	—	V
Emitter-Emitter Voltage	BV _{EE}		±100	±200	—	V
Collector Current	I _C		-10	-20		mA
Emitter Current	I _E		-10	-20		mA
Operating Temperature Range	T _A		0		70	°C
Maximum Junction Temperature	T _{JMAX}				150	°C
Storage Temperature	T _{STORE}		-45		125	°C

Electrical Characteristics²						
Parameter	Symbol	Conditions	Min	Typ	Max	Units
Current Gain	h _{fe}	V _{CB} = 10 V				
		I _C = 1 mA	50	75	—	
		I _C = 10 μA	50	75	—	
Current Gain Matching	Δh _{fe}	V _{CB} = 10 V, I _C = 1 mA	—	5	—	%
Noise Voltage Density	e _N	V _{CB} = 10 V, I _C = 1 mA, 1 kHz	—	0.75	—	nV / √Hz
Gain-Bandwidth Product	f _t	I _C = 1 mA, V _{CB} = 10 V		325		MHz
ΔV _{BE} (V _{BE1} -V _{BE2} ; V _{BE3} -V _{BE4})	V _{OS}	I _C = 1 mA	—	±0.5	±3	mV
		I _C = 10 μA	—	±0.5	±3	mV
ΔI _B (I _{B1} -I _{B2} ; I _{B3} -I _{B4})	I _{OS}	I _C = 1 mA	—	±700	±1800	nA
		I _C = 10 μA	—	±7	±18	nA
Collector-Base Leakage Current	I _{CBO}	V _{CB} = 25 V	—	-25	—	pA
Bulk Resistance	r _{BE}	V _{CB} = 0 V, 10μA < I _C < 10mA	—	2	—	Ω
Base Spreading Resistance	r _{bb}	V _{CB} = 10 V, I _C = 1mA	—	25	—	Ω
Collector Saturation Voltage	V _{CE(SAT)}	I _C = 1 mA, I _B = 100 μA	—	-0.05		V
Output Capacitance	C _{OB}	V _{CB} = 10 V, I _E = 0 mA, 100 kHz		3		pF
Collector-Collector Capacitance Q ₁ /Q ₂ ; Q ₃ /Q ₄	C _{CC}	V _{CC} = 0 V, 100 kHz		0.6		pF

1. All specifications subject to change without notice.

2. Unless otherwise noted, T_A=25°C.