

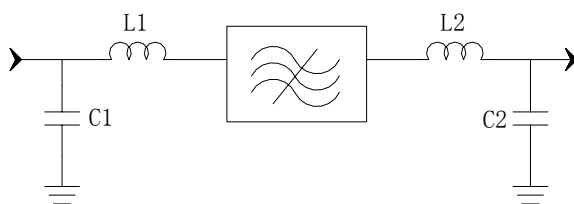
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	159.9	160	160.1
Insertion Loss	dB		23.8	25.5
1 dB Bandwidth	MHz	8.5	9	
3 dB Bandwidth	MHz	9.3	9.4	
40 dB Bandwidth	MHz		10.8	11.5
Passband Variation	dB		0.8	1
Absolute Delay	usec		1.77	
Group Delay Variation ($f_0 \pm 4.375\text{MHz}$)	nsec		38	80
Ultimate Rejection	dB	40	45	
Substrate Material		112LT		
Ambient Temperature	°C	25		
Package Size		SMP-53 (13.3 x 6.5 x 1.56 mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=47nH L2=33nH
C1=33pF C2=33pF

Source/Load Impedance=50 ohm

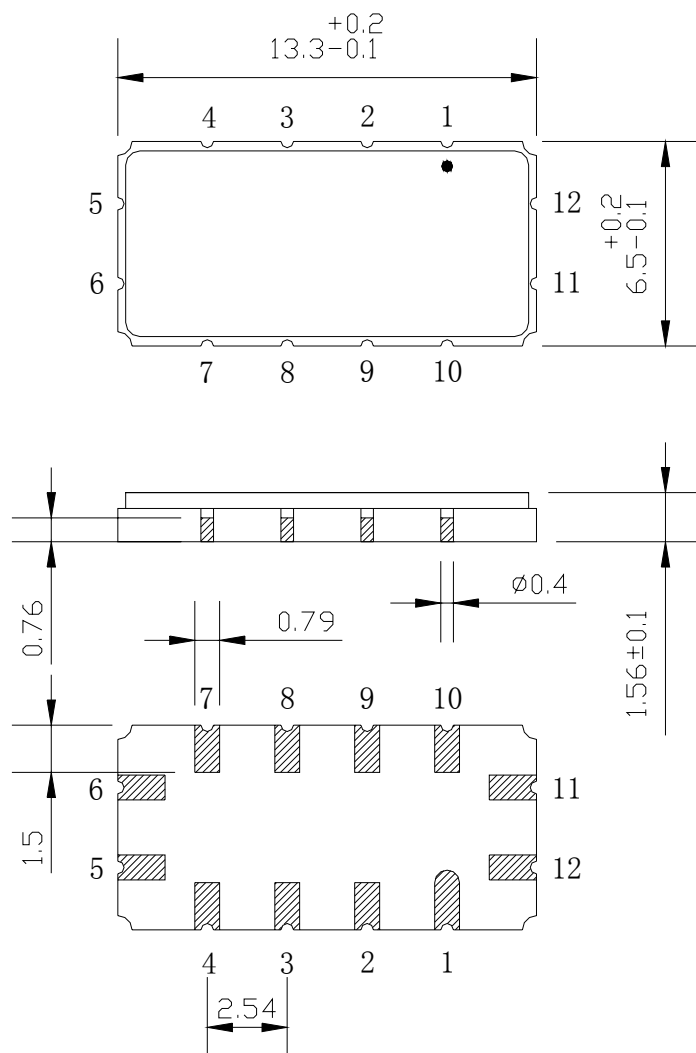
Notes - Component values may change depending
on board layout.



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Part Number	LBT16010		
Rev. Date	2005-3-7		
Rev.	2.0	Page	1/3

Package Dimension

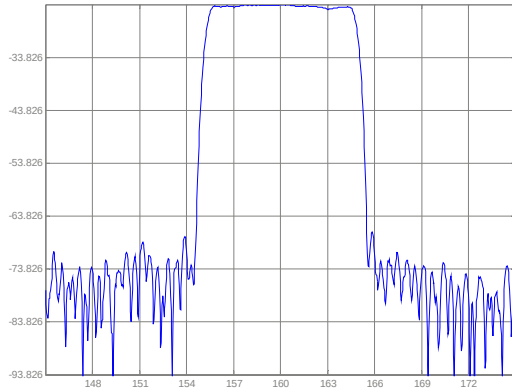


Pin 11: Input
Pin 5: Output

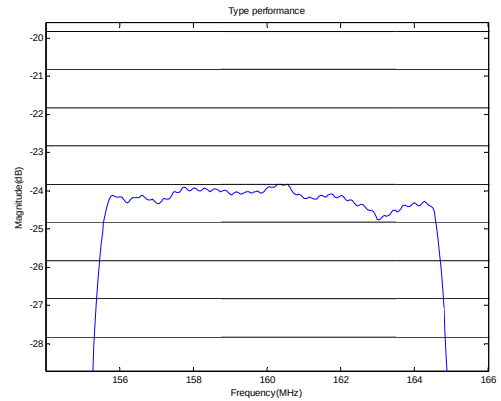
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		Rev.		2.0	Page 2/3

Typical Performance

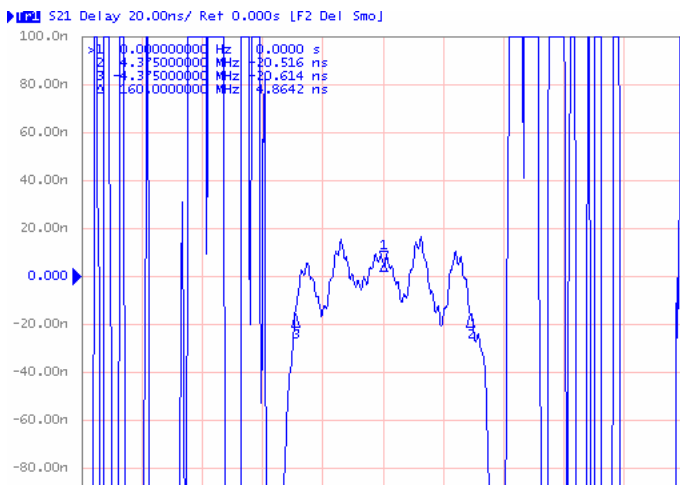
Frequency Respond



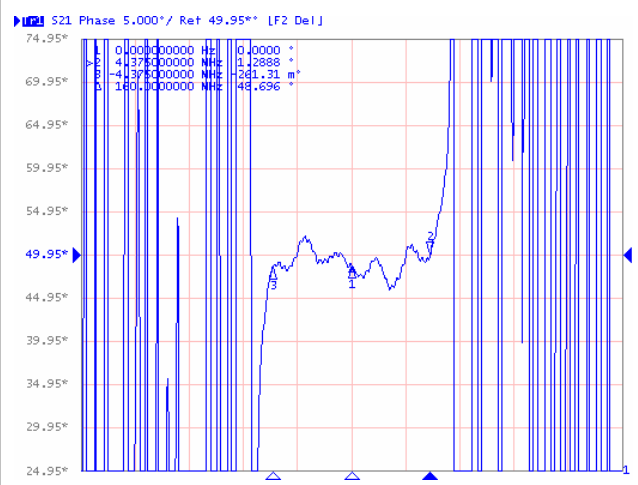
Passband Respond



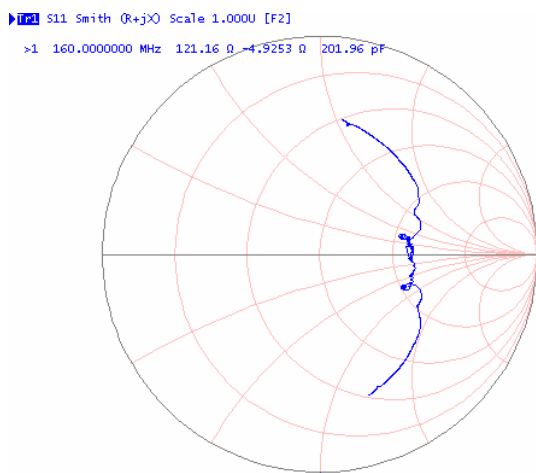
Group Delay Variation($f_0 \pm 4.375\text{kHz}$)



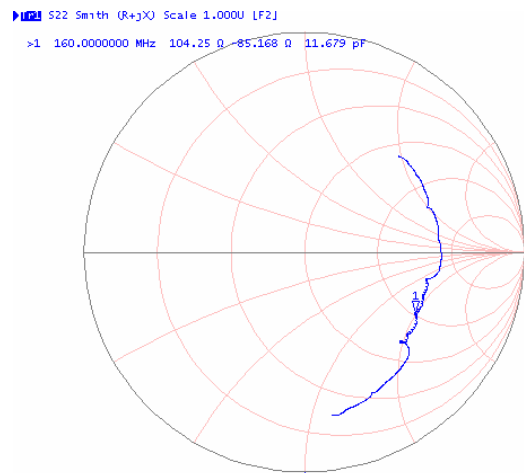
Phase Linearity($f_0 \pm 4.375\text{kHz}$)



Simth Chart S11



Simth Chart S22



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