AUTOMOTIVE

Available

RoHS

HALOGEN



# Vishay General Semiconductor

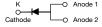
# **High Current Density Surface Mount** Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.43 \text{ V}$  at  $I_F = 5 \text{ A}$ 





#### **TO-277A (SMPC)**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	30 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	200 A			
E <sub>AS</sub>	100 mJ			
V <sub>F</sub> at I <sub>F</sub> = 12 A	0.58 V			
T <sub>J</sub> max.	150 °C			

#### TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters and polarity protection applications.

- Very low profile typical height of 1.1 mm
- Ideal for automatic placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

#### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and

commercial grade

Base P/NHM3 - halogen-free, RoHS compliant, and

automotive grade

Terminals: Matte tin plated leads, solderable

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V12P10	UNIT	
Device marking code		V1210		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub>	12	А	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200	Α	
Non-repetitive avalanche energy at $I_{AS} = 2.0 \text{ A}$ , $T_{J} = 25 ^{\circ}\text{C}$	E <sub>AS</sub>	100	mJ	
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °C	I <sub>RRM</sub>	1.0	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

# V12P10

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	$V_{BR}$	100 (minimum)	-		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.50	-		
Instantaneous forward voltage	I <sub>F</sub> = 12 A			0.65	0.70	V	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.43	-		
	I <sub>F</sub> = 12 A			0.58	0.64		
Reverse current	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	T <sub>A</sub> = 25 °C		7.0	-	μΑ
	V <sub>R</sub> = 70 V	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	4.4	-	mA	
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		21.3	250	μΑ	
		T <sub>A</sub> = 125 °C		11.8	20	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise specified)				
PARAMETER	SYMBOL	V12P10	UNIT	
Typical thermal registance	R <sub>eJA</sub> <sup>(1)</sup>	60	°C/W	
Typical thermal resistance	$R_{ heta JL}$	3		

#### Note

<sup>(1)</sup> Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V12P10-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V12P10-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	
V12P10HM3/86A (1)	0.10	86A	1500	7" diameter plastic tape and reel	
V12P10HM3/87A (1)	0.10	87A	6500	13" diameter plastic tape and reel	

### Note

(1) Automotive grade



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#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

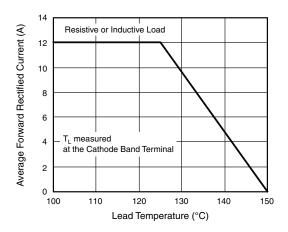


Fig. 1 - Maximum Forward Current Derating Curve

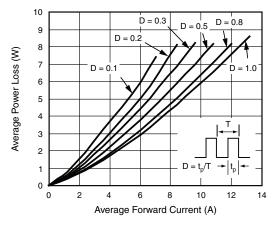


Fig. 2 - Forward Power Loss Characteristics

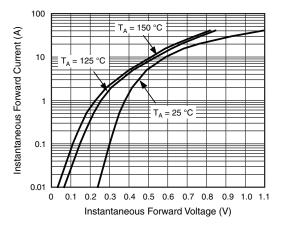


Fig. 3 - Typical Instantaneous Forward Characteristics

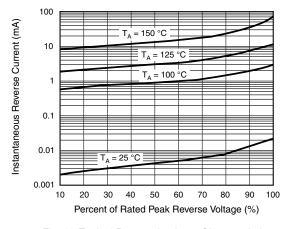


Fig. 4 - Typical Reverse Leakage Characteristics

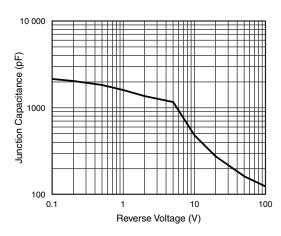


Fig. 5 - Typical Junction Capacitance

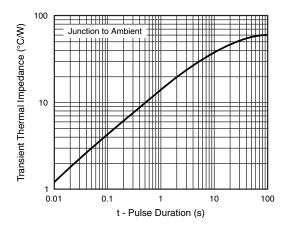
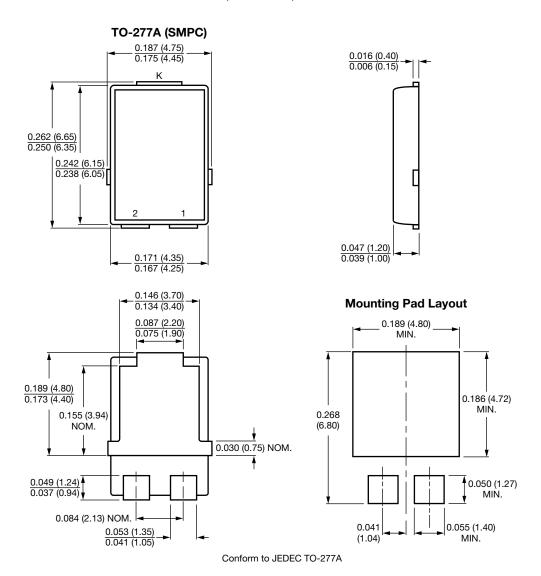


Fig. 6 - Typical Transient Thermal Impedance

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#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







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Revision: 11-Mar-11