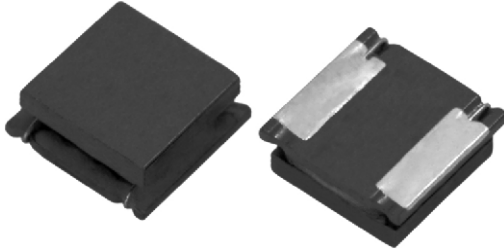


Low Profile, High Current Inductors



FEATURES

- Shielded construction
- Frequency range up to 5.0 MHz
- Handles high transient current spikes without saturation
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

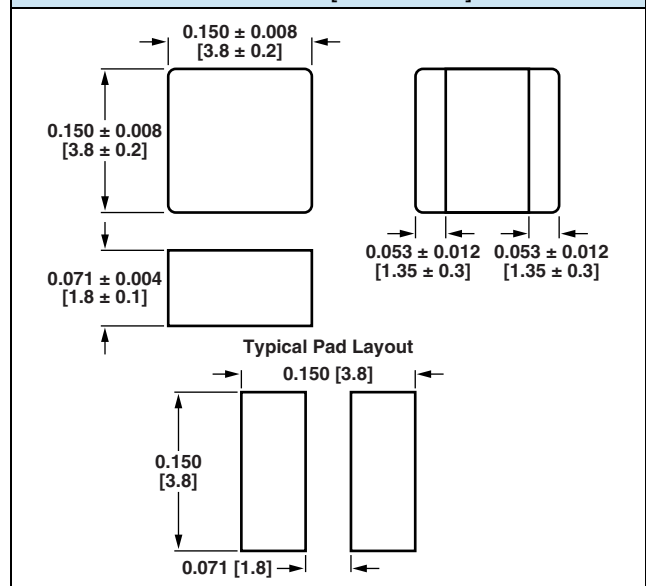
- PDA/notebook/desktop/server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS				
L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽³⁾	SATURATION CURRENT DC TYP. (A) ⁽⁴⁾
0.56	17	22	5.40	5.50
1.0	20	25	3.80	3.80
1.2	25	30	3.60	3.60
2.2	35	45	3.00	3.00
3.3	45	56	2.70	2.40
4.7	70	90	2.20	2.00
6.8	90	115	1.90	1.50
8.2	105	132	1.40	1.40
10.0	135	170	1.30	1.30
15.0	185	222	1.25	1.00
22.0	250	315	1.20	0.83
33.0	405	486	0.90	0.68
47.0	495	594	0.80	0.56

Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range - 55 °C to + 125 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- (4) DC current (A) that will cause L_0 to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

DIMENSIONS in inches [millimeters]



DESCRIPTION

IFSC-1515AH-01	3.3 μH	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	F	S	C	1	5	1	5	A	H	E	R	3	R	3	M	0	1
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES		



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