International
IOR Rectifier

REPETITIVE AVALANCHE AND dv/dt RATED
HEXFET® TRANSISTORS THRU-HOLE (TO-204AA/AE)

IRFAC40
600V, N-CHANNEL

Product Summary

Part Number	BVDSS	RDS(on)	ID
IRFAC40	600V	1.2Ω	6.2A

The HEXFET® technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry and unique processing of this latest "State of the Art" design achieves: very low on-state resistance combined with high transconductance; superior reverse energy and diode recovery dv/dt capability.

The HEXFET transistors also feature all of the well established advantages of MOSFETs such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high energy pulse circuits.



TO-3

Features:

- Repetitive Avalanche Ratings
- Dynamic dv/dt Rating
- Hermetically Sealed
- Simple Drive Requirements
- Ease of Paralleling

Absolute Maximum Ratings

	Parameter		Units
I_D @ V_{GS} = 0V, T_C = 25°C	Continuous Drain Current	6.2	A
I_D @ V_{GS} = 0V, T_C = 100°C	Continuous Drain Current	3.9	
I_{DM}	Pulsed Drain Current ①	25	
P_D @ T_C = 25°C	Max. Power Dissipation	125	W
	Linear Derating Factor	1.0	W/°C
V_{GS}	Gate-to-Source Voltage	±20	V
E_{AS}	Single Pulse Avalanche Energy ②	570	mJ
I_{AR}	Avalanche Current ①	6.2	A
E_{AR}	Repetitive Avalanche Energy ①	12.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	3.0	V/ns
T_J	Operating Junction	-55 to 150	°C
T_{STG}	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10s)	
	Weight	11.5(typical)	g

For footnotes refer to the last page

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	600	—	—	V	V_{GS} = 0V, I_D = 1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.70	—	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	1.2	Ω	V_{GS} = 10V, I_D = 3.9A④
		—	—	1.4		V_{GS} = 10V, I_D =6.2A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	V_{DS} = V_{GS}, I_D =250µA
g_{fs}	Forward Transconductance	4.7	—	—	S (℧)	V_{DS} > 15V, I_{DS} = 3.9A ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	25	µA	V_{DS}=480V,V_{GS}=0V
		—	—	250		V_{DS} =480V V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V_{GS} = 20V
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		V_{GS} = -20V
Q_g	Total Gate Charge	26	—	60	nC	V_{GS} = 10V, ID = 6.2A V_{DS} = 300V
Q_{gs}	Gate-to-Source Charge	3.6	—	8.3		
Q_{gd}	Gate-to-Drain ('Miller') Charge	13	—	30		
$t_{d(on)}$	Turn-On Delay Time	—	—	20	ns	V_{DD} =300V, I_D =6.2A, R_G =9.1Ω
t_r	Rise Time	—	—	27		
$t_{d(off)}$	Turn-Off Delay Time	—	—	83		
t_f	Fall Time	—	—	30		
$L_S + L_D$	Total Inductance	—	6.1	—	nH	Measured from drain lead (6mm/0.25in. from package) to source lead (6mm/0.25in. from package)
C_{iss}	Input Capacitance	—	1300		pF	V_{GS} = 0V, V_{DS} = 25V f = 1.0MHz
C_{oss}	Output Capacitance	—	160	—		
C_{rss}	Reverse Transfer Capacitance	—	45	—		

Source-Drain Diode Ratings and Characteristics

	Parameter		Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)		—	—	6.2	A	
I_{SM}	Pulse Source Current (Body Diode) ①		—	—	25		
V_{SD}	Diode Forward Voltage		—	—	1.1	V	T_j = 25°C, I_S =6.2A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time		—	—	940	nS	T_j = 25°C, I_F = 6.2A, di/dt ≤ 100A/µs
Q_{RR}	Reverse Recovery Charge		—	—	8.0	µC	V_{DD} ≤ 50V ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L_S + L_D.					

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction to Case	—	—	1.0	°C/W	
R_{thJA}	Junction to Ambient	—	—	30		Typical socket mount

For footnotes refer to the last page

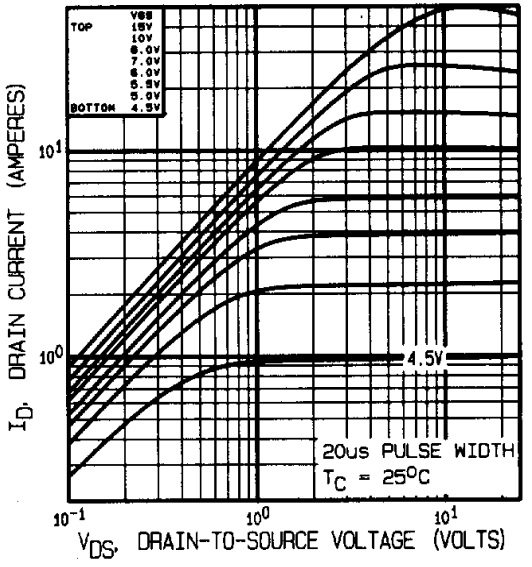


Fig 1. Typical Output Characteristics

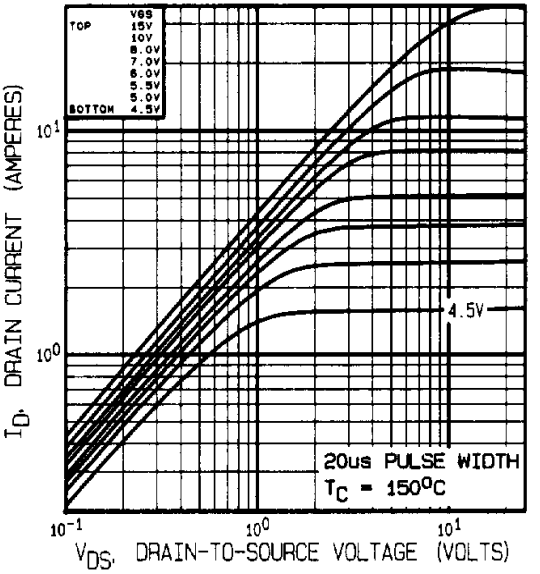


Fig 2. Typical Output Characteristics

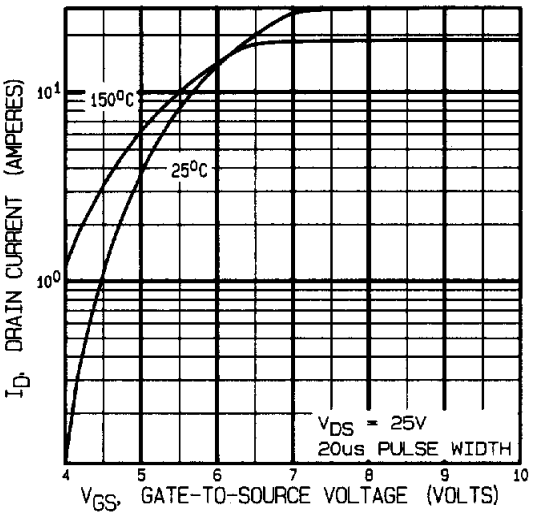


Fig 3. Typical Transfer Characteristics

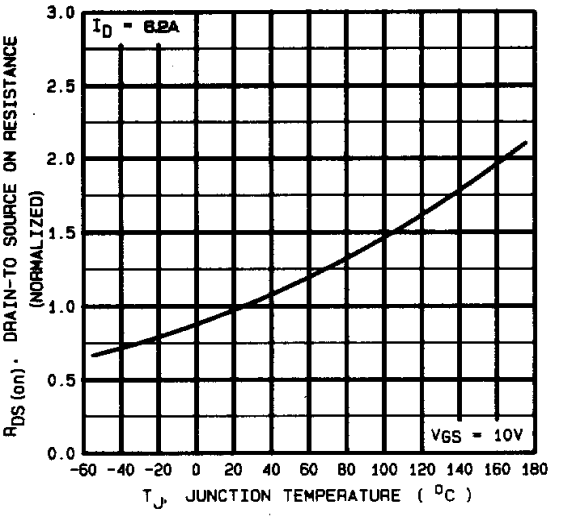


Fig 4. Normalized On-Resistance Vs. Temperature

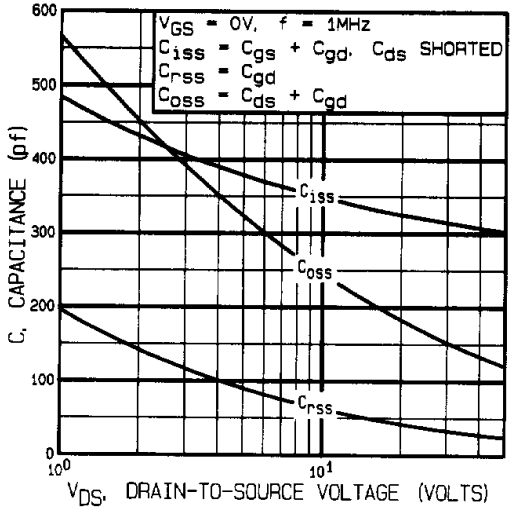


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

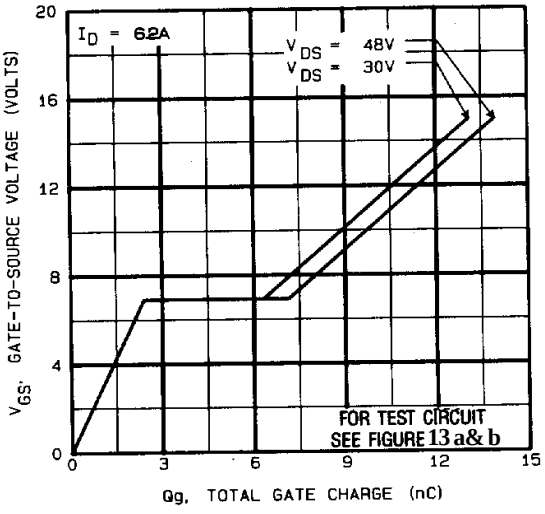


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

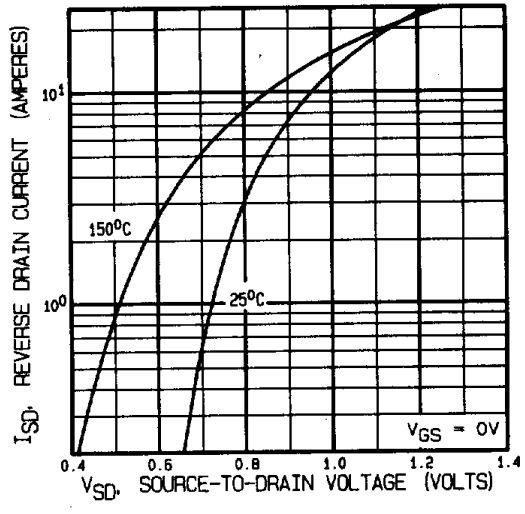


Fig 7. Typical Source-Drain Diode Forward Voltage

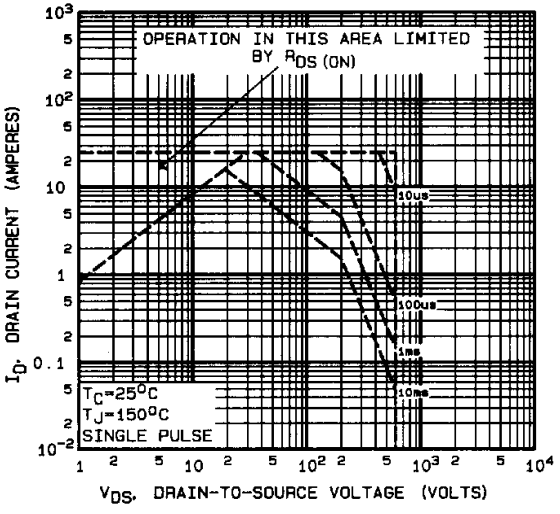


Fig 8. Maximum Safe Operating Area

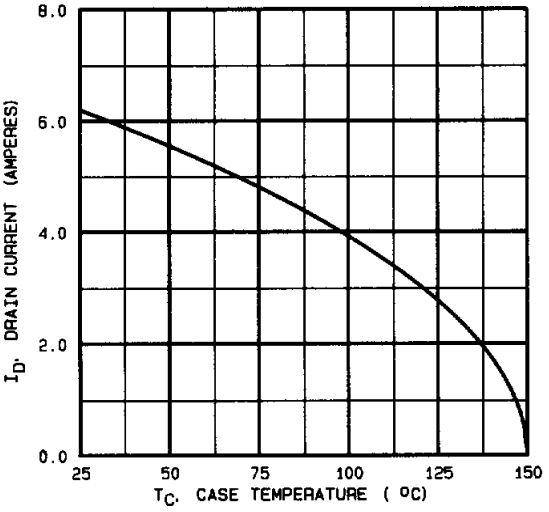


Fig 9. Maximum Drain Current Vs. Case Temperature

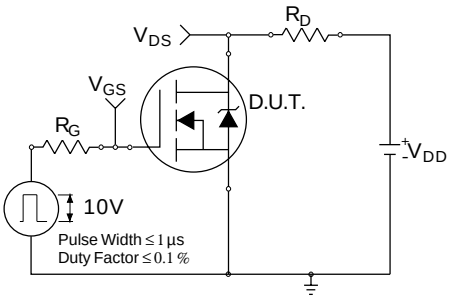


Fig 10a. Switching Time Test Circuit

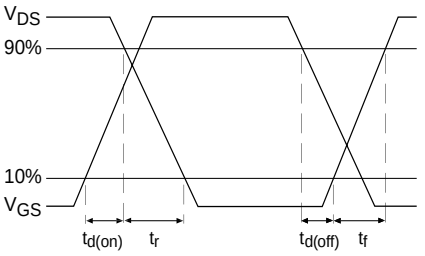


Fig 10b. Switching Time Waveforms

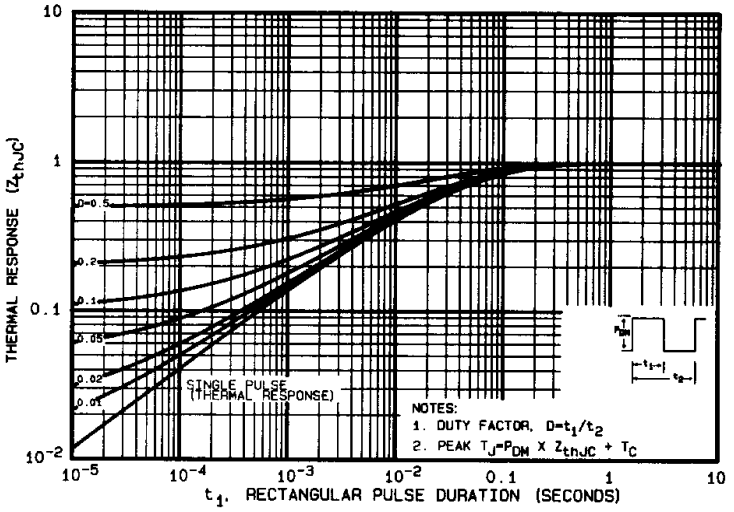


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

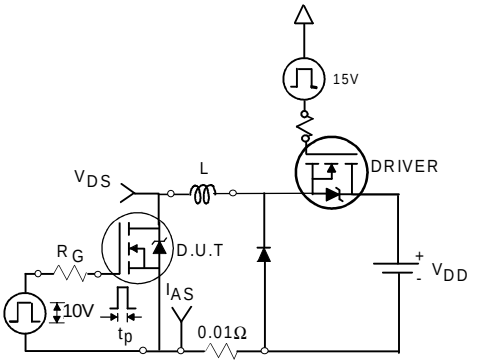


Fig 12a. Unclamped Inductive Test Circuit

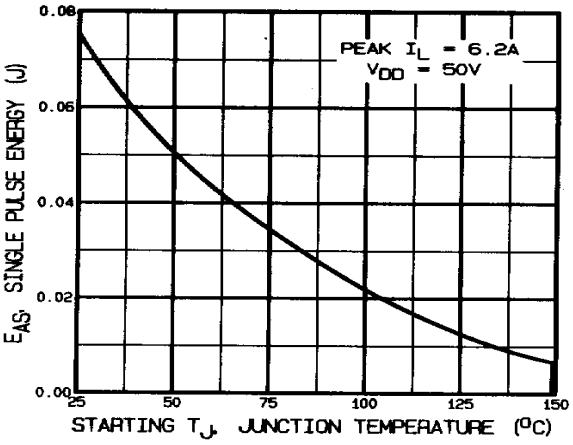


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

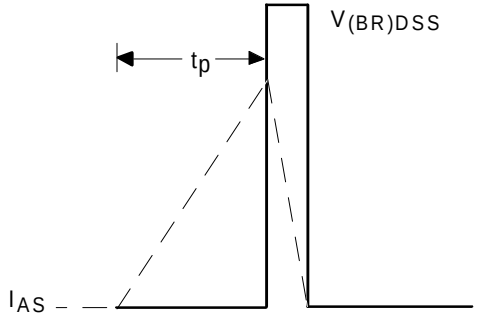


Fig 12b. Unclamped Inductive Waveforms

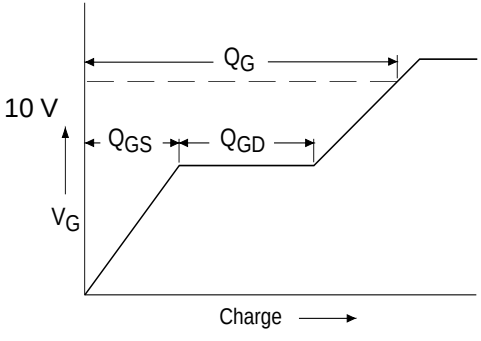


Fig 13a. Basic Gate Charge Waveform

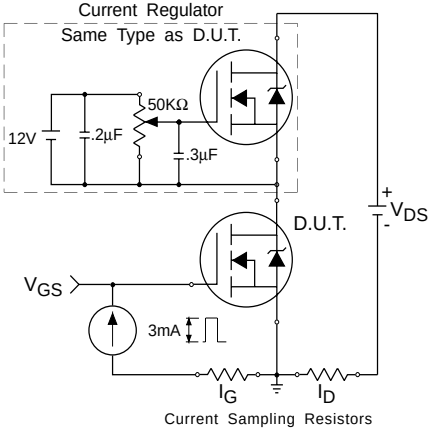


Fig 13b. Gate Charge Test Circuit

Foot Notes:

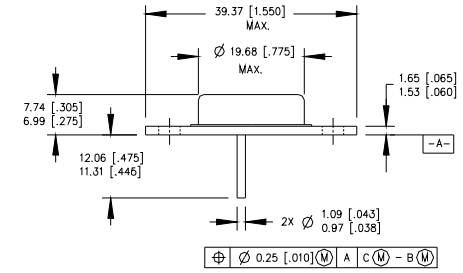
① Repetitive Rating; Pulse width limited by maximum junction temperature.

② V_{DD} = 50V, starting T_J = 25°C, Peak I_L = 6.2A,

③ $I_{SD} \leq 6.2A$, $di/dt \leq 80A/\mu s$, $V_{DD} \leq 600V$, $T_J \leq 150°C$ Suggested RG =9.1 Ω

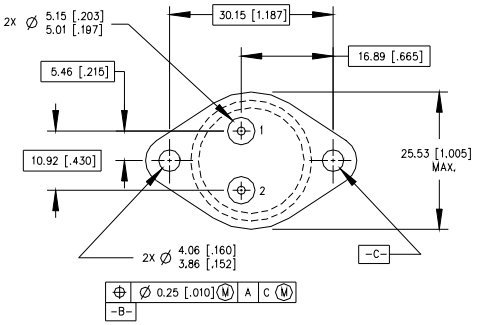
④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%

Case Outline and Dimensions —TO-204AA (Modified TO-3)



PIN ASSIGNMENTS

HEXFET	SCHOTTKY	IGBT
1 – SOURCE	1 – ANODE 1	1 – GATE
2 – GATE	2 – ANODE 2	2 – EMITTER
3 – DRAIN (CASE)	3 – COMMON CATHODE (CASE)	3 – COLLECTOR (CASE)



NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M–1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE TO–204–AA.

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Data and specifications subject to change without notice. 1/01