

BGA751L7

Single-Band UMTS LNA
(800, 900 MHz)

RF & Protection Devices



Never stop thinking

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BGA751L7**Revision History: 2009-05-27, V3.2****Previous Version: 2008-11-24, V3.1**

Page	Subjects (major changes since last revision)
7	Updated DC Characteristics (added limits)
9, 10	Updated footnotes

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1 Description

The BGA751L7 is a low current single-band low noise amplifier MMIC for UMTS bands V, VI and VIII. The LNA is based upon Infineon's proprietary and cost-effective SiGe:C technology and comes in a low profile TSLP-7-1 leadless green package. Because the matching is off chip, the 800 MHz path can be easily converted into a 900 MHz path by optimizing the input and output matching network. This document specifies the electrical parameters, pinout, application circuit and packaging of the chip.

Features

- Gain: 16 / -8 dB in high / low gain mode
- Noise figure: 1.05 dB in high gain mode
- Supply current: 3.3 / 0.5 mA in high / low gain mode
- Standby mode (< 2 μ A typ.)
- Output internally matched to 50 Ω
- Inputs pre-matched to 50 Ω
- 2kV HBM ESD protection
- Low external component count
- Small leadless TSLP-7-1 package (2.0 x 1.3 x 0.39 mm)
- Pb-free (RoHS compliant) package



TSLP-7-1 package

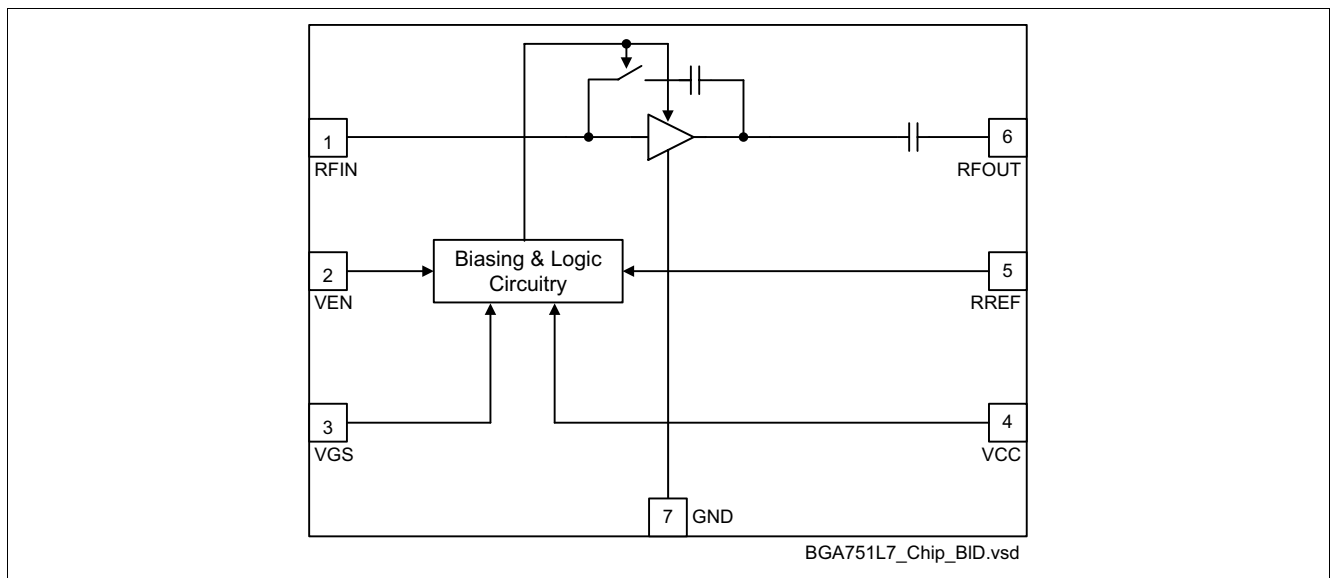


Figure 1 Block diagram of single-band LNA

Type	Package	Marking	Chip
BGA751L7	TSLP-7-1	B5	T1533

2 Electrical Characteristics

2.1 Absolute Maximum Ratings

Table 1 Absolute Maximum Ratings

Parameter	Symbol	Values		Unit	Note / Test Condition
		Min.	Max.		
Supply voltage	V_{CC}	-0.3	3.6	V	
Supply current	I_{CC}		10	mA	
Pin voltage	V_{PIN}	-0.3	$V_{CC}+0.3$	V	All pins except RF input pin
Pin voltage RF Input Pin	V_{RFIN}	-0.3	0.9	V	
RF input power	P_{RFIN}		4	dBm	
Junction temperature	T_j		150	°C	
Ambient temperature range	T_A	-30	85	°C	
Storage temperature range	T_{stg}	-65	150	°C	

2.2 Thermal Resistance

Table 2 Thermal Resistance

Parameter	Symbol	Value	Unit	Note / Test Conditions
Thermal resistance junction to soldering point	R_{thJS}	150	K/W	

2.3 ESD Integrity

Table 3 ESD Integrity

Parameter	Symbol	Value (typ.)	Unit	Note / Test Conditions
ESD hardness HBM ¹⁾	$V_{ESD-HBM}$	2000	V	All pins

1) According to JESD22-A114

2.4 DC Characteristics

Table 4 DC Characteristics, $T_A = 25\text{ °C}$

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Supply voltage	V_{CC}	2.6	2.8	3.0	V	
Supply current high gain mode	I_{CCHG}		3.3		mA	
Supply current low gain mode	I_{CCLG}		500		μA	
Supply current standby mode	I_{CCOFF}		0.1	2.0	μA	
Logic level high	V_{HI}	1.5	2.8		V	VEN and VGS
Logic level low	V_{LO}	-0.2	0.0	0.5	V	
Logic currents VEN	I_{ENL}			0.1	μA	VEN
	I_{ENH}		5.0	6.0	μA	
Logic currents VGS	I_{GSL}			0.1	μA	VGS
	I_{GSH}		5.0	6.0	μA	

2.5 Gain Mode Select Truth Table

Table 5 Truth Table

Control Voltage		State	
		Bands V, VI and VIII	
VEN	VGS	HG	LG
H	L	OFF	ON
H	H	ON	OFF
L	L	STANDBY ¹⁾	
L	H		

1) In order to achieve minimum standby current it is encouraged to apply logic low-level at the VGS pin in standby mode although this is not mandatory. Details see section 2.4.

2.6 Switching Times

Table 6 Typical switching times; $T_A = -30 \dots 85\text{ °C}$

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Settling time gainstep	t_{GS}		1		μs	Switching LG $\leftrightarrow$ HG

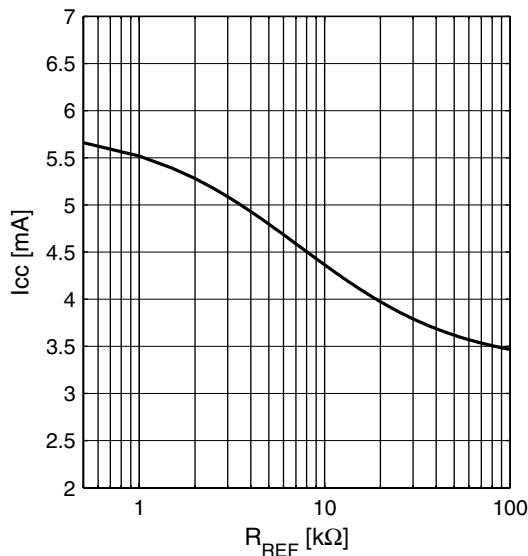
Supply current and Power gain characteristics; $T_A = 25\text{ }^{\circ}\text{C}$

2.7 Supply current and Power gain characteristics; $T_A = 25\text{ }^{\circ}\text{C}$

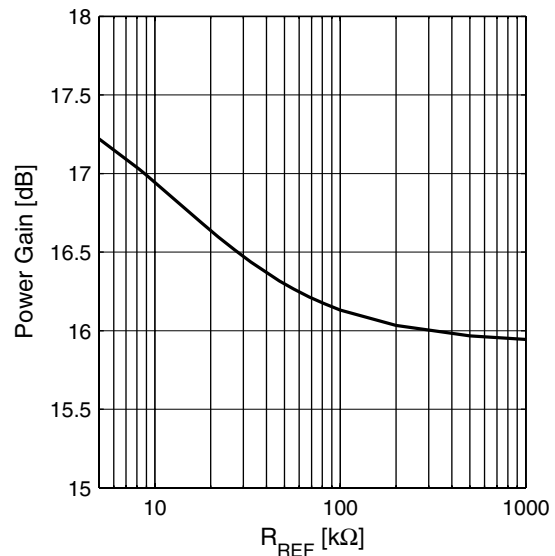
Supply current and Power gain high gain mode versus reference resistor R_{REF} (low gain mode supply current is independent of reference resistor).

Note: In order to achieve higher gain an external reference resistor can be soldered between RREF (Pin 5) and ground (see [Figure 2 on page 16](#)).

Supply Current $I_{CC} = f(R_{REF})$
 $V_{CC} = 2.8\text{ V}$



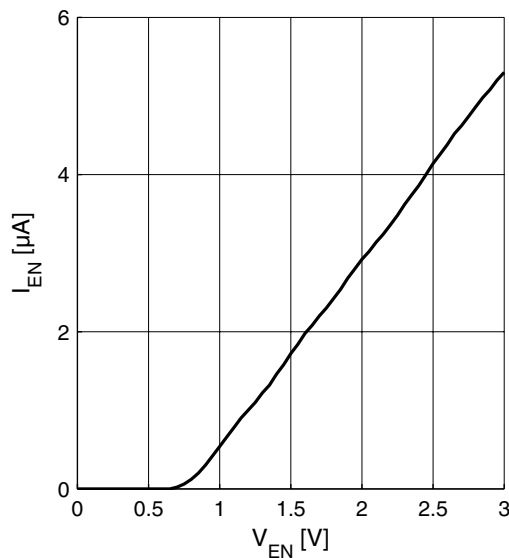
Power Gain $|S_{21}| = f(R_{REF})$
 $V_{CC} = 2.8\text{ V}$



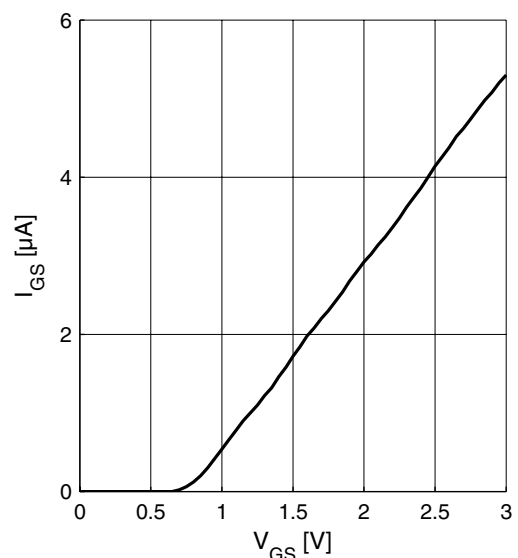
2.8 Logic Signal Characteristics; $T_A = 25\text{ }^{\circ}\text{C}$

Current consumption of logic inputs V_{EN} , V_{GS}

Logic currents $I_{EN} = f(V_{EN})$
 $V_{CC} = 2.8\text{ V}$



Logic currents $I_{GS} = f(V_{GS})$
 $V_{CC} = 2.8\text{ V}$



2.9 Measured RF Characteristics UMTS Bands V / VI

Table 7 Typical Characteristics 800 MHz Band, $T_A = 25\text{ }^{\circ}\text{C}$, $V_{CC} = 2.8\text{ V}$, $R_{REF} = \text{n/c}$

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Pass band range band V		869		894	MHz	
Pass band range band VI		875		885	MHz	
Current consumption	I_{CCHG}		3.3		mA	High gain mode
	I_{CCLG}		0.5		mA	Low gain mode
Gain	S_{21HG}		15.8		dB	High gain mode
	S_{21LG}		-7.7		dB	Low gain mode
Reverse Isolation ¹⁾	S_{12HG}		-36		dB	High gain mode
	S_{12LG}		-8		dB	Low gain mode
Noise figure	NF_{HG}		1.05		dB	High gain mode
	NF_{LG}		7.9		dB	Low gain mode
Input return loss ¹⁾	S_{11HG}		-21		dB	50 Ω , high gain mode
	S_{11LG}		-13		dB	50 Ω , low gain mode
Output return loss ¹⁾	S_{22HG}		-21		dB	50 Ω , high gain mode
	S_{22LG}		-13		dB	50 Ω , low gain mode
Stability factor ²⁾	k		>2.3			DC to 10 GHz; all gain modes
Input compression point ¹⁾	IP_{1dBHG}		-5		dBm	High gain mode
	$IP_{1dB LG}$		-8		dBm	Low gain mode
Inband IIP3 ¹⁾ $f_1 - f_2 = 1\text{ MHz}$ $P_{f1} = P_{f2} = -37\text{ dBm}$	$IIP3_{HG}$		-7		dBm	High gain mode
	$IIP3_{LG}$		1			Low gain mode

1) Verification based on AQL; not 100% tested in production

2) Guaranteed by device design; not tested in production

2.10 Measured RF Characteristics UMTS Band VIII

Table 8 Typical Characteristics 900 MHz Band, $T_A = 25\text{ °C}$, $V_{CC} = 2.8\text{ V}$, $R_{REF} = \text{n/c}$

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Pass band range band VIII		925		960	MHz	
Current consumption	I_{CCHG}		3.3		mA	High gain mode
	I_{CCLG}		0.5		mA	Low gain mode
Gain	S_{21HG}		15.5		dB	High gain mode
	S_{21LG}		-7.2		dB	Low gain mode
Reverse Isolation ¹⁾	S_{12HG}		-36		dB	High gain mode
	S_{12LG}		-7		dB	Low gain mode
Noise figure	NF_{HG}		1.15		dB	High gain mode
	NF_{LG}		7.7		dB	Low gain mode
Input return loss ¹⁾	S_{11HG}		-12		dB	50 Ω , high gain mode
	S_{11LG}		-15		dB	50 Ω , low gain mode
Output return loss ¹⁾	S_{22HG}		-12		dB	50 Ω , high gain mode
	S_{22LG}		-12		dB	50 Ω , low gain mode
Stability factor ²⁾	k		>4.3			DC to 10 GHz; all gain modes
Input compression point ¹⁾	IP_{1dBHG}		-4		dBm	High gain mode
	$IP_{1dB LG}$		-5		dBm	Low gain mode
Inband IIP3 ¹⁾ $f_1 - f_2 = 1\text{ MHz}$ $P_{f1} = P_{f2} = -37\text{ dBm}$	$IIP3_{HG}$		-6		dBm	High gain mode
	$IIP3_{LG}$		1			Low gain mode

1) Verification based on AQL; not 100% tested in production

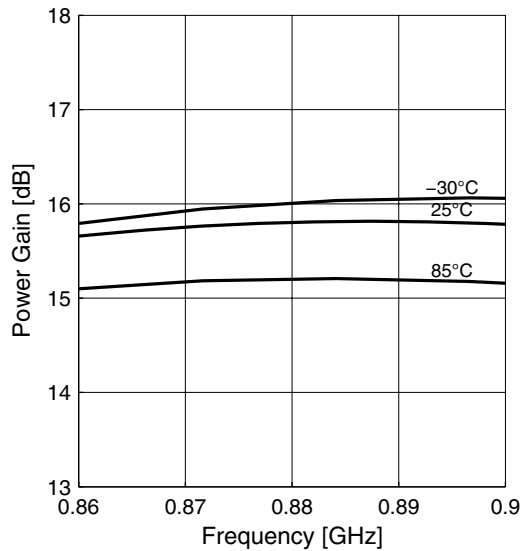
2) Guaranteed by device design; not tested in production

Measured Performance Low Band (Band V) High Gain Mode vs. Frequency

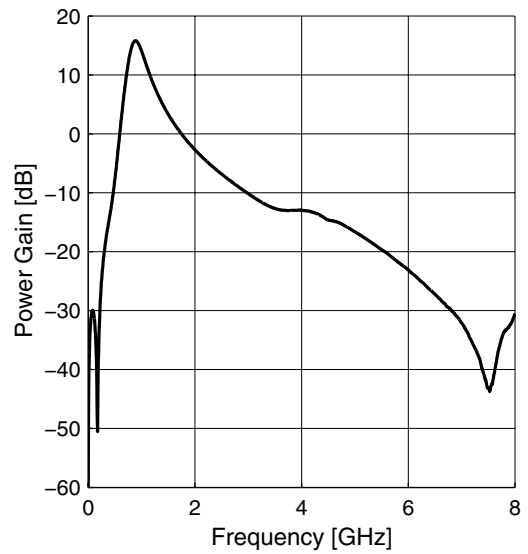
2.11 Measured Performance Low Band (Band V) High Gain Mode vs. Frequency

$T_A = 25^\circ\text{C}$, $V_{CC} = 2.8\text{ V}$, $V_{GS} = 2.8\text{ V}$, $V_{EN} = 2.8\text{ V}$, $R_{REF} = \text{n/c}$

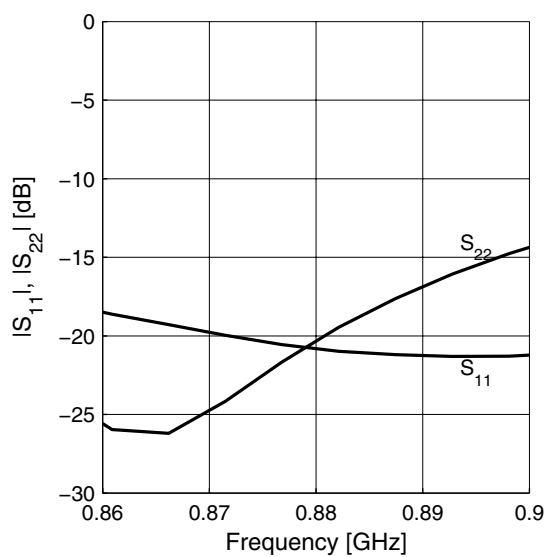
Power Gain $|S_{21}| = f(f)$



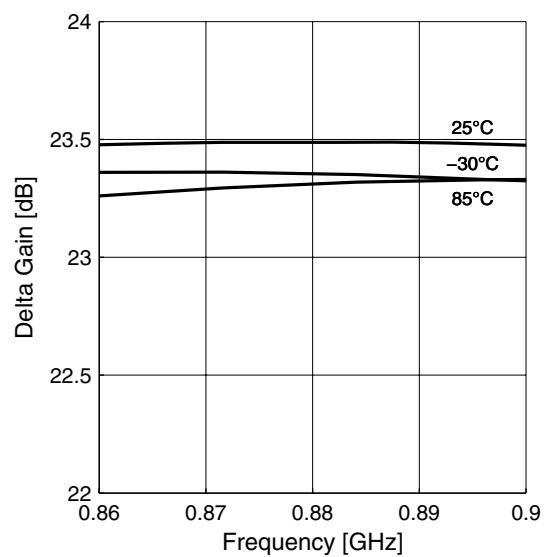
Power Gain wideband $|S_{21}| = f(f)$



Matching $|S_{11}| = f(f)$, $|S_{22}| = f(f)$

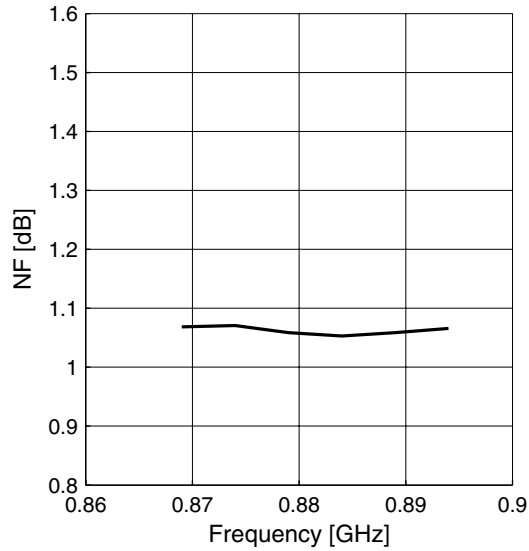


Gainstep HG-LG $|\Delta S_{21}| = f(f)$

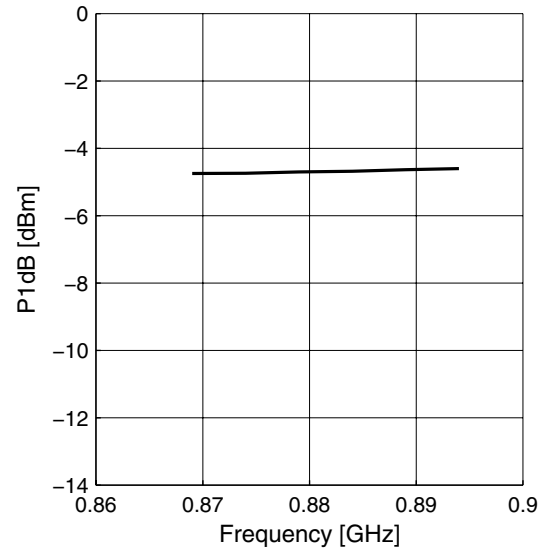


Measured Performance Low Band (Band V) High Gain Mode vs. Temperature

Noise Figure $NF = f(f)$



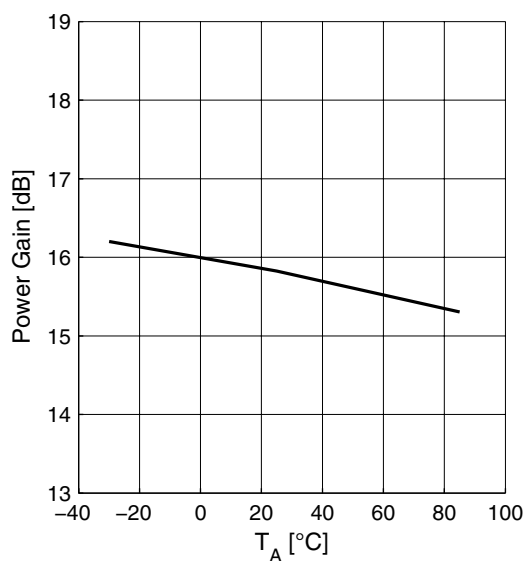
Input Compression $P1dB = f(f)$



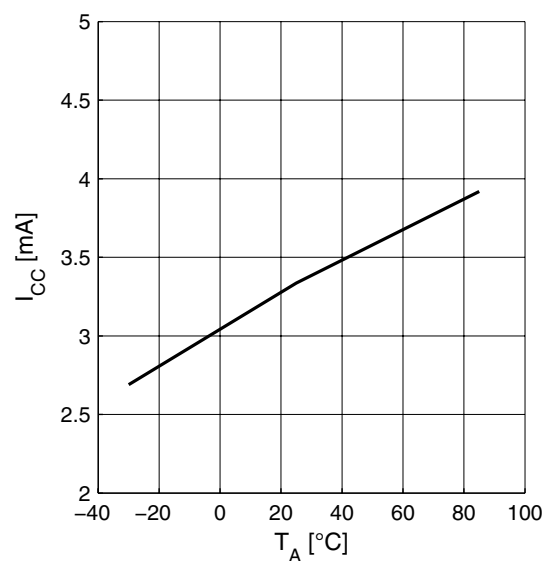
2.12 Measured Performance Low Band (Band V) High Gain Mode vs. Temperature

$V_{CC} = 2.8 \text{ V}$, $V_{GS} = 2.8 \text{ V}$, $V_{EN} = 2.8 \text{ V}$, $f = 880 \text{ MHz}$, $R_{REF} = n/c$

Power Gain $|S_{21}| = f(T_A)$

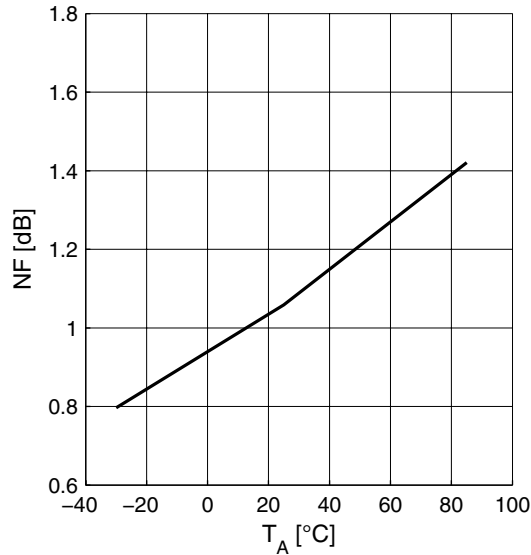


Supply Current $I_{CC} = f(T_A)$

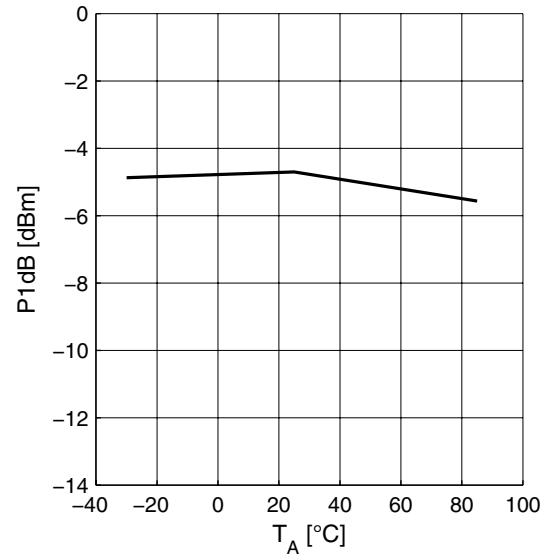


Measured Performance Low Band (Band V) Low Gain Mode vs. Frequency

Noise Figure $NF = f(T_A)$



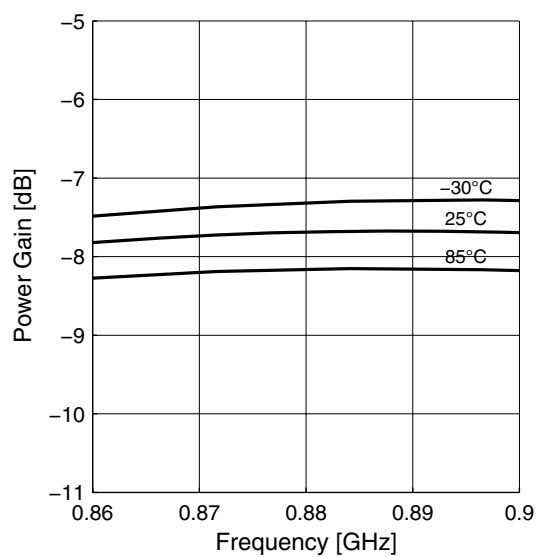
Input Compression $P1dB = f(T_A)$



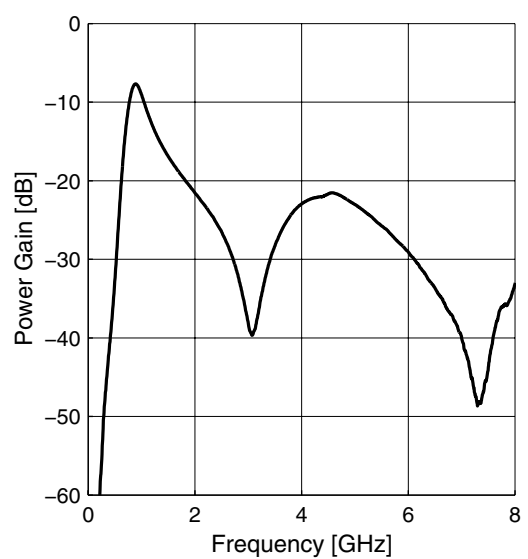
2.13 Measured Performance Low Band (Band V) Low Gain Mode vs. Frequency

$T_A = 25^\circ\text{C}$, $V_{CC} = 2.8\text{ V}$, $V_{GS} = 0\text{ V}$, $V_{EN} = 2.8\text{ V}$, $R_{REF} = n/c$

Power Gain $|S_{21}| = f(f)$

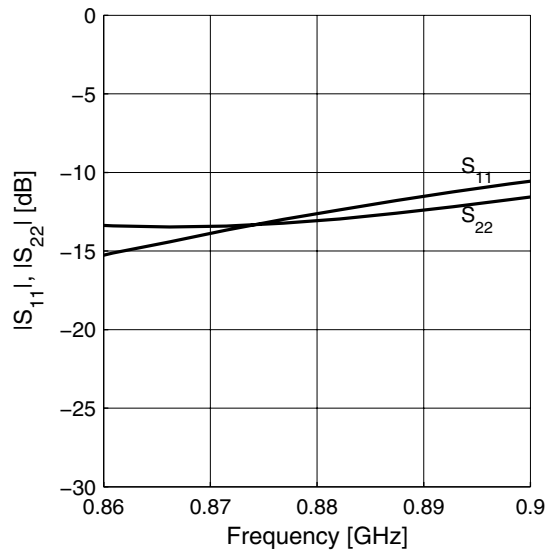


Power Gain wideband $|S_{21}| = f(f)$

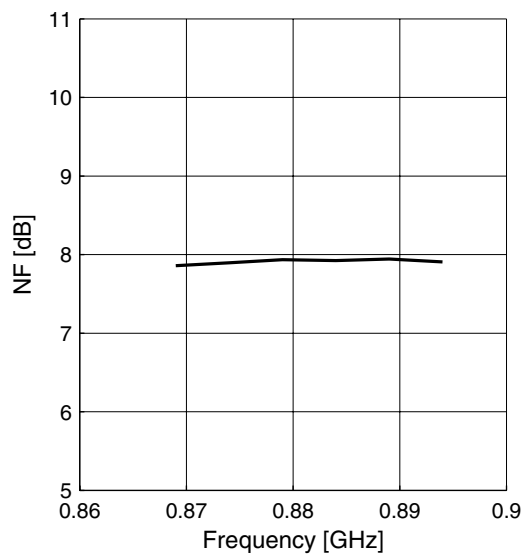


Measured Performance Low Band (Band V) Low Gain Mode vs. Frequency

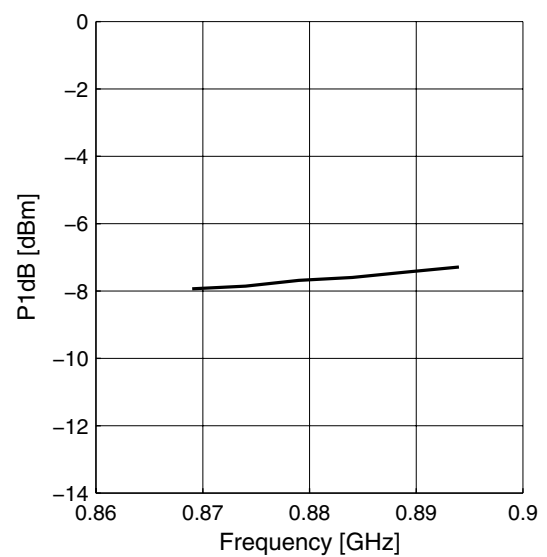
Matching $|S_{11}| = f(f)$, $|S_{22}| = f(f)$



Noise Figure $NF = f(f)$



Input Compression $P1dB = f(f)$

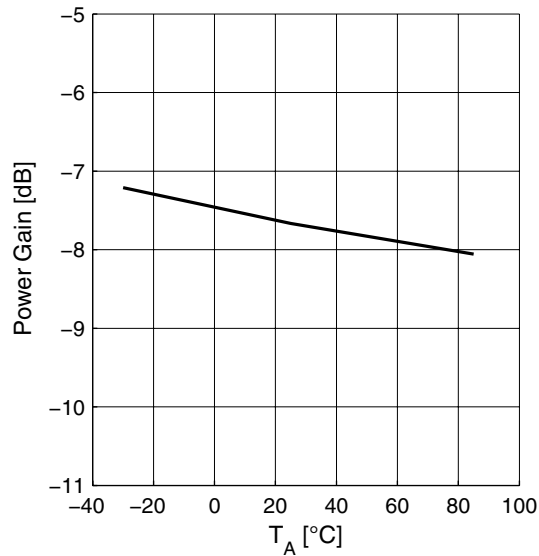


Measured Performance Low Band (Band V) Low Gain Mode vs. Temperature

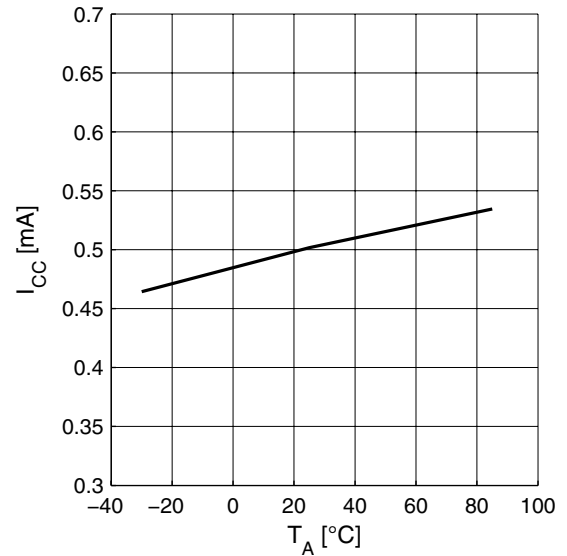
2.14 Measured Performance Low Band (Band V) Low Gain Mode vs. Temperature

$V_{CC} = 2.8 \text{ V}$, $V_{GS} = 0 \text{ V}$, $V_{EN} = 2.8 \text{ V}$, $f = 880 \text{ MHz}$, $R_{REF} = n/c$

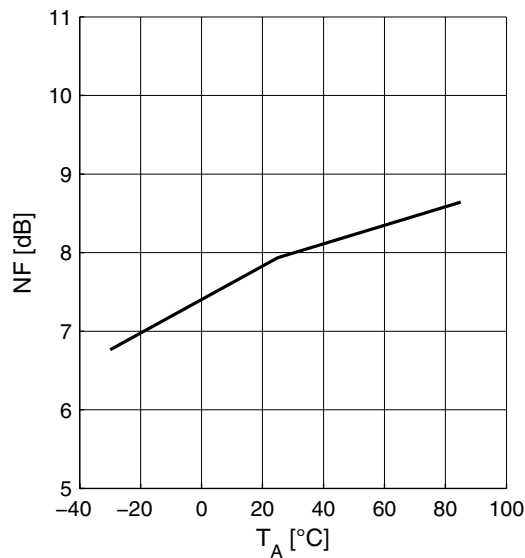
Power Gain $|S_{21}| = f(T_A)$



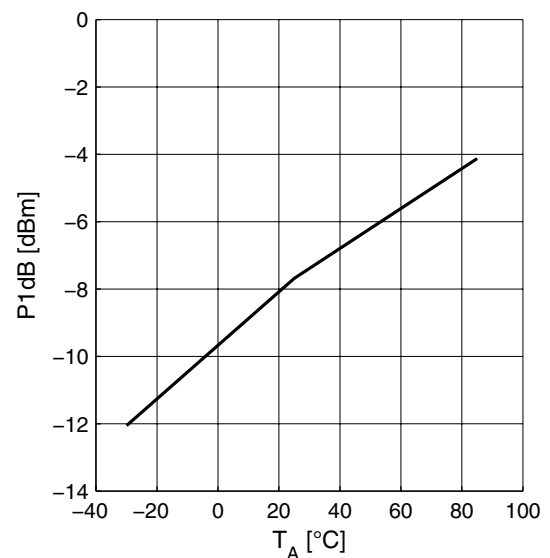
Supply Current $I_{CC} = f(T_A)$



Noise Figure $NF = f(T_A)$



Input Compression $P1dB = f(T_A)$



3 Application Circuit and Block Diagram

3.1 UMTS bands V and VI Application Circuit Schematic

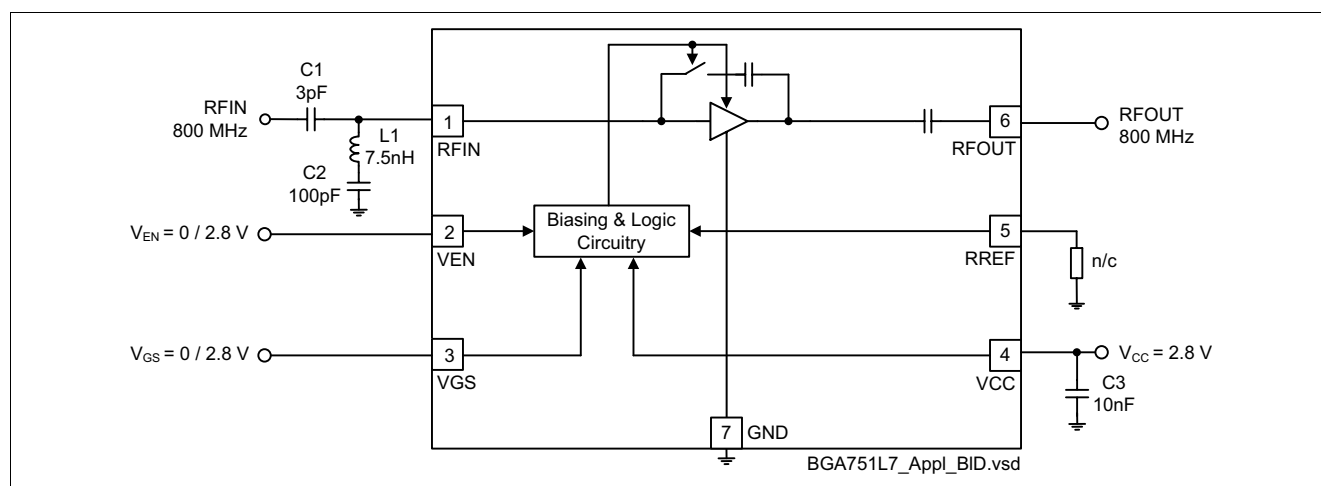


Figure 2 Application circuit with chip outline (top view)

Note: Package paddle (Pin 0) has to be RF grounded.

Table 9 Parts List

Part Number	Part Type	Manufacturer	Size	Comment
L1	Chip inductor	Various	0402	Wirewound, Q ≈ 50
C1 ... C3	Chip capacitor	Various	0402	

3.2 UMTS band VIII Application Circuit Schematic

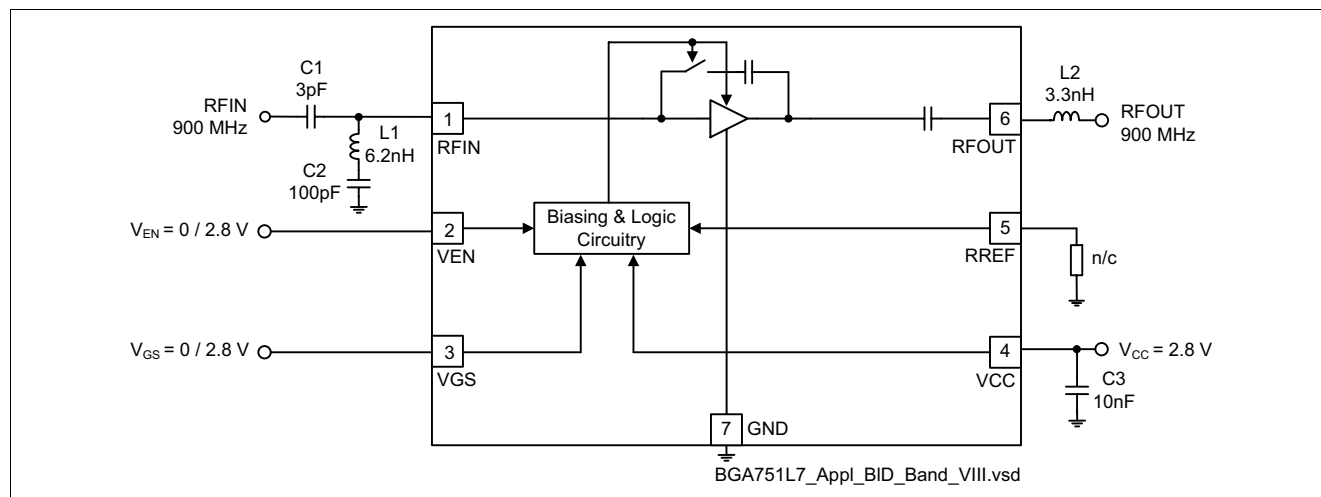


Figure 3 Application circuit with chip outline (top view)

Note: Package paddle (Pin 0) has to be RF grounded.

Table 10 Parts List

Part Number	Part Type	Manufacturer	Size	Comment
L1, L2	Chip inductor	Various	0402	Wirewound, Q ≈ 50
C1 ... C3	Chip capacitor	Various	0402	

3.3 Pin Definition

Table 11 Pin Definition and Function

Pin Number	Symbol	Function
1	RFIN	LNA input (800/900 MHz)
2	VEN	Band select control
3	VGS	Gain step control
4	VCC	Supply voltage
5	RREF	Bias current reference resistor (high gain mode)
6	RFOUT	LNA output (800/900 MHz)
7	GND	Package paddle; ground connection for LNA and control circuitry

3.4 Application Board

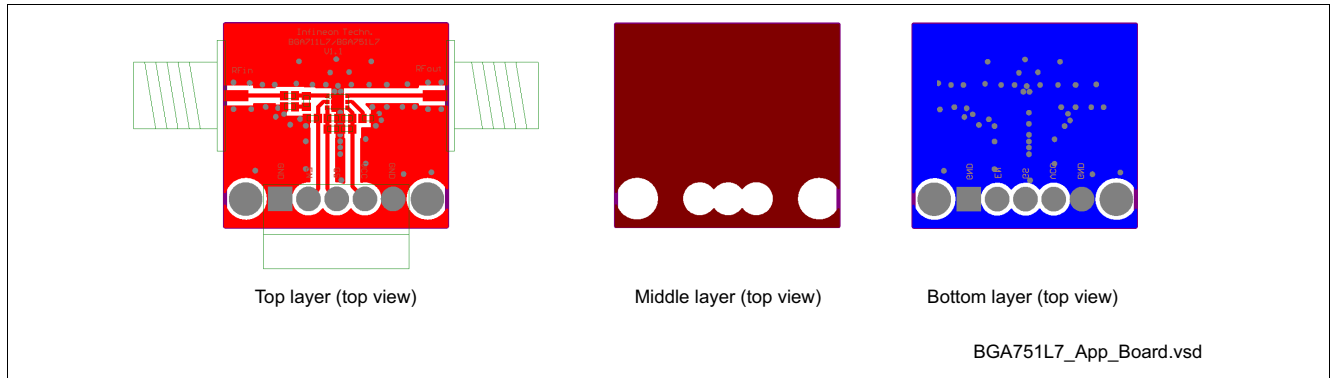


Figure 4 Application board layout on 3-layer FR4. Top layer thickness: 0.2 mm, bottom layer thickness: 0.8 mm, 17 μm Cu metallization, gold plated. Board size: 21 x 19 mm

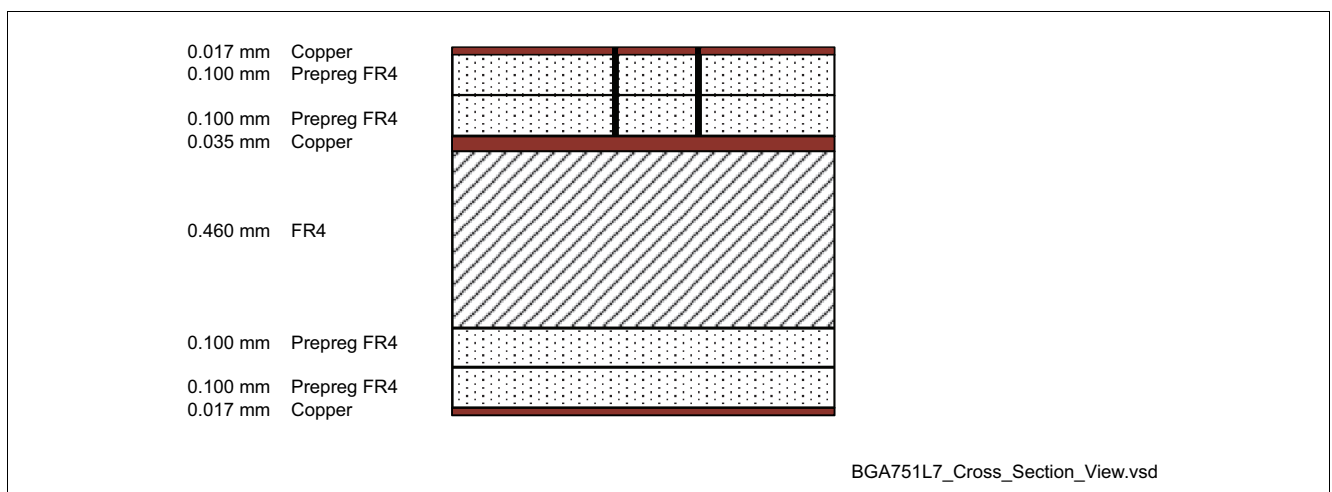


Figure 5 Cross-section view of application board

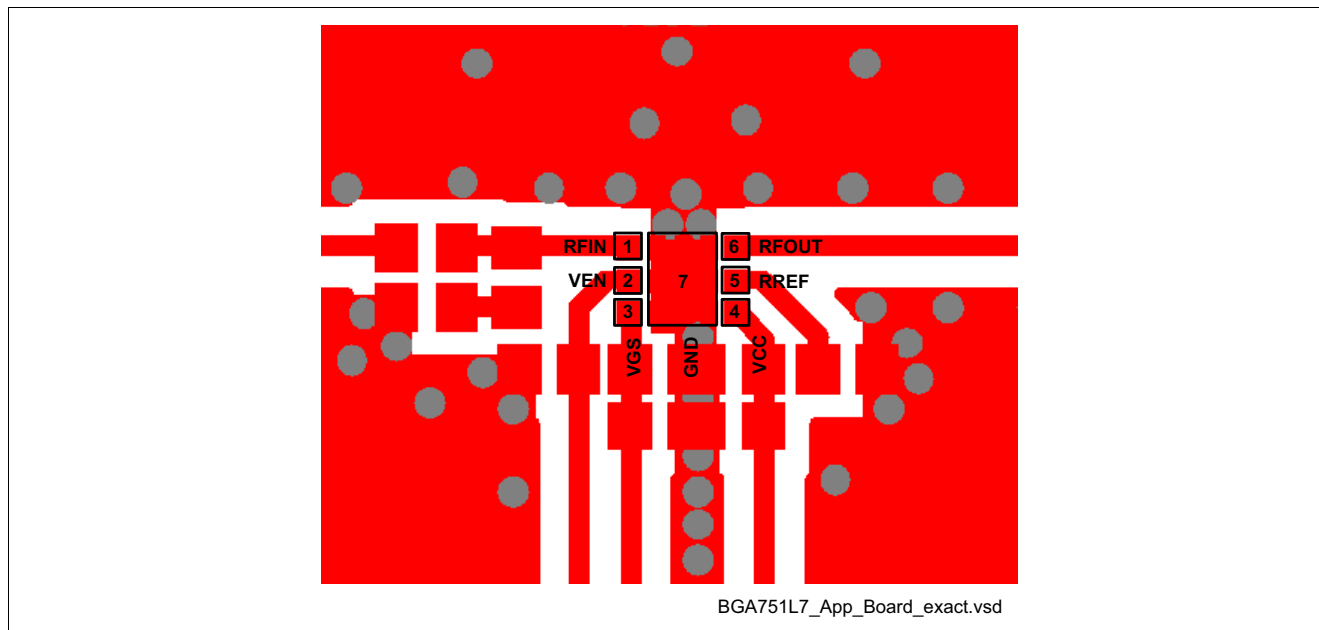


Figure 6 Detail of application board layout

Note: In order to achieve the same performance as given in this datasheet please follow the suggested PCB-layout as closely as possible. The position of the GND vias is critical for RF performance.

4 Physical Characteristics

4.1 Package Dimensions

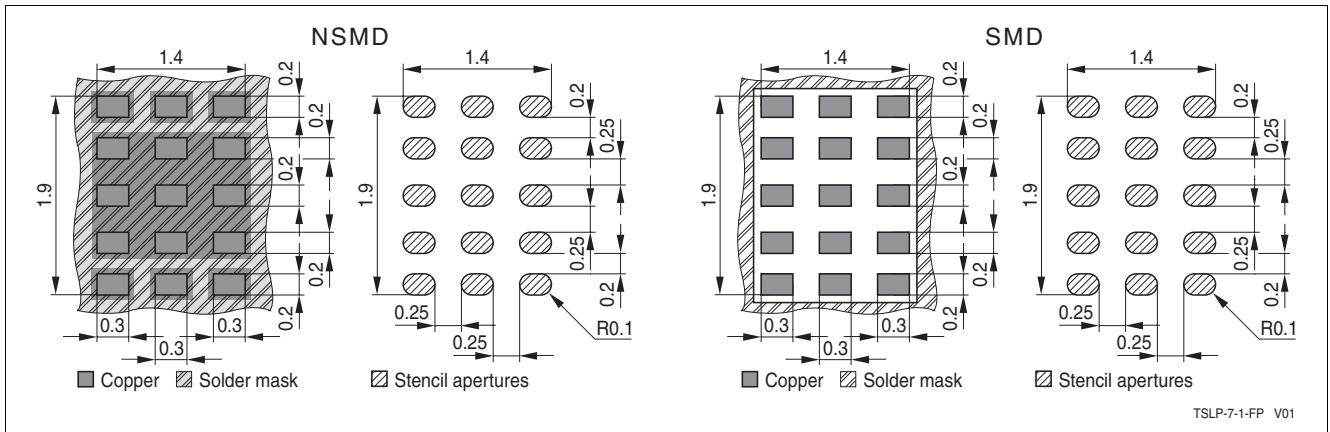


Figure 7 Recommended footprint and stencil layout for the TSLP-7-1 package

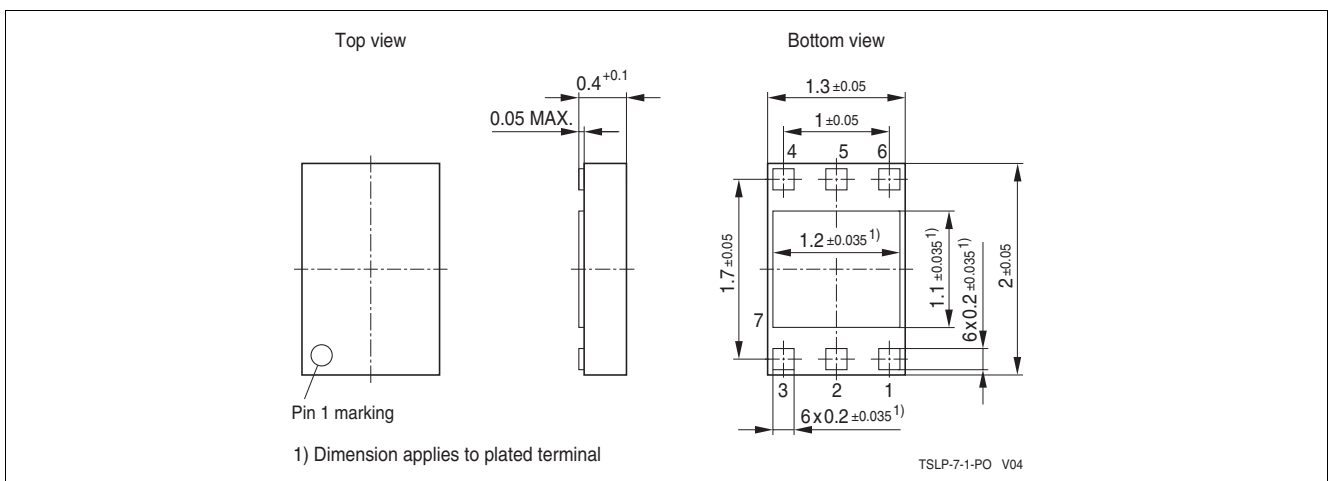


Figure 8 Package outline (top, side and bottom view)

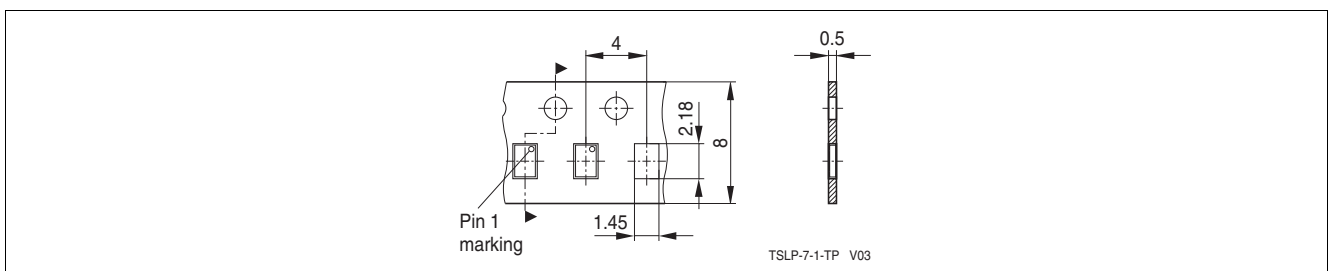


Figure 9 Tape & Reel Dimensions

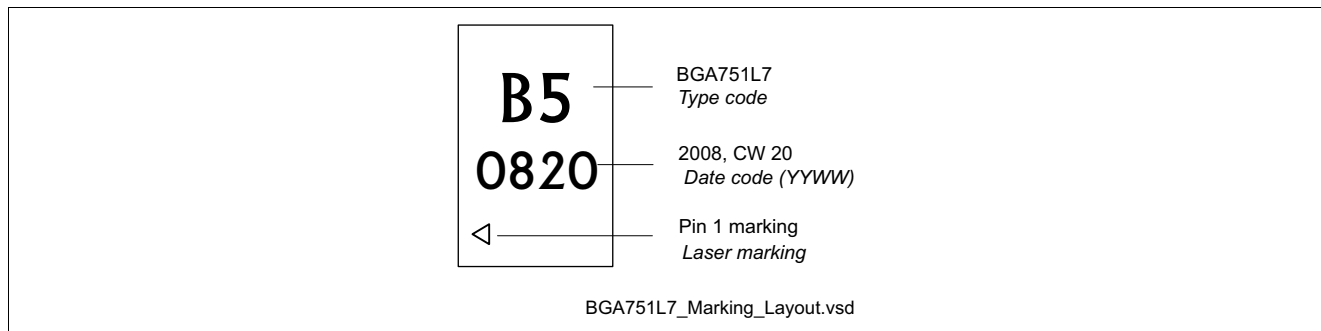


Figure 10 Marking Layout

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