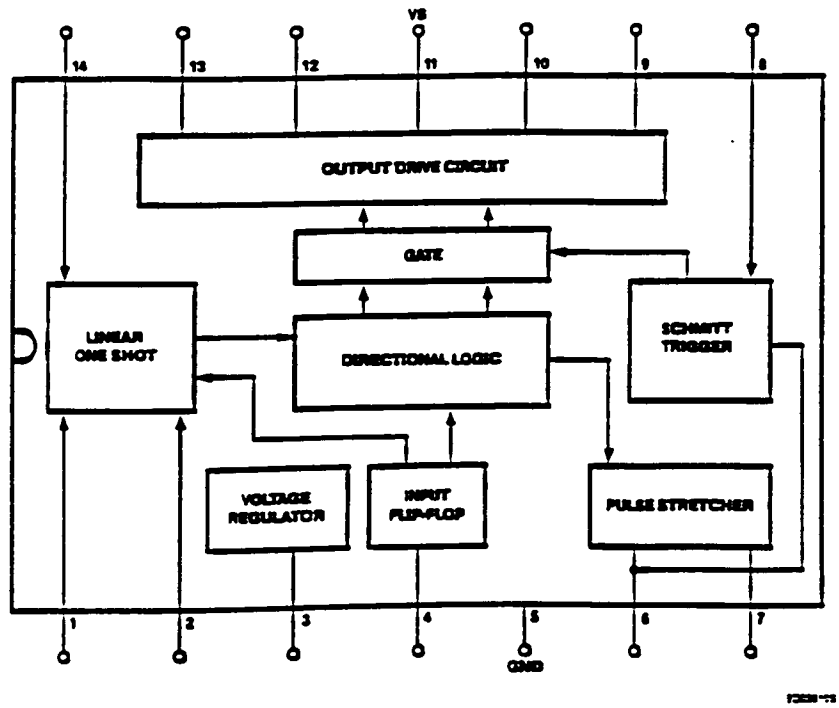
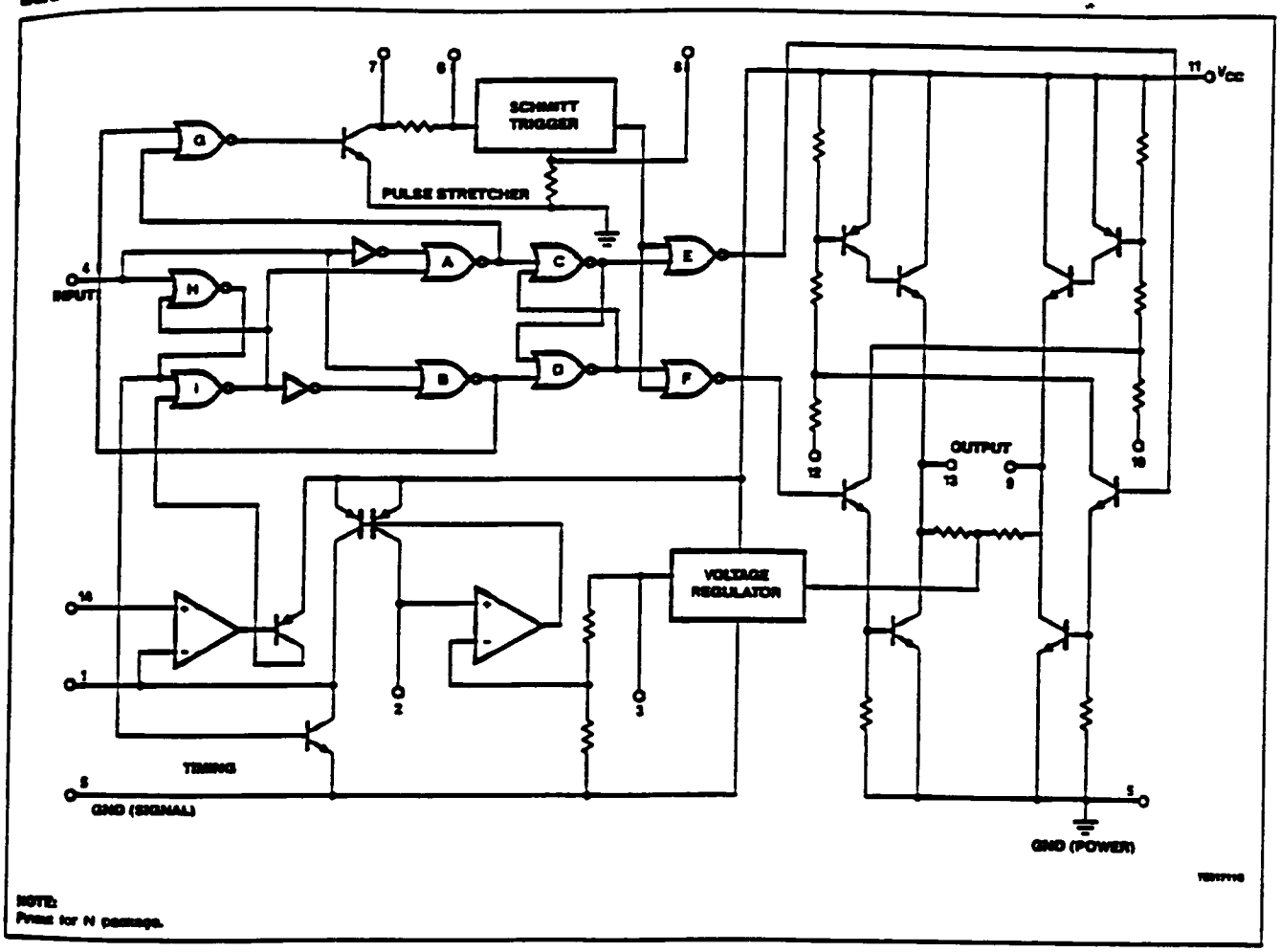
BLOCK DIAGRAM



NOTE:
Pinout for N package.

EQUIVALENT SCHEMATIC

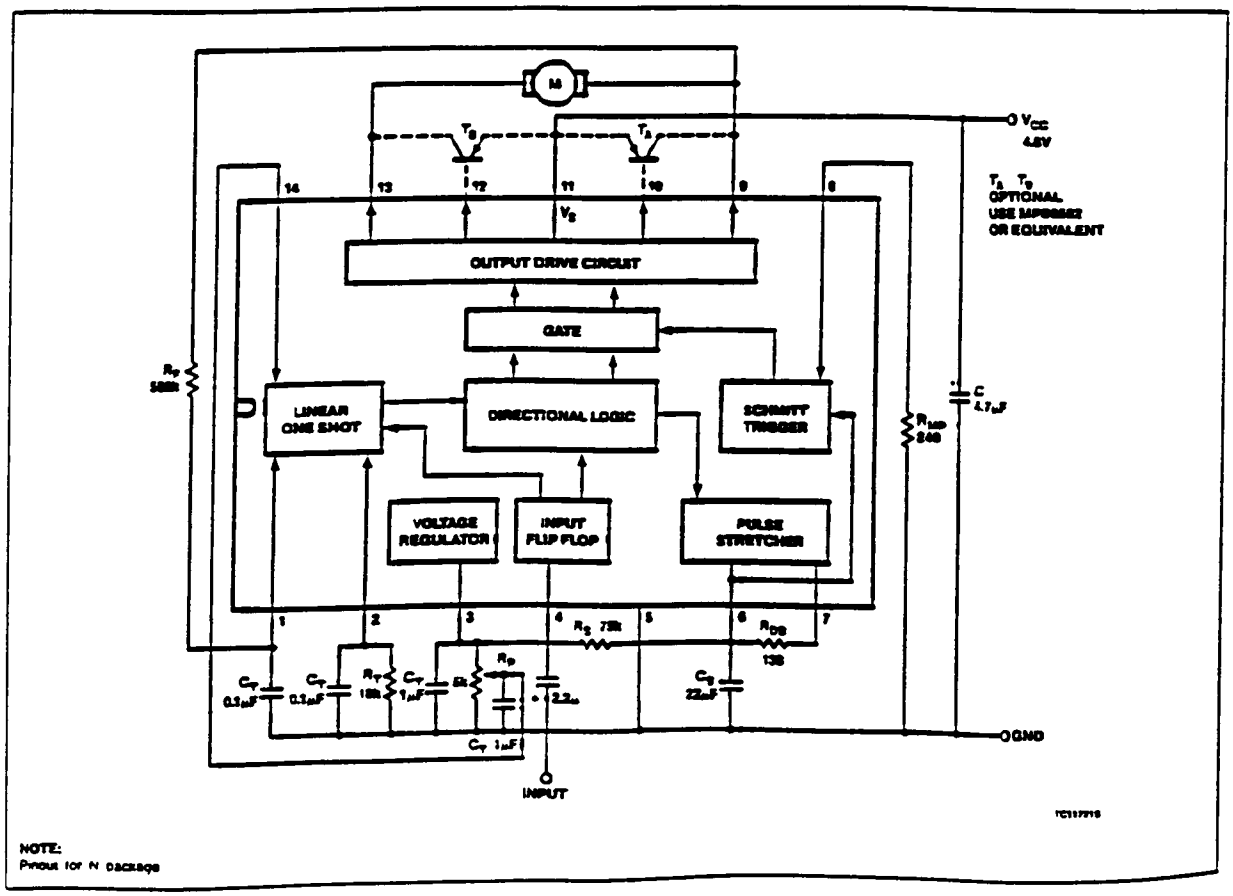


NOTE:
Pinout for N package.

DC ELECTRICAL CHARACTERISTICS $T_A = 25°C$, $V_S = 4.8V$ unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
V_{CC} I_{CC}	Supply voltage Supply current, Pin 11	Quiescent	3.2 4.2	4.8 5.5	6 10	V mA
V_{TH}	Input threshold, Pin 4 On Off			 1.5 1.4		V
Z_{IN}	Input resistance, Pin 4			18		kΩ
V_{OL} V_{OH}	Output voltage Low High	Pin 9 or 13, $I_L = 400mA$		 0.3 3.9		V
V_{REG}	Regulated voltage, Pin 3		2.1	2.5	2.9	V
ΔV_{REG}	Regulation, Pin 3 Minimum deadband, Pin 7 One-shot temperature coefficient	$3.9V < V_{CC} < 6V$ $R_{DB} = 0$		10 1 0.01		mV/V µs %/°C
	Standby output voltage PNP drive current	Pins 9 and 13 Pins 10 and 12		2.5 20		V mA

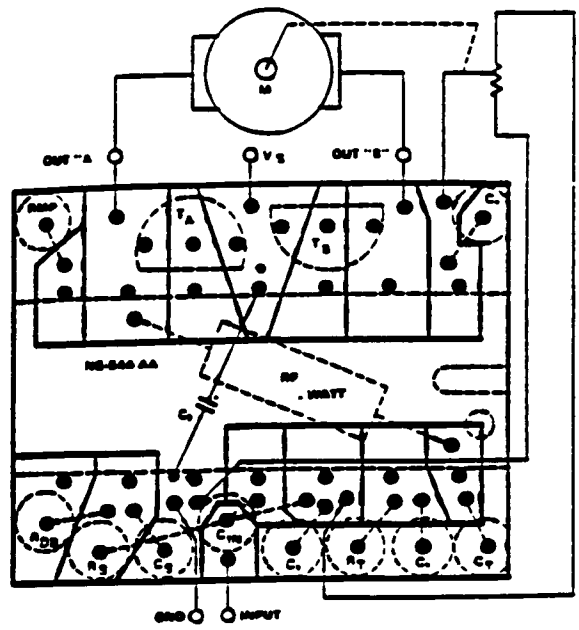
TYPICAL CONNECTION OF NE544N FOR LINEAR ONE-SHOT TIMING



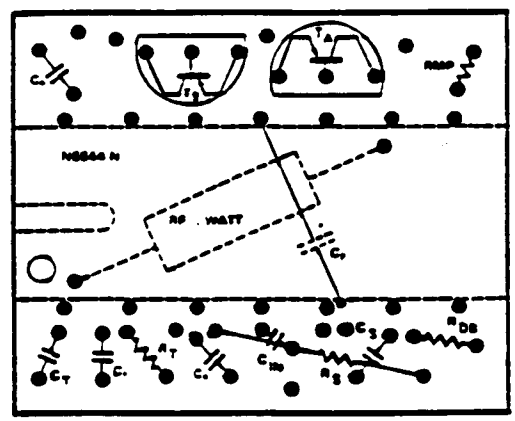
NOTE:
Pinout for N package

PC BOARD — N PACKAGE

PC BOARD—N PACKAGE



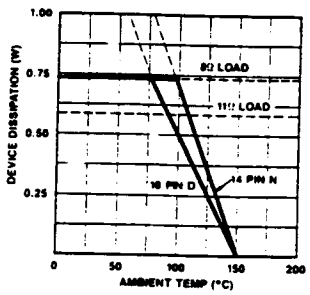
Typical PC Board Layout Bottom View



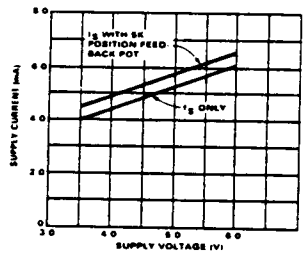
Parts Layout Top View

TYPICAL PERFORMANCE CHARACTERISTICS

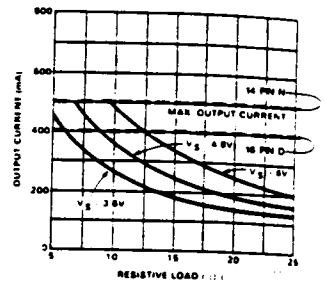
Maximum Dissipation vs Ambient Temperature



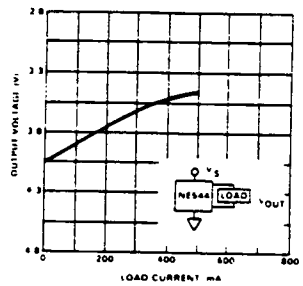
Supply Current vs Supply Voltage



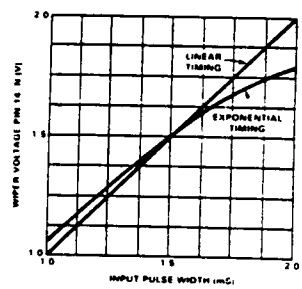
Output Current vs Load Resistance



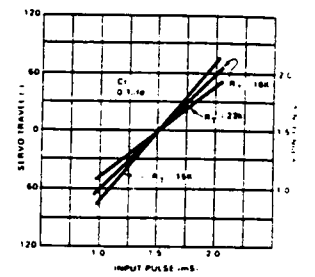
Output Voltage vs Load Current



Input Pulse Width vs Feedback Pot Output



Input Pulse vs Servo Travel



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