

### Resettable Fuse PTC SMD0805 Series

#### Features

RoHS Compliant & Halogen Free

faster tripping, 0805 Dimension, Surface mountable, Solid state

Operation Current: 0.05A~1.10A

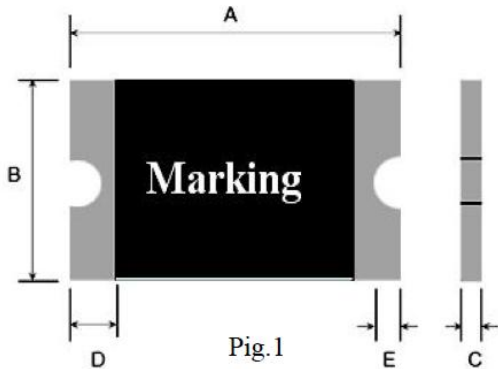
Maximum Voltage: 6V~24Vdc

Operating Temperature: -40°C to +85°C

Agency recognition:   



#### Dimensions(2012mm/ 0805 mils) Unit: mm



Terminal pad materials :Tin-Plated Nickle-copper  
Terminal pad solderability : Meets EIA specification  
RS 186-9E and ANSI/J-STD-002 Category 3.

Part number	Marking	A		B		C		D	E	Certification		Delivery Time	
		Min	max	Min	Max	Min	Max	Min	Min	UL	TUV	in stock	Produce
JK-SMD0805-005	1	2.00	2.20	1.20	1.50	0.45	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-010	1	2.00	2.20	1.20	1.50	0.40	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-010-24	1	2.00	2.20	1.20	1.50	0.40	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-020	2	2.00	2.20	1.20	1.50	0.40	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-025	2	2.00	2.20	1.20	1.50	0.40	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-030	3	2.00	2.20	1.20	1.50	0.30	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-035	3	2.00	2.20	1.20	1.50	0.30	1.00	0.20	0.1	√	√	3days	18days
JK-SMD0805-050	5	2.00	2.20	1.20	1.50	0.40	0.80	0.20	0.1	√	√	3days	18days
JK-SMD0805-075	7	2.00	2.20	1.20	1.50	0.50	1.20	0.20	0.1	√	√	3days	18days
JK-SMD0805-100	0	2.00	2.20	1.20	1.50	0.50	1.20	0.20	0.1	√	√	3days	18days
JK-SMD0805-110	0	2.00	2.20	1.20	1.50	0.50	1.20	0.20	0.1	√	√	3days	18days

### Thermal Derating Chart-IH(A)



Maximum ambient operating temperatures °C

Part Number	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
JK-SMD0805-005	0.07	0.06	0.055	0.05	0.04	0.035	0.03	0.025	0.015
JK-SMD0805-010	0.14	0.12	0.11	0.10	0.08	0.07	0.06	0.05	0.035
JK-SMD0805-010-24	0.14	0.12	0.11	0.10	0.08	0.07	0.06	0.05	0.035
JK-SMD0805-020	0.28	0.25	0.23	0.20	0.17	0.14	0.12	0.10	0.07
JK-SMD0805-025	0.35	0.31	0.29	0.25	0.21	0.18	0.15	0.13	0.09
JK-SMD0805-030	0.42	0.38	0.35	0.30	0.255	0.21	0.18	0.15	0.11
JK-SMD0805-035	0.47	0.44	0.39	0.35	0.30	0.27	0.24	0.20	0.14
JK-SMD0805-050	0.68	0.62	0.55	0.50	0.40	0.37	0.33	0.29	0.23
JK-SMD0805-075	1.00	0.90	0.79	0.75	0.63	0.57	0.53	0.41	0.34
JK-SMD0805-100	1.35	1.25	1.10	1.00	0.82	0.74	0.65	0.55	0.42
JK-SMD0805-110	1.45	1.35	1.20	1.10	0.92	0.84	0.75	0.65	0.52

### Electrical characteristics(25°C)

Part Number	I <sub>Hold</sub>	I <sub>Trip</sub>	V <sub>max</sub>	I <sub>max</sub>	P <sub>d</sub> Max	Maximum Time to Trip		Resistance (Ω)		Certification		Delivery Time	
	(A)	(A)	DC	A	W	Current (A)	Time (S)	R <sub>imin</sub>	R <sub>1max</sub>	UL	TUV	in stock	Produce
JK-SMD0805-005	0.05	0.15	24V	100A	0.5	0.5	1.50	1.50	20.0	√	√	3days	18days
JK-SMD0805-010	0.10	0.30	15V	100A	0.5	0.5	1.50	1.00	6.00	√	√	3days	18days
JK-SMD0805-010-24	0.10	0.30	24V	40A	0.5	1.5	1.00	6.00	6.00	√	√	3days	18days
JK-SMD0805-020	0.20	0.50	9V	100A	0.5	8.0	0.02	0.50	3.50	√	√	3days	18days
JK-SMD0805-025	0.25	0.50	6V	100A	0.5	8.0	0.02	0.45	3.20	√	√	3days	18days
JK-SMD0805-030	0.30	0.70	6V	100A	0.5	8.0	0.10	0.25	2.00	√	√	3days	18days
JK-SMD0805-035	0.35	0.75	6V	100A	0.5	8.0	0.10	0.25	1.20	√	√	3days	18days
JK-SMD0805-050	0.50	1.00	6V	100A	0.6	8.0	0.10	0.15	0.85	√	√	3days	18days
JK-SMD0805-075	0.75	1.50	6V	100A	0.6	8.0	0.20	0.09	0.385	√	√	3days	18days
JK-SMD0805-100	1.00	1.95	6V	100A	0.6	8.0	0.30	0.06	0.23	√	√	3days	18days
JK-SMD0805-110	1.10	2.20	6V	100A	0.6	8.0	0.30	0.06	0.21	√	√	3days	18days

I<sub>hold</sub> = Hold Current. Maximum current device will not trip in 25°C still air.

I<sub>trip</sub> = Trip Current. Minimum current at which the device will always trip in 25°C still air.

V<sub>max</sub> = Maximum operating voltage device can withstand without damage at rated current (I<sub>max</sub>).

I<sub>max</sub> = Maximum fault current device can withstand without damage at rated voltage (V<sub>max</sub>).

P<sub>d</sub> = Maximum power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.

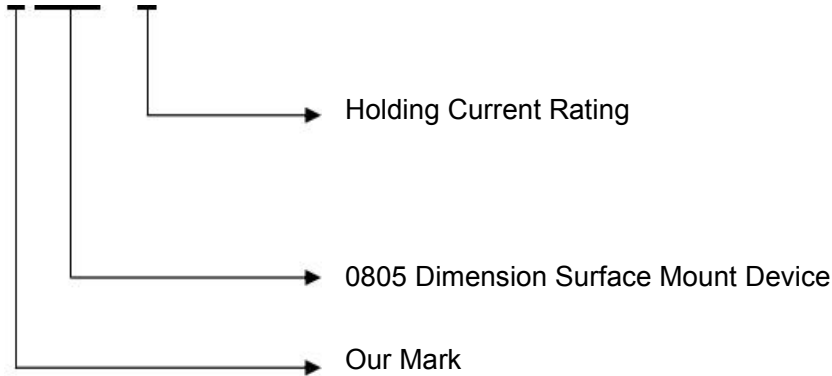
R<sub>imin/max</sub> = Minimum/Maximum device resistance prior to tripping at 25°C.

R<sub>1max</sub> = Maximum device resistance is measured one hour post reflow.

## Part number System



JK- SMD0805 -XXX



## Test Procedures and Requirements

Test	Test Conditions	Accept/Reject Criteria
Resistance	In still air @ 25°C	$R_{min} \leq R \leq R_{max}$
Time to Trip	Specified current, $V_{max}$ , 25°C	Tmaximum Time to Trip
Hold Current	30min ,at $I_H$	No trip
Trip Cycle Life	$V_{max}$ , $I_{max}$ , 100cycles	No arcing or burning
Trip Endurance	$V_{max}$ , 1hours	No arcing or burning

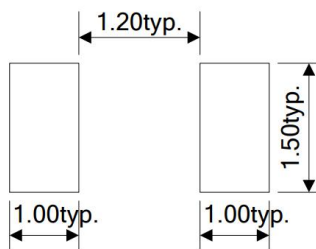
## Physical Characteristics

Terminal materials :	Tin-Plated Nickle-copper
Soldering zone	Meets EIA specification RS 186-9E and ANSI/J-STD-002 Category 3.

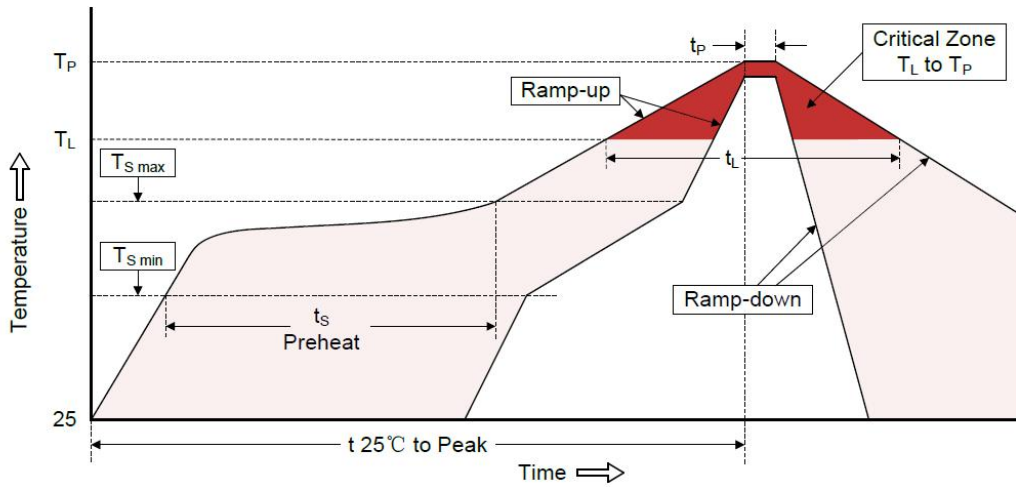
## Environmental Specifications

Test	Conditions	Resistance change
Passive aging	+85°C, 1000hours	±10%
Humidity aging	+85°C/85%R.H.1000hours	±5%
Thermal shock	MIL-STD-202,Method 107G ,+85°C/-40°C,20times	-30% typical resistance change
Solvent Resistance	MIL-STD-202,Method 215	No change
Vibration	ML-STD-883C,Test Condition A	No change

## Recommended Pad layout(mm)



Solder reflow conditions



Profile Feature	Pb-Free Assembly
Average ramp-up rate (TS max to TP)	3°C/second max.
Preheat	
-Temperature Min (TS min)	150°C
-Temperature Max (TS max)	200°C
-Time (min to max) (TS min to TS max)	60-180 seconds
Time maintained above:	
-Temperature (TL)	217°C
-Time (tL)	60-150 seconds
Peak Temperature (TP)	260°C
Time within 5°C of actual Peak Temperature (tP)	20-40 seconds
Ramp-down Rate	3°C/second max.
Time 25°C to Peak Temperature	8 minutes max.
Storage Condition	0°C~35°C, ≤70%RH

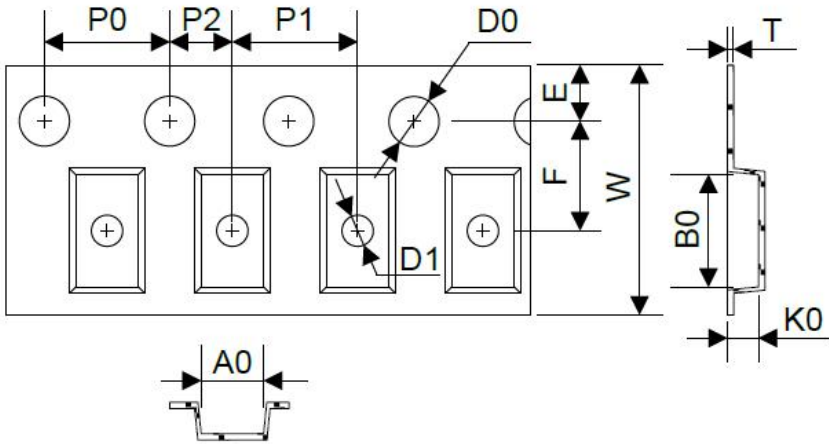
- Recommended reflow methods: IR, vapor phase oven, hot air oven, N2 environment for lead-free
- Devices are not designed to be wave soldered to the bottom side of the board.
- Recommended maximum paste thickness is 0.25mm (0.010 inch)
- Device can be cleaned using standard industry methods and solvents.
- Soldering temperature profile meets RoHS leadfree process.

Note : If reflow temperatures exceed the recommended profile, devices may not meet the performance Requirements

### Tape Specification and Reel Dimensions

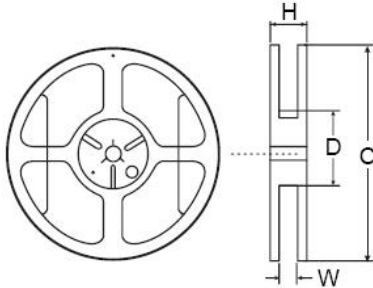


#### Tape



Symbol	Dimensions(mm)
W	8.00±0.30
F	3.50±0.05
E	1.75±0.10
D0	1.55±0.05
D1	1.00(MIN)
P0	4.00±0.10
P1	4.00±0.10
P2	2.00±0.05
A0	1.60±0.10
B0	2.30±0.10
T	0.25±0.10
K0	0.9/1.5±0.10
Leader min	390
Trailer min	160
C	Φ178±1.0
D	Φ60.2±0.5
H	11.0±0.5
W	9.0±1.5

#### Reel



#### Packaging Quantity

Part Number	Quantity	Part Number	Quantity
JK-SMD0805-005	5000PCS	JK-SMD0805-035	5000PCS
JK-SMD0805-010	5000PCS	JK-SMD0805-050	5000PCS
JK-SMD0805-010-24	5000PCS	JK-SMD0805-075	4000PCS
JK-SMD0805-020	5000PCS	JK-SMD0805-100	4000PCS
JK-SMD0805-025	5000PCS	JK-SMD0805-110	4000PCS
JK-SMD0805-030	5000PCS	--	--

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## Storage

The maximum ambient temperature shall not exceed 38°C. Storage temperatures higher than 38°C could result in the deformation of packaging materials. The maximum relative humidity recommended for storage is 60%. High humidity with high temperature can accelerate the oxidation of the solder plating on the termination and reduce the solderability of the components. Sealed plastic bags with desiccant shall be used to reduce the oxidation of the termination and shall only be opened prior to use. The products shall not be stored in areas where harmful gases containing sulfur or chlorine are present.

## Warning

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- Use PPTC with a large inductance in circuit will generate a circuit voltage ( $L di/dt$ ) above the rated voltage of the PPTC.
- Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.
- Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.

## Notes

The specification is intended to present application, product and technical data to assist the user in selecting PPTC circuit production devices. However, users should independently evaluate and test the suitability of each product. HUAAN makes no warranties as to the accuracy or completeness of the information and disclaims any liability resulting from its use, HUAAN's only obligations are those in the HUAAN Standard Terms and Conditions of Sale and in no case will HUAAN be liable for any incidental, indirect, or consequential damages arising from the sale, resale, or misuse of its products. HUAAN reserves the right to change or update, without notice, any information contained in this specification.