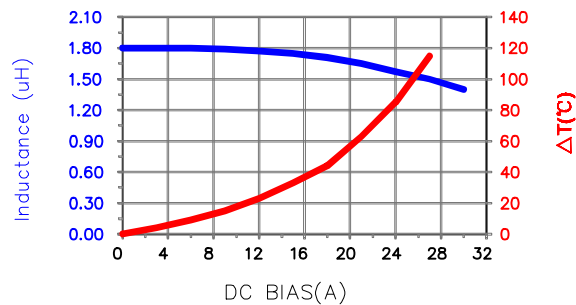


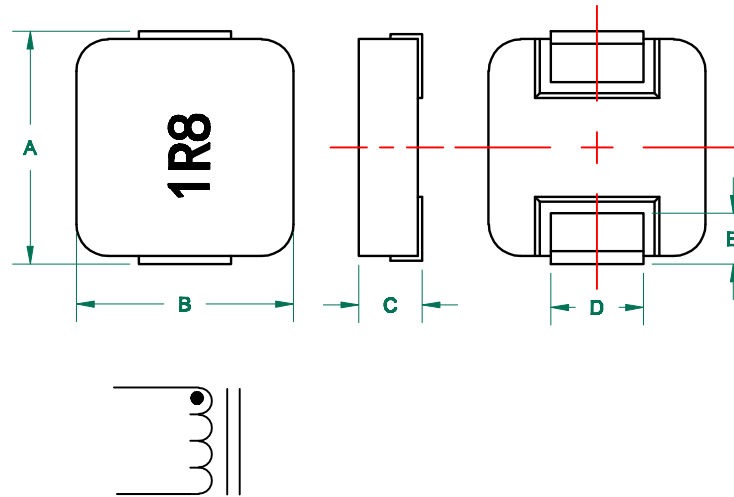
**UNCONTROLLED
DOCUMENT**

Diagram illustrating the layout of two square panels. The left panel is 2.90 units wide. The right panel is 2.90 units wide and 5.00 units high. The total width of the two panels is 7.90 units.



	Min	Nom	Max
INDUCTANCE (uH) L @ 100 KHZ/0.25V ± 20%	1.44	1.80	2.16
DCR (Ω)			0.007

Saturation Current ³ Isat (A)	30.00
Temperature Rise Current Irms ⁴ (A)	16.50



- NOTES: UNLESS OTHERWISE SPECIFIED
- 1.COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
 - 2.OPERATION TEMPERATURE RANGE:
-40°C~+125°C (INCLUDING SELF-HEATING).
 - 3.SATURATION CURRENT I_{sat} IS DEFINED AS MAXIMUM AMOUNT OF CURRENT BY WHICH INDUCTANCE WILL DROP BY TYPICAL VALUE OF 25% OF INITIAL INDUCTANCE ($T_a=25\pm5^\circ\text{C}$).
 - 4.TEMPERATURE RISE CURRENT (I_{rms}):DC CURRENT THAT CAUSES THE TEMPERATURE RISE ($\Delta T \leq 40^\circ\text{C}$) FROM 25°C AMBIENT.

DIMENSIONS ARE IN mm.				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved. Laird				
				PROJECT/PART NUMBER:		REV	PART TYPE:	DRAWN BY:
				MGV12031R8M-10		A	POWER INDUCTOR	QIU
				DATE: 05/14/13		SCALE: NTS		SHEET:
A	ORIGINAL DRAFT	05/14/13	QIU	CAD #		TOOL #		1 of 1
REV	DESCRIPTION	DATE	INT	MGV12031R8M-10-A		-		