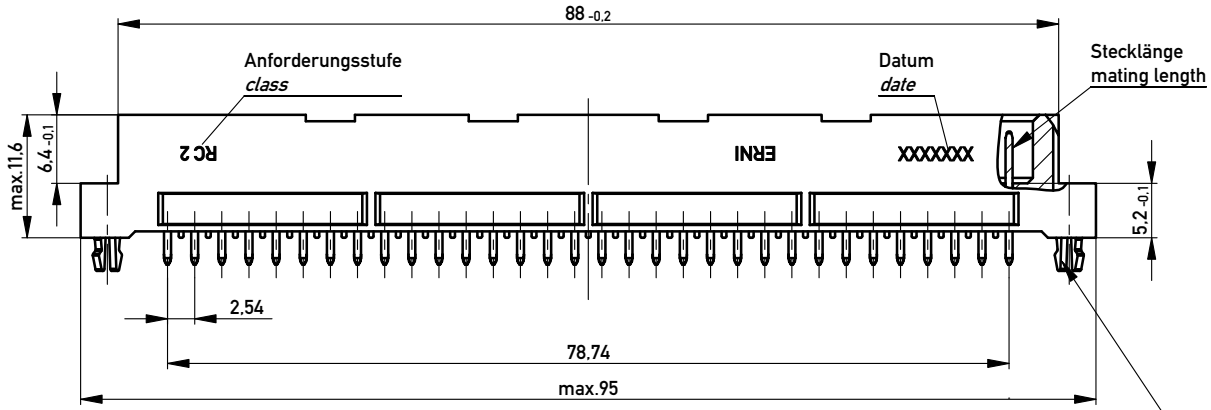
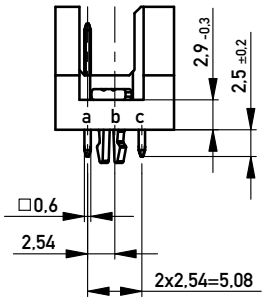
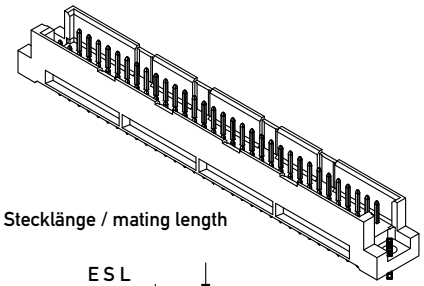
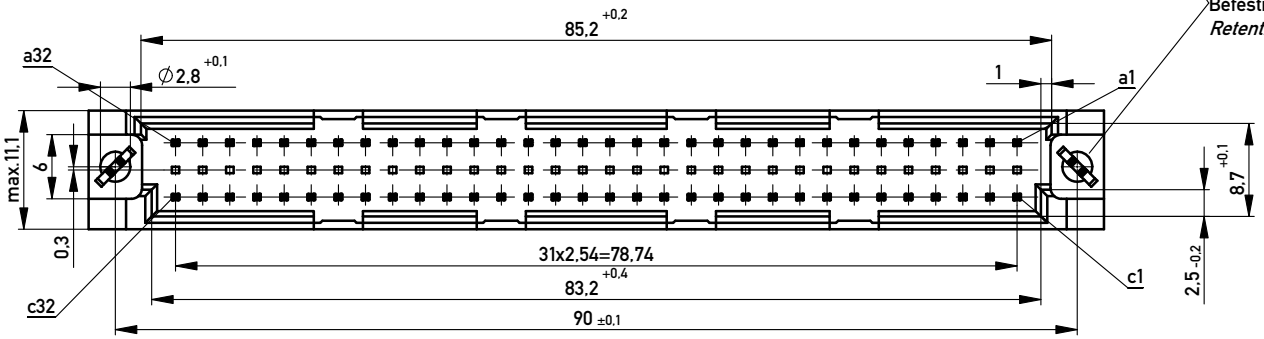
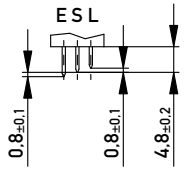






Stecklänge / mating length



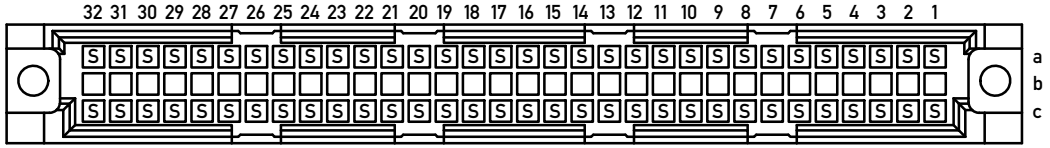


E = *early mate, late break* / voreilend

S = standard

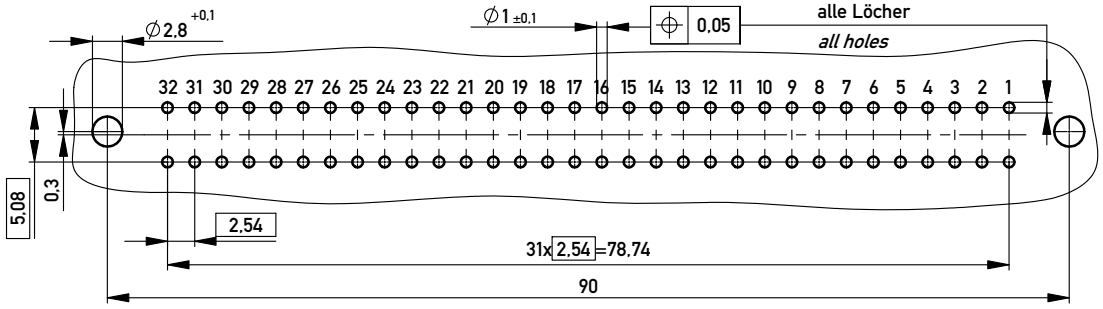
L = late mate, early break / nacheilend

Bestückungsplan / *contact layout*



THR = *through hole reflow*
Bauart 4 / t*ype 4*
Anforderungsstufe 2 = 400 Steckzyklen */ performance class 2 = 400 mating cycles*
Fehlende Maße und Angaben nach IEC 60 603-2 / DIN EN 60 603-2
Missing information and dimensions per IEC 60 603-2 / DIN EN 60 603-2

Leiterplatten-Lochbild / *PCB drillhole pattern*



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Information:	Tolerances	Scale 2:1	
	All Dimensions in mm		
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Index Date			A3
		Class STVR	