

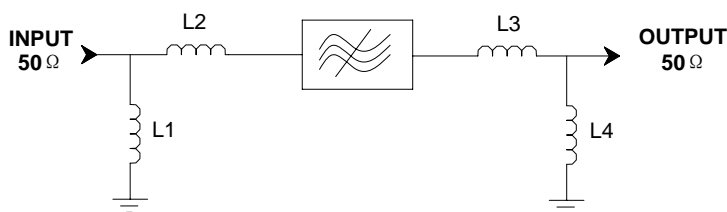
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	79.875	80	80.125
Insertion Loss	dB	-	27.8	31
2 dB Bandwidth	MHz	24.1	24.18	-
3 dB Bandwidth	MHz	-	24.26	-
20 dB Bandwidth	MHz	-	24.77	24.8
30 dB Bandwidth	MHz	-	24.91	25.2
40 dB Bandwidth	MHz	-	25	25.6
50 dB Bandwidth	MHz	-	25.1	26
Passband Variation	dB	-	1.4	2
Absolute Delay	usec	-	3.7	4.5
Ultimate Rejection	dB	50	52	-
Material Temperature coefficient	KHz/°C	-7.52		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=L4=120nH
L2=39nH L3=56nH
Source/Load Impedance=50 ohm

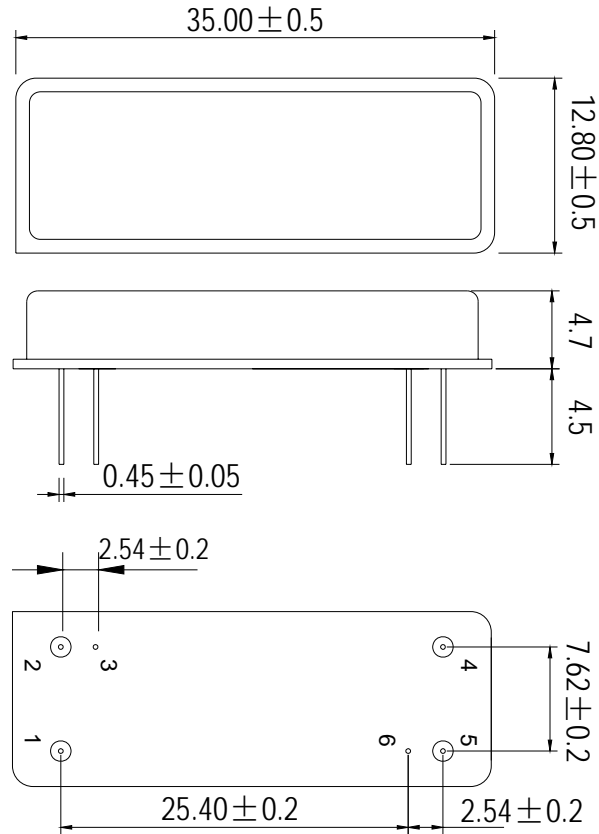
Notes - Component values may change depending on board layout.



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Package Dimension



Input	1
Output	5
Ground	2,3,4,6

Package: DIP3512

Unit: mm

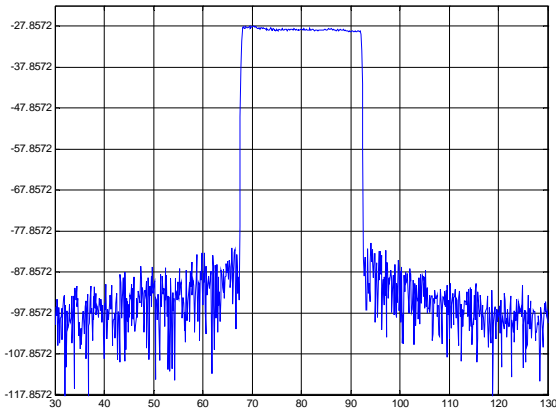


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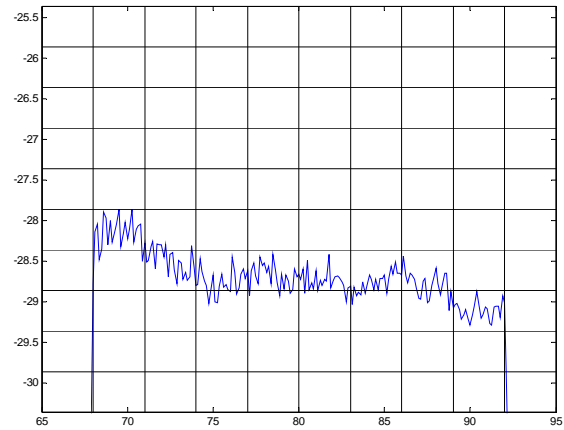
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Typical Performance

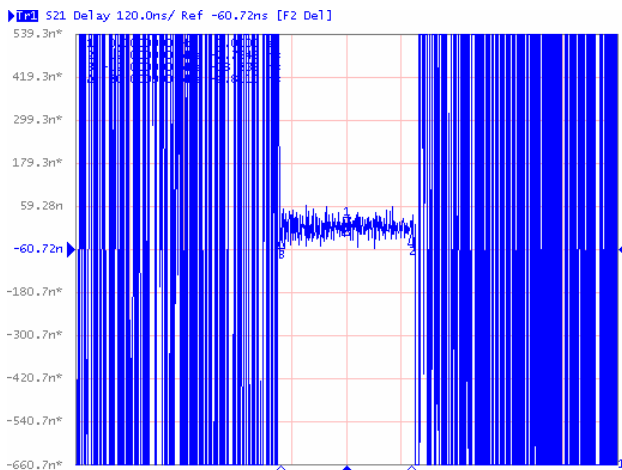
Frequency Respond



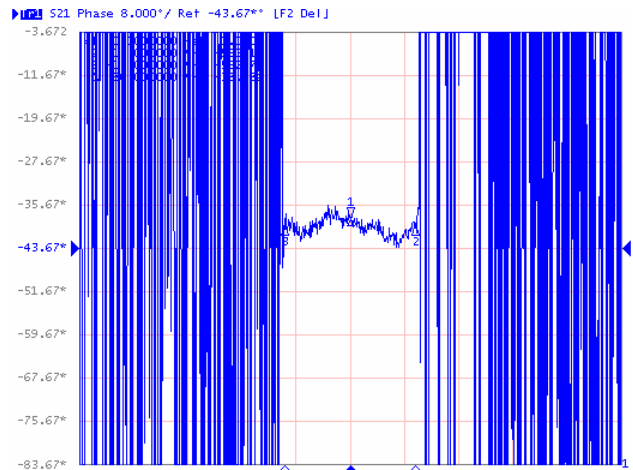
Passband Respond



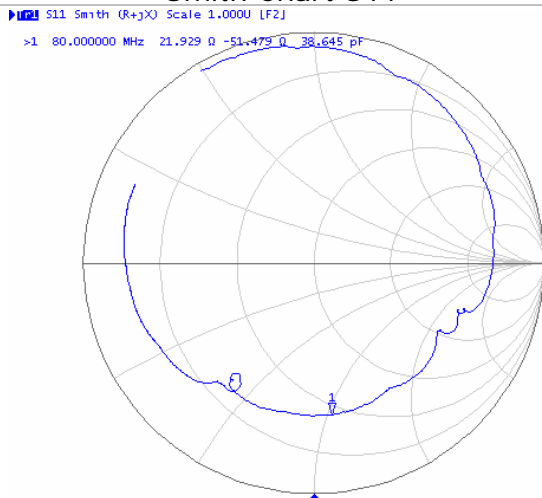
Group Delay Variation($f_0 \pm 12\text{MHz}$)



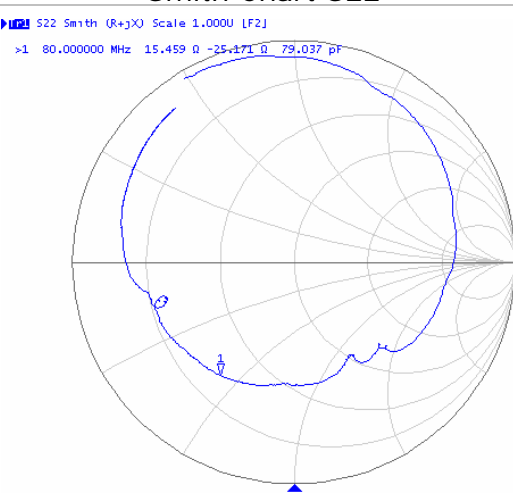
Phase Linearity($f_0 \pm 12\text{MHz}$)



Smith Chart S11



Smith Chart S22



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