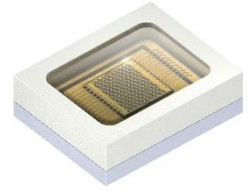


# V107C000A-850

## BIDOS® P2835 C

VCSEL pulsed laser in small footprint ceramic



### Applications

- Virtual Reality
- 3D Sensing
- Industrial Automation
- Access Control (IRIS/Vein Scan, Face Recognition)
- Augmented Reality, Mixed Reality
- Flash & Autofocus
- Gesture Recognition

### Features:

- Package: SMD ceramic package with glass diffuser window
- Chip Technology: GaAs VCSEL power array
- IR Laser Wavelength: 850 nm
- Optical Power Class: 4 W pulsed
- Radiation Profile: 23°x23° (Epoxy molding)
- ESD: 8 kV acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 3B)

### Ordering Information

Type	Operational Mode: $I_F = 5A$ , $T_S = 25^\circ C$ $t_p = 100\mu S$ , DC = 1%	Ordering Code
V107C000A-850	4W	Q65113A0124

Depending on the mode of operation, these devices emit highly concentrated visible and non-visible light which can be hazardous to the human eye. Products which incorporate these devices must follow the safety precautions given in the “Notes” section.

## Maximum Ratings

$T_a = 25^\circ\text{C}$

Parameter	Symbol		Values
Operation/Solder temperature	$T_s$	min.	$-40^\circ\text{C}$
$t_p = 100\ \mu\text{s}$ ; DC = 1 %		max.	$85^\circ\text{C}$
Storage temperature	$T_{\text{stg}}$	min.	$-40^\circ\text{C}$
		max.	$85^\circ\text{C}$
Forward current	$I_f$	max.	13 A
Pulsed operation; $T_p = 100\ \mu\text{s}$ ; DC = 1 %; $T_s = 25^\circ\text{C}$			
Forward current	$I_f$	max.	8.5 A
Direct current operation; DC = 100%; $T_s = 25^\circ\text{C}$			
Reverse Voltage	Not designed for reverse operation		
Reflow soldering temperature	$T_{\text{Ref}}$	max.	$260^\circ\text{C}$
ESD withstand voltage	$V_{\text{ESD}}$	max.	8 kV
acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 3B)			

Note: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device.

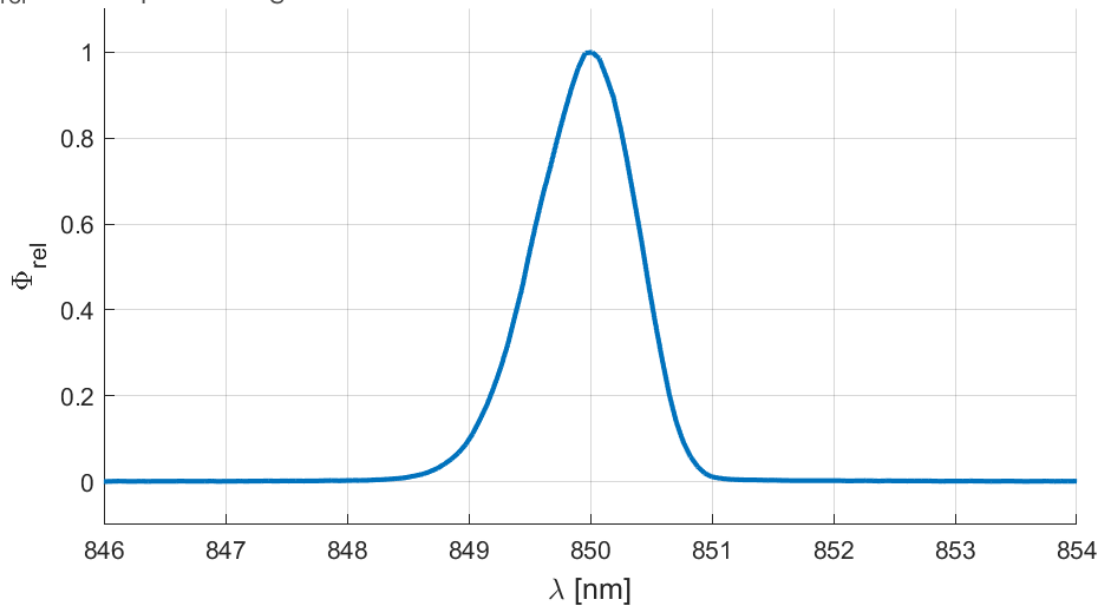
## Characteristics

$T_a = 25^\circ\text{C}$ ,  $I_F = 5\text{ A}$ ;  $t_p = 100\mu\text{s}$ ; DC = 1%

Parameter	Symbol		Values
Forward voltage	$V_F$	typ.	2.3 V
Output power	$\Phi$	typ.	4.0 W
Threshold current	$I_{th}$	typ.	1.40 A
Slope efficiency	SE	typ.	1.1 W / A
Power conversion efficiency	$\eta$	typ.	35 %
Peak wavelength	$\lambda_{peak}$	min.	840 nm
		typ.	850 nm
		max.	860 nm
Spectral bandwidth at FWHM (50% of $\Phi_{max}$ )	$\lambda_{FWHM}$	typ.	2 nm
Temperature coefficient of wavelength	$TC_\lambda$	typ.	0.06 nm / K
Field of view at FWHM (HFOV)	$\theta_{  }$	typ.	23 °
Field of view at FWHM (VFOV)	$\theta_{\perp}$	typ.	23 °
Thermal resistance junction/solder point	$R_{th, JS}$	typ.	15.9 K / W

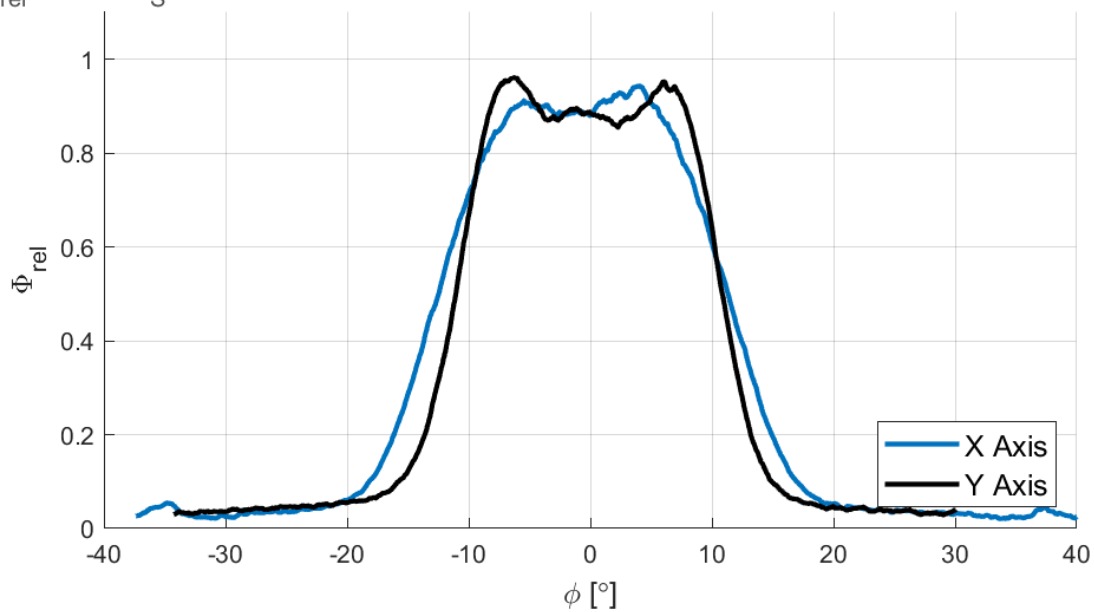
## Relative Spectral Emission <sup>1)</sup>

$$\Phi_{\text{rel}} = f(\lambda); I_F = 5 \text{ A}; T_S = 25 \text{ }^\circ\text{C}$$



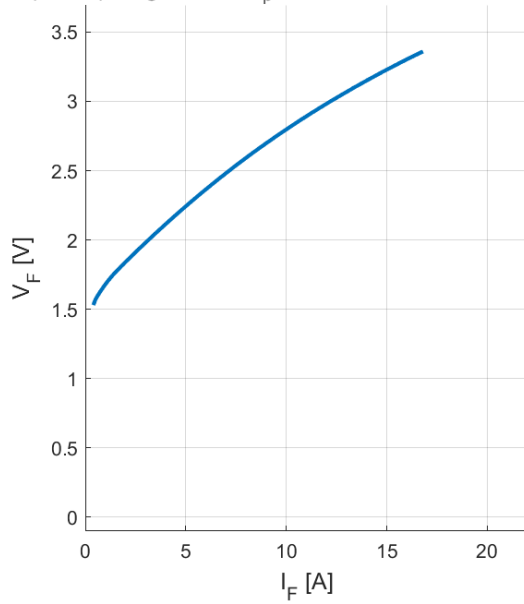
## Radiation Characteristics <sup>1)</sup>

$$\Phi_{\text{rel}} = f(\phi); T_S = 25 \text{ }^\circ\text{C}$$



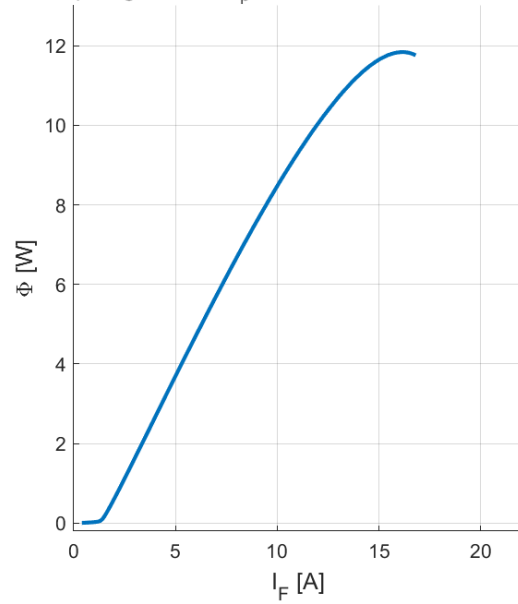
### Forward Voltage <sup>1) 2)</sup>

$$V_F = f(I_F); T_S = 25\text{ °C}; t_p = 100\ \mu\text{s}; \text{DC} = 1\%$$



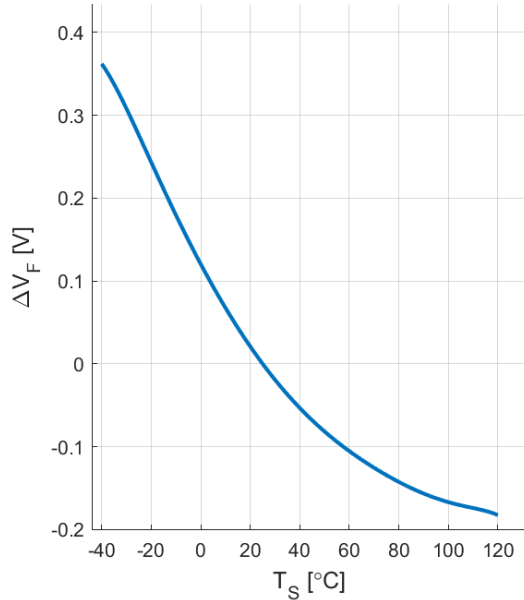
### Optical Output Power <sup>1) 2)</sup>

$$\Phi = f(I_F); T_S = 25\text{ °C}; t_p = 100\ \mu\text{s}; \text{DC} = 1\%$$



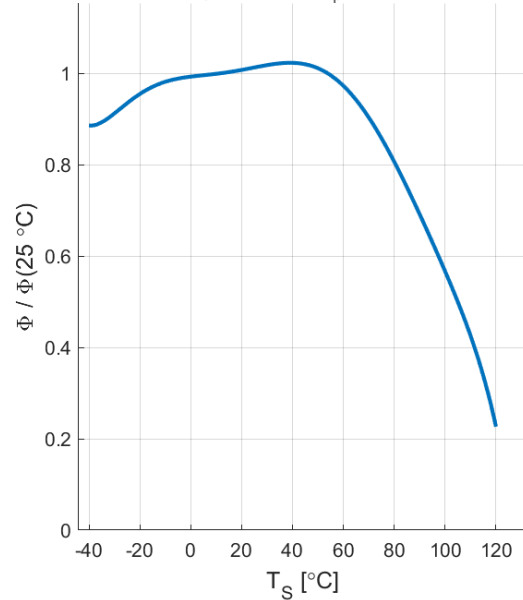
### Relative Forward Voltage <sup>1)</sup>

$$\Delta V_F = V_F - V_F(25\text{ °C}) = f(T_S); I_F = 5\text{ A}$$



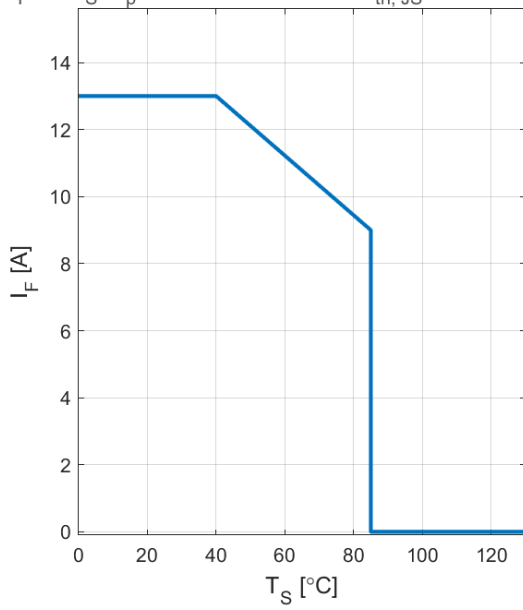
### Relative Radiant Power <sup>1)</sup>

$$\Phi / \Phi(25\text{ °C}) = f(T_S); I_F = 5\text{ A}; t_p = 100\ \mu\text{s}; \text{DC} = 1\%$$



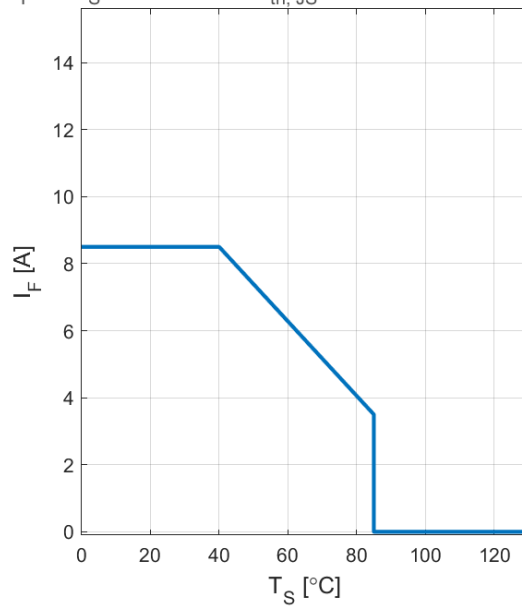
### Max Permissible Pulse Current

$$I_F = f(T_S); t_p = 100 \mu\text{s}; \text{DC} = 1\%; R_{th, JS} = 15.9 \text{ K/W}$$

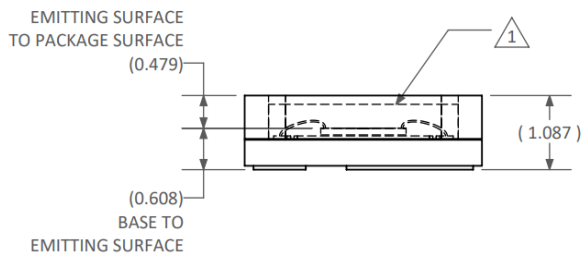
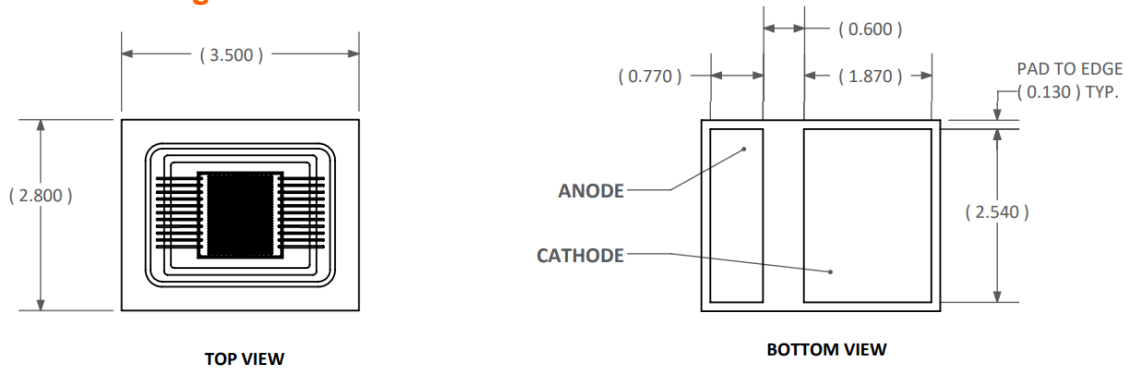


### Max Permissible Current

$$I_F = f(T_S); \text{DC} = 100\%; R_{th, JS} = 15.9 \text{ K/W}$$



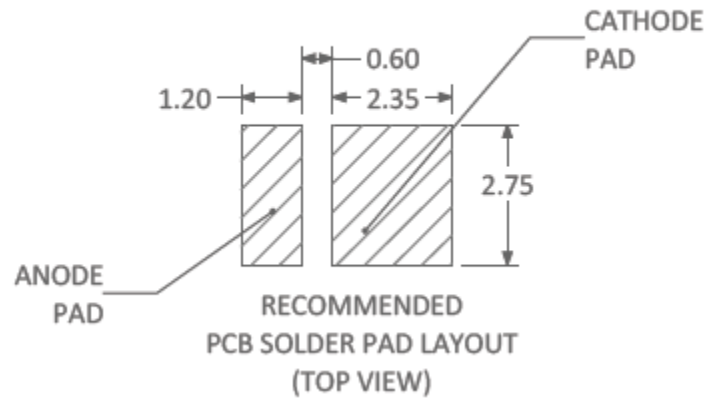
### Dimension Drawings <sup>3)</sup>



### Further Information

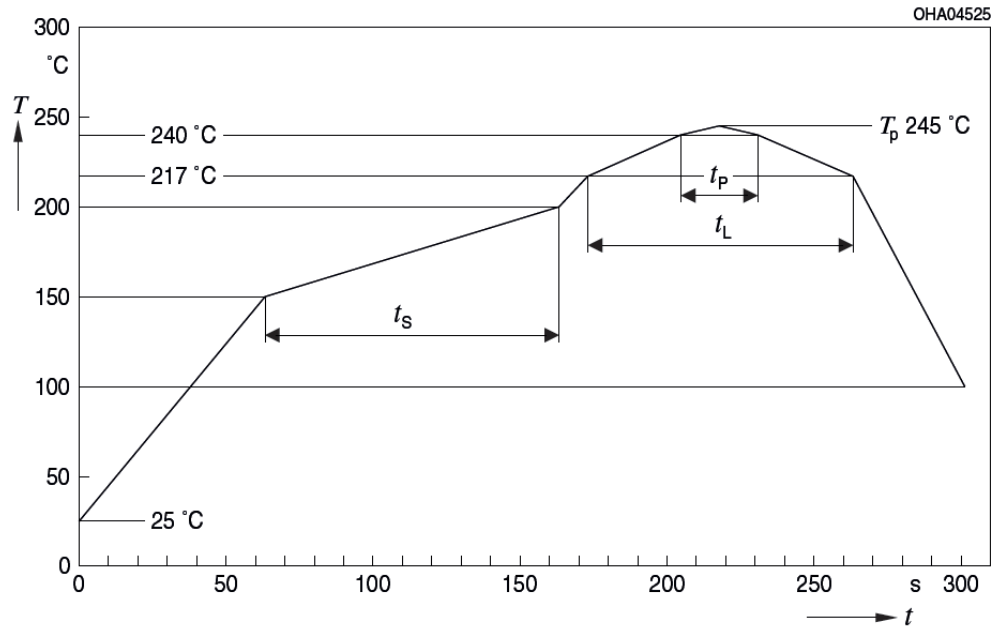
Approximate Weight: 38 mg

### Recommended Solder Pad Layout <sup>3)</sup>



## Reflow Soldering Profile

Product complies to MSL Level 2 acc. to JEDEC J-STD-020E



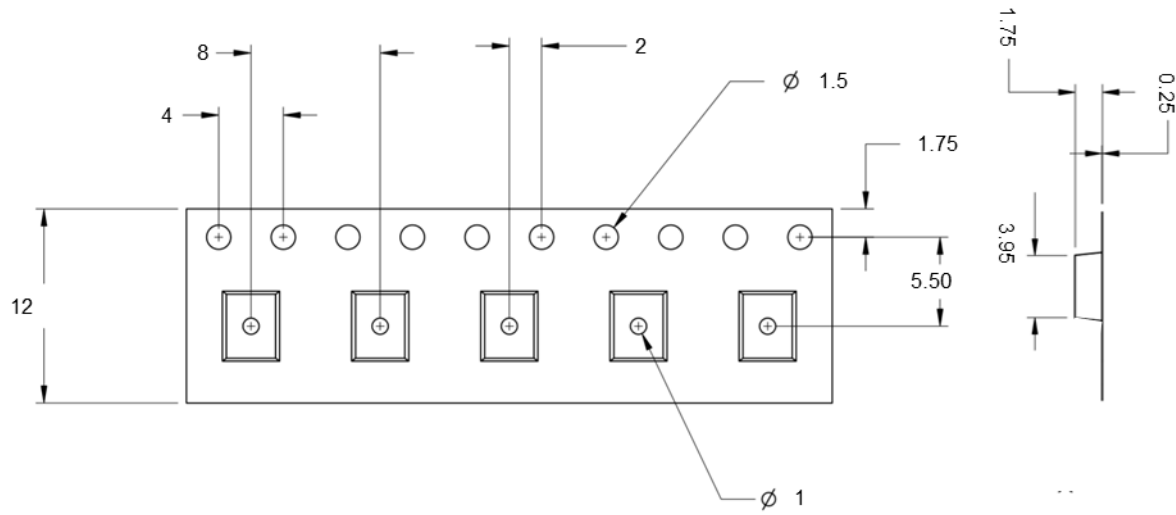
Profile Feature	Symbol	Pb-Free (SnAgCu) Assembly			Unit
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat*) 25°C to 150°C			2	3	K/s
Time $t_s$ $T_{smin}$ to $T_{smax}$	$t_s$	60	100	120	s
Ramp-up rate to peak*) $T_{smax}$ to $T_P$			2	3	K/s
Liquidus temperature	$T_L$		217		°C
Time above liquidus temperature	$t_L$		45	60	s
Peak temperature	$T_P$		245	260	°C
Time within 5 °C of the specified peak temperature $T_P - 5$ K	$t_p$	10	20	30	S
Ramp-down rate* $T_P$ to 100°C			3	6	K/s
Time 25°C to $T_P$				480	s

All temperatures refer to the center of the package, measured on the top of the component

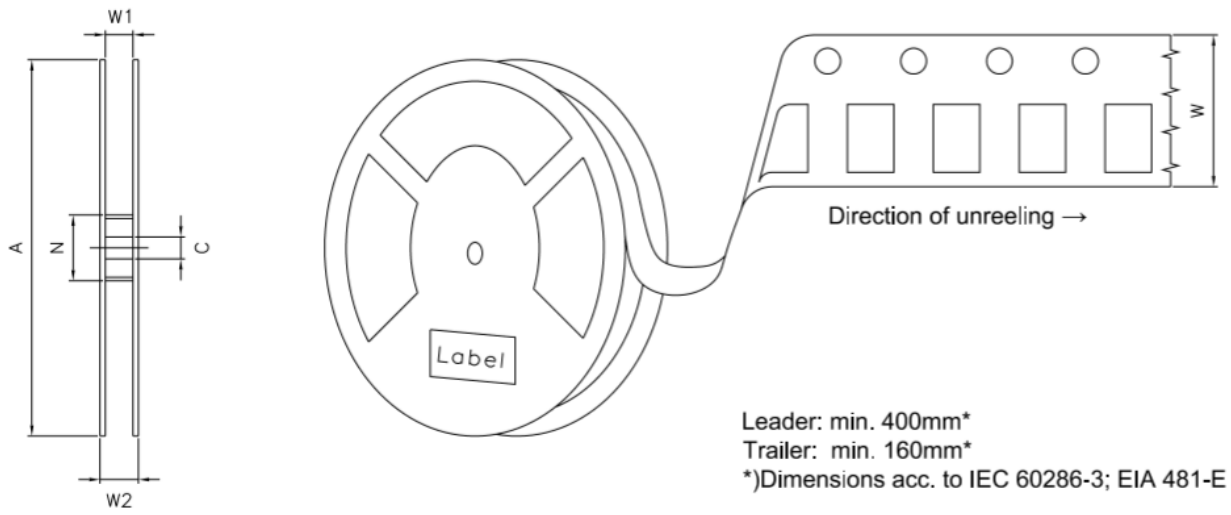
\*slope calculation  $DT/Dt$ :  $Dt$  max. 5s; fulfillment for the whole T-range



## Taping <sup>3) 4)</sup>



## Tape and Reel <sup>4)</sup>



### Reel dimensions [mm]

A	W	N <sub>min</sub>	W <sub>1</sub>	W <sub>2 max</sub>	Pieces per PU
330 mm	12 + 0.3 / - 0.1	60	12.4 + 2	18.4	2500

## Barcode-Product-Label (BPL)

**Vixar**  
a company of  
**OSRAM**  
Opto Semiconductors

LX XXXX BIN1:XX-XX-X-XXX-X

RoHS Compliant

(6P) BATCH NO: 1234567890

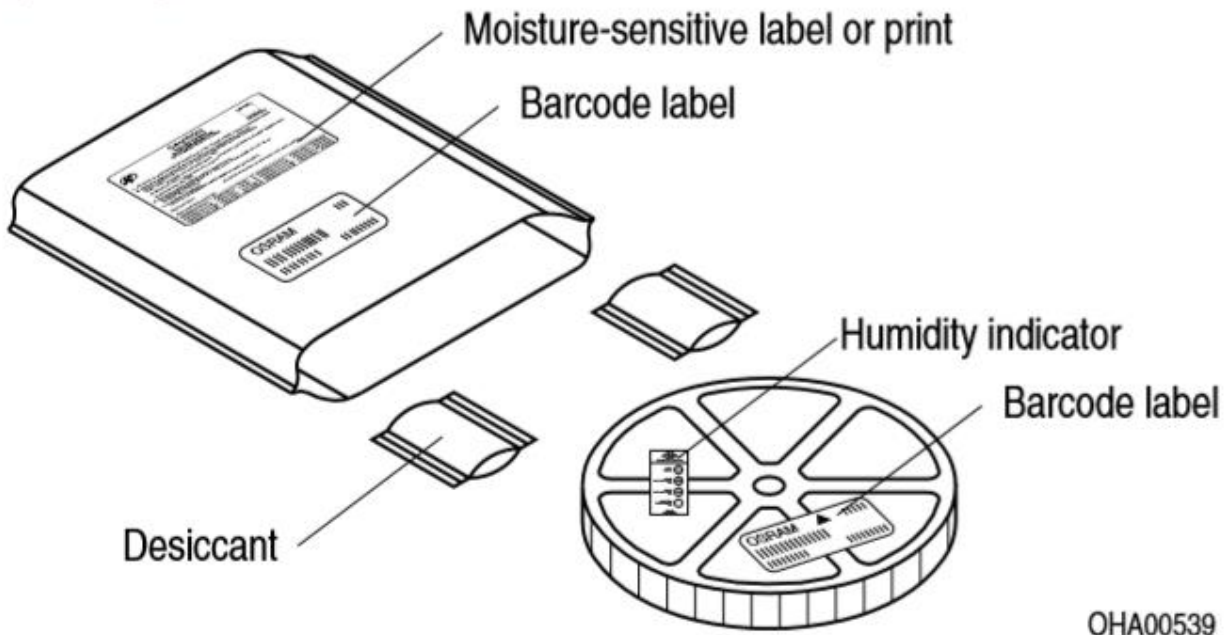
(1T) LOT NO: 1234567890 (9D) D/C: 1234

ML TEMP ST  
2 260°C

Pack: RXX  
DEMY XXX  
X\_X123\_1234.1234 X

(X)PROD NO:123456789 (Q)QTY: 9999 (G)Group:XX-XX-X-X

## Dry Packing Process and Materials



Moisture-sensitive product is packed in a dry bag containing desiccant and a humidity card according JEDEC-STD-033.

## Notes

Depending on the mode of operation, these devices emit highly concentrated visible and non-visible light which can be hazardous to the human eye. Products which incorporate these devices must follow the safety precautions given in IEC 60825-1.

Subcomponents of this device contain, in addition to other substances, metal filled materials including silver. Metal filled materials can be affected by environments that contain traces of aggressive substances. Therefore, we recommend that customers minimize device exposure to aggressive substances during storage, production, and use. Devices that showed visible discoloration when tested using the described tests above did show no performance deviations within failure limits during the stated test duration. Respective failure limits are described in the IEC60810.

For further application related information please visit [www.osram-os.com/appnotes](http://www.osram-os.com/appnotes)

## Disclaimer

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Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office.

By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

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## Glossary

- 1) **Typical Values:** Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- 2) **Testing temperature:**  $T_a = 25^\circ\text{C}$
- 3) **Tolerance of Measure:** Unless otherwise noted in drawing, tolerances are specified with  $\pm 0.1$  and dimensions are specified in mm.
- 4) **Tape and Reel:** All dimensions and tolerances are specified acc. IEC 60286-3 and specified in mm.

## Revision History

Version	Date	Change
0.0	2019-08-02	Initiation of preliminary datasheet
1.0	2020-03-18	Application and Ordering information table update

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