CO8301

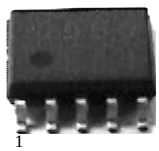
(Preliminary)

Stereo Headphone Amplifier

Description

The CO8301 is a monolithic integrated circuit and suitable dual amplifier for low power.

10-SSOP-225



Feature

- Low quiescent current
- High power supply ripple rejection
- Low voltage operation
- A few of external part required
- Built in power save switch
- Built in mute switch

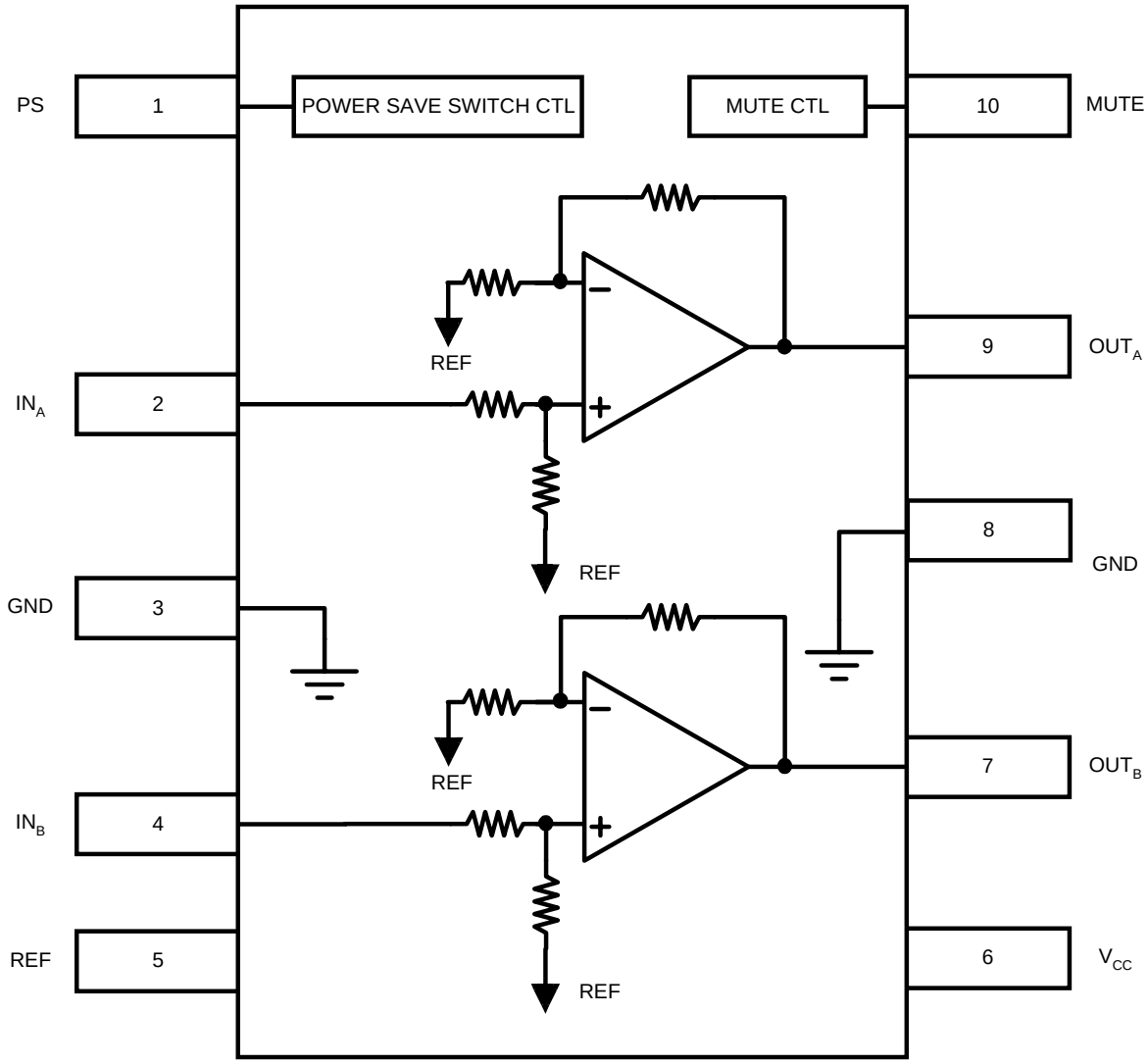
Typical Applications

- Portable compact disk player (DISCMAN)
- Portable mini disk player (MD)
- Disc-Man
- MP3 Player
- CD-Rom
- Other portable compact Disk Media Fan Motor Driver

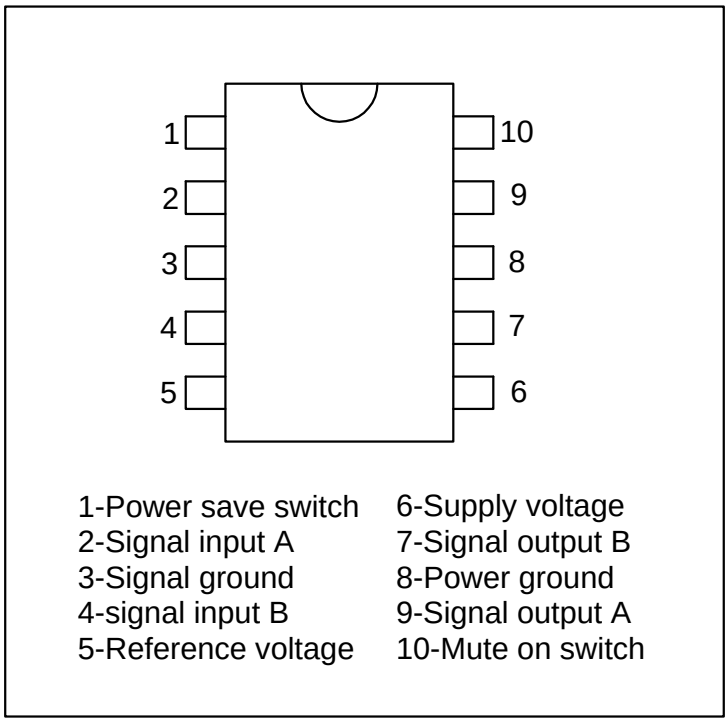
* All specs and applications shown above subject to change without prior notice.

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Email: server@ceramate.com.tw
Tel:886-3-3214525
Http: www.ceramate.com.tw
Fax:886-3-3521052

Block Diagram



Pin Configuration



1-Power save switch	6-Supply voltage
2-Signal input A	7-Signal output B
3-Signal ground	8-Power ground
4-signal input B	9-Signal output A
5-Reference voltage	10-Mute on switch

Ordering Information

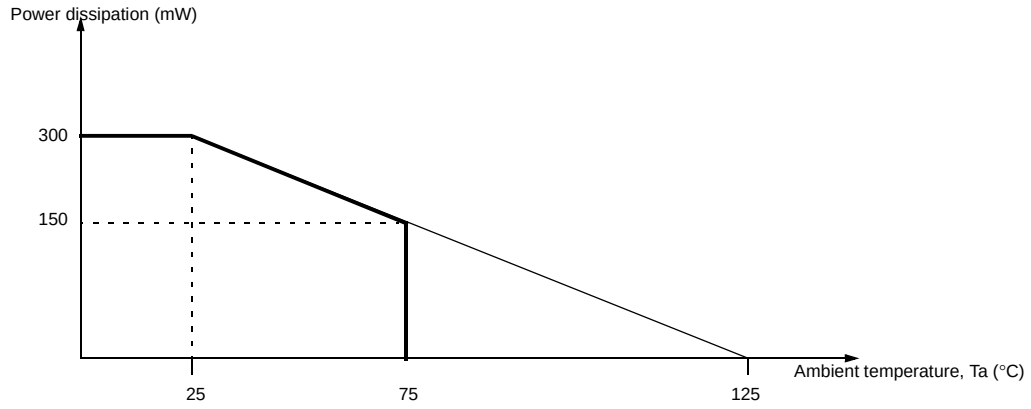
Device	Package	Operating Temp.
CO8301	10-SSOP-225	-20℃ ~ +75℃.

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Absolute Maximum Rating (Ta = 25℃)

Parameter	Symbol	Value	Unit	Remark
Maximum Supply Voltage	V_{CC}	4.5	V	Maximum Supply Voltage
Power Dissipation	P_D	300	mW	Power Dissipation
Operating Temperature	T_{OPR}	-20 ~ +75	°C	Operating Temperature
Storage Temperature	T_{STG}	-55 ~ +125	°C	Storage Temperature
Thermal Resistance	T_{ja}	150	°C/W	-

Power Dissipation Curve



Recommended Operating Conditions (Ta = 25℃)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Operating Supply Voltage	V_{CC}	1.8	3.0	4.0	V
Recommended Load	R_L	16	-	32	Ω

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Electrical Characteristics (RL =16Ω, Rg =600Ω, Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Quiescent Current 1	I_{CC1}	V_{CC} = 2.4V	-	5.5	10.0	mA
Quiescent Current 2	I_{CC2}	V_{CC} = 4.5V, Mute = GND	-	1.0	2.0	mA
Quiescent Current 3	I_{CC3}	V_{CC} = 4.5V, PS = GND	-	-	1.0	μA
Close Loop Voltage Gain 1	G_{VC1}	V_{CC} = 2.4V, f = 1kHz, V_O = -10dBm	30	32	34	dB
Close Loop Voltage Gain 2	G_{VC1}	V_{CC} = 1.8V, f = 1kHz, V_O = -20dBm	29	32	34	dB
Channel Balance 1	ΔG_{V1}	V_{CC} = 2.4V, f = 1kHz, V_O = -10dBm	-	-	1.0	dB
Channel Balance 2	ΔG_{V2}	V_{CC} = 1.8V, f = 1kHz, V_O = -20dBm	-	-	1.0	dB
Total Harmonic Distortion	THD	V_{CC} = 2.0V, f = 1kHz, P_O = 1mW	-	0.5	1.5	%
Ripple Rejection Ratio	RR	V_{CC} = 1.8V, f = 100Hz, Rg = 1kΩ, V_R = -20dBm, BPF = 100Hz	43	60	-	dB
Crosstalk	CT	V_{CC} = 2.4V, f = 100Hz, Rg = 1kΩ, V_O = -10dB	43	50	-	dB
Output Noise Voltage	V_{NOISE}	V_{CC} = 4.5V, Rg = 1kΩ, BPF = 20Hz ~ 20kHz	-	60	100	μVrms
Output Power	P_{OUT}	V_{CC} = 3.0V, f = 1kHz, THD = 10%	20	40	-	mW
PS Attenuation Ratio	ATT_{PS}	V_{CC} = 1.8V, f = 100Hz, PS = GND, V_{IN} = -10dB	-	-	-80	dB
MUTE Attenuation Ratio	ATT_{MU}	V_{CC} = 1.8V, f = 100Hz, MUTE = GND, V_{IN} = -10dB	-	-	-80	dB
PS ON Input Current	I_{PSON}	V_{CC} = 1.5V, V_{REF} ≥ 0.85V	-	0.2	1.0	μA
MUTE OFF Input Current	I_{MOFF}	V_{CC} = 1.5V, V_{REF} ≥ 0.85V	-	0.2	1.0	μA
PS ON High Level	V_{HPS}	V_{CC} = 1.5V, V_{REF} ≥ 0.85V	0.5	0.65	-	V
MUTE OFF High Level	V_{HMU}	V_{CC} = 1.5V, V_{REF} ≥ 0.85V	0.5	0.65	-	V

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Application Information

1. PS Block

This block diagram describes the power save switch circuits.
The drive block is controlled by PS pin, which can be derived from micro controller.
It controls bias of the internal circuits of CO8301, so that it makes CO8301 operate when input voltage level reaches high level.

2. Mute Block

The block diagram describes the mute on switch circuits.
The drive block is controlled by MUTE pin, which can be derived from micro controller.
When the pin of mute turns on, it makes reference voltage of internal circuits approximately 0V, so that it keeps the device of CO8301 off.

3. AMP Block

This block diagram describes the AMP block with resistances, which control gain of CO8301.

The gain of CO8301 is $\frac{V_{OUT}}{V_{IN}} \cong 40 \cong 32[dB]$

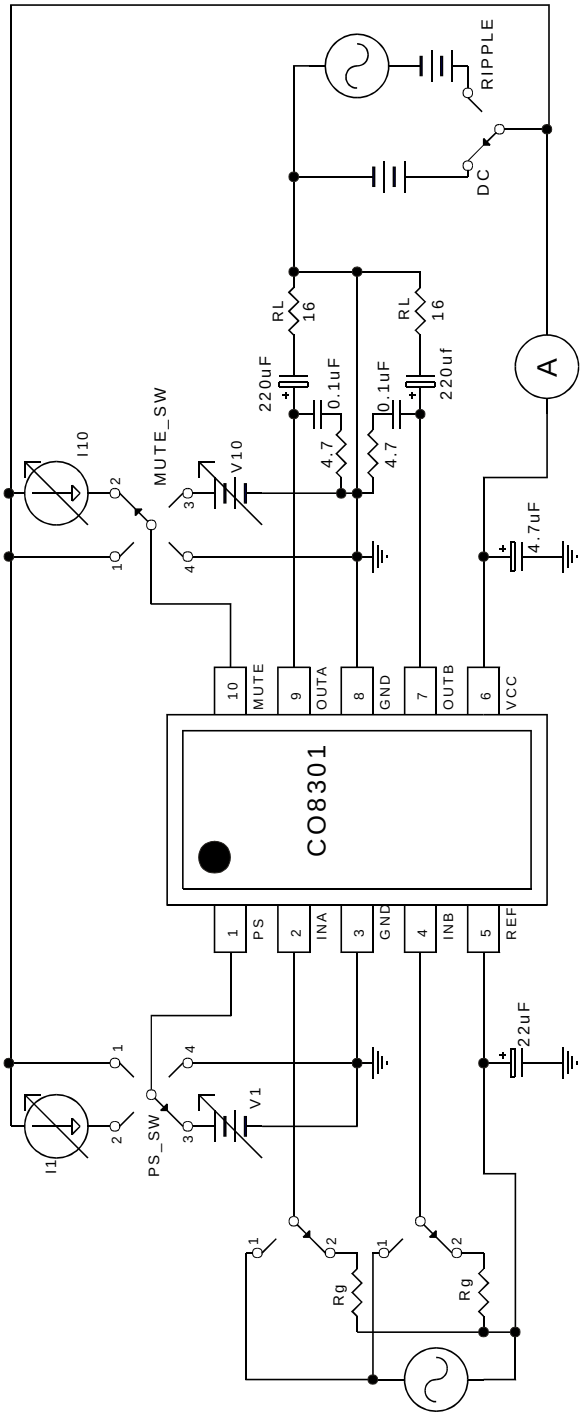
Output voltage of CO8301 can be 40 times as much as input voltage, so it eliminates the number of external circuits and offers headphone input.

4. Popping Noise Reduction

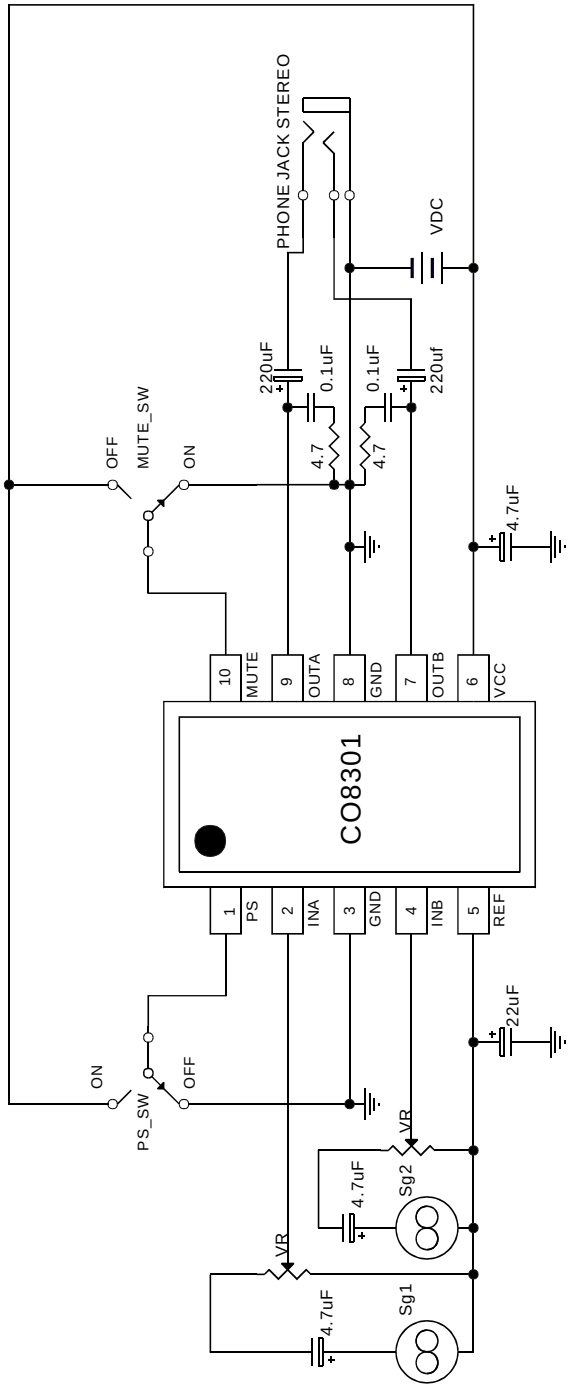
If PS pin (Pin1 , Power save switch) connect the micro controller, the micro controller must follow the same sequence 1 in order to reduce popping noise on mute mode.
PS on → Mute on → PS off → Mute operation → PS on → Mute off → Normal operation (Sequence 1) If PS connect V_{CC}, the micro controller follow the sequence 2.
Mute on → Mute Operation → Mute off → Normal operation (Sequence 2)

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Test Circuits



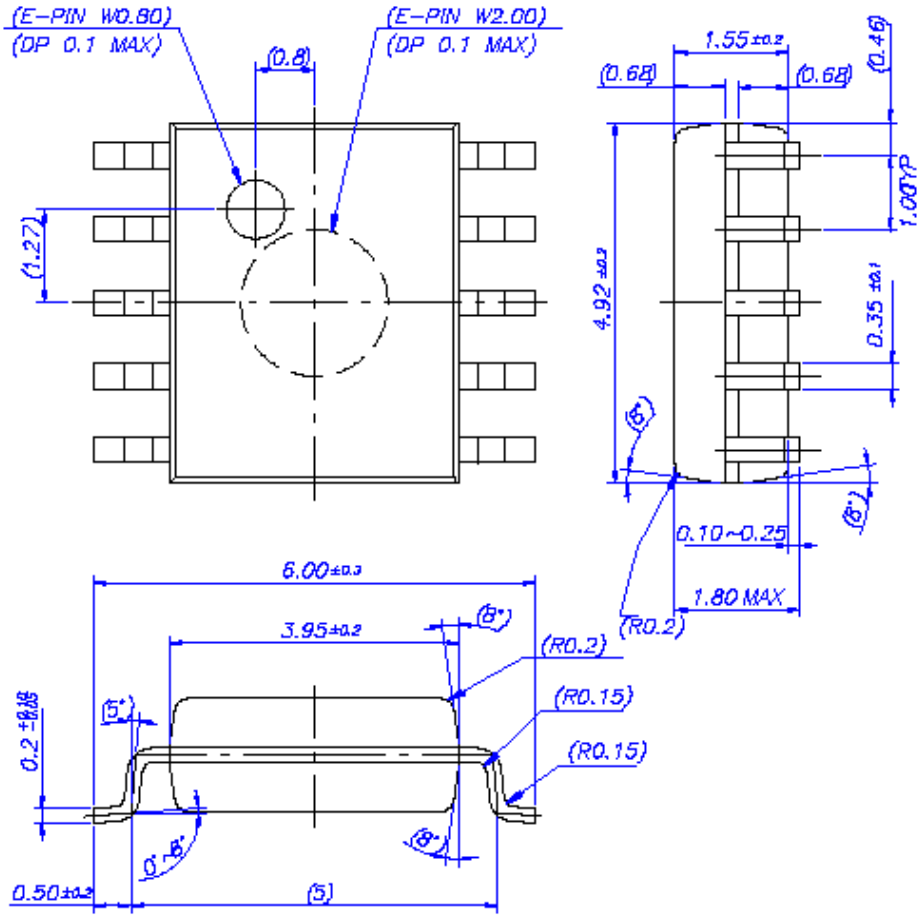
Typical Application Circuits



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Package Dimensions

10-SSOP-225



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