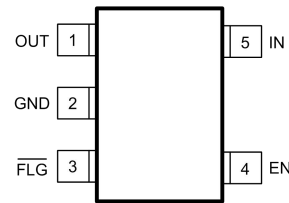


**WS4611**
**80mΩ, Current Limited, Power Distribution Switch**
[www.sh-willsemi.com](http://www.sh-willsemi.com)
**Descriptions**

The WS4611 is high-side switch with ultra-low ON resistance P-MOSFET. Integrated current-limit function can limit inrush current for heave capacitive load, over load current, and short-circuit current to protect power source.

The WS4611 is also integrated reverse protection function to eliminate any reverse current flow across the switch when the device is off. Thermal shutdown function can protect the device and load. The output auto-discharge function is disabled in WS4611.

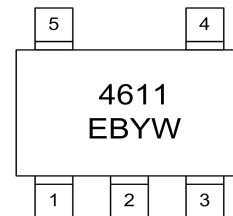
The WS4611 is available in SOT-23-5L package. Standard product is Pb-free and Halogen-free.


**SOT-23-5L**

**Pin configuration (Top view)**
**Features**

- Input voltage range : 2.5~5.5V
- Main switch  $R_{ON}$  : 80mΩ @  $V_{IN}=5V$
- Current limit threshold
  - WS4611EB : 1.0A (Typ.)
- Reverse block (No “body diode”)
- Over temperature protection

**Applications**

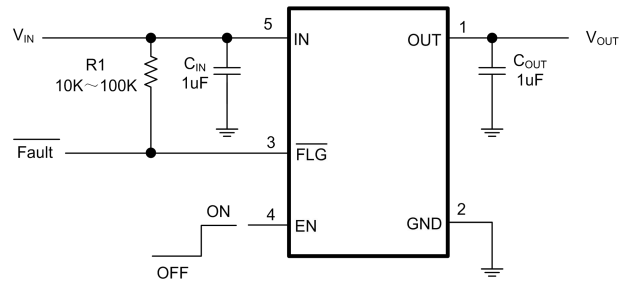
- USB peripherals
- USB Dongle
- USB 3G data card
- 3.3V or 5V Power Switch
- 3.3V or 5V Power Distribution



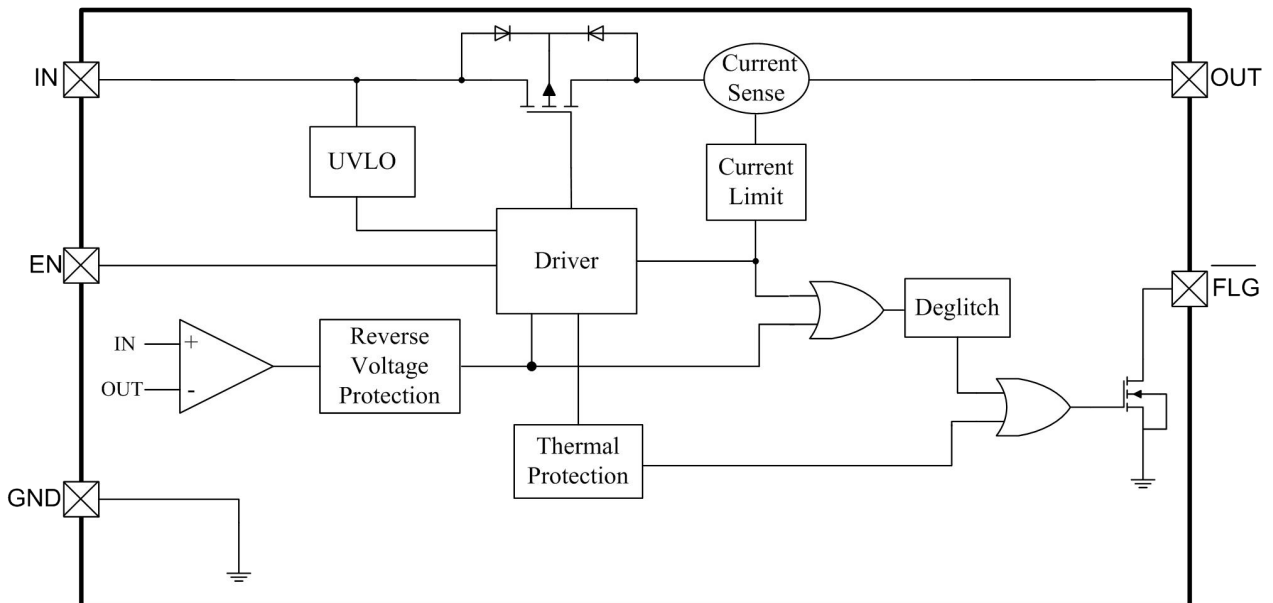
**4611** = Device code  
**EB** = Special code  
**Y** = Year code  
**W** = Week code

**Marking**
**Order information**

Device	Package	Shipping
WS4611EB-5/TR	SOT-23-5L	3000/Reel&Tape

**Typical Applications**

**Pin Descriptions**

Pin Number	Symbol	Descriptions
1	OUT	Output Pin
2	GND	Ground
3	$\overline{\text{FLG}}$	Fault Flag Pin, Open-Drain, Active Low
4	EN	Enable Pin, Active High
5	IN	Input Pin

**Block Diagram**


**Absolute maximum ratings**

Parameter	Symbol	Value	Unit
IN pin voltage range	$V_{IN}$	-0.3~6.5	V
OUT pin voltage range	$V_{OUT}$	-0.3~6.5	V
$\overline{FLG}$ pin voltage range	$V_{\overline{FLG}}$	-0.3~6.5	V
EN pin voltage range	$V_{EN}$	-0.3~6.5	V
Junction temperature	$T_J$	-40~150	°C
Lead temperature(Soldering, 10s)	$T_L$	260	°C
Storage temperature	$T_{stg}$	-55 ~ 150	°C
IN, OUT Pin ESD Ratings	HBM	8000	V
	MM	400	V
$\overline{FLG}$ , EN Pin ESD Ratings	HBM	4000	V
	MM	400	V

These are stress ratings only. Stresses exceeding the range specified under “Absolute Maximum Ratings” may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

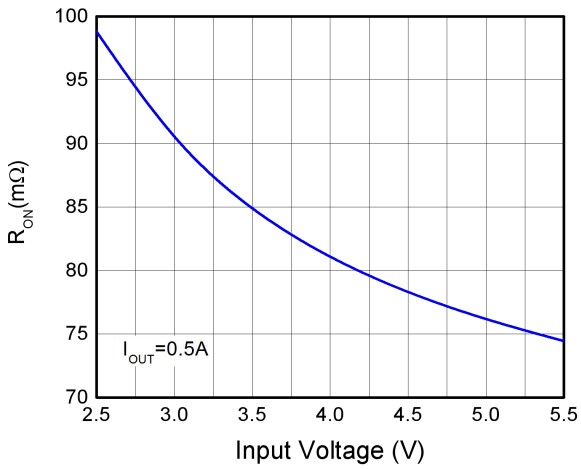
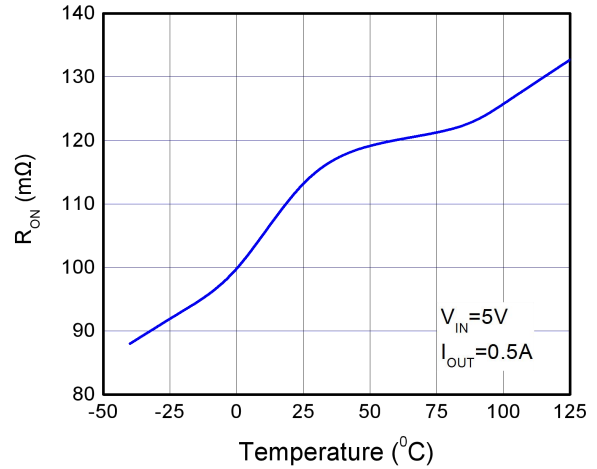
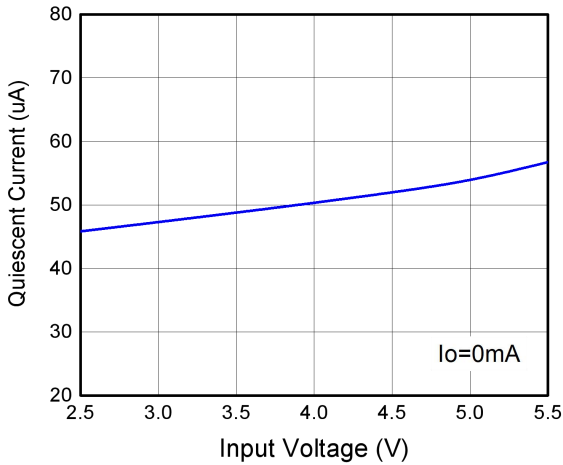
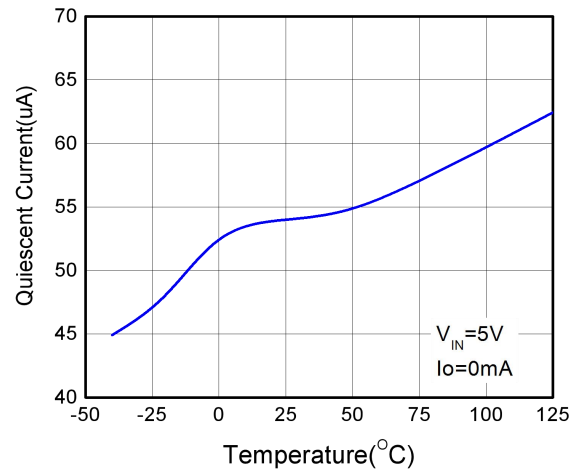
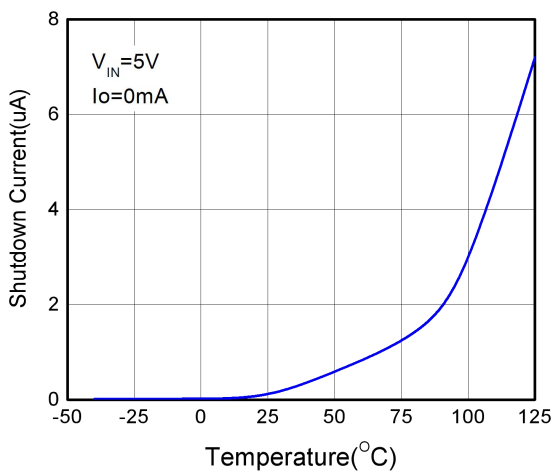
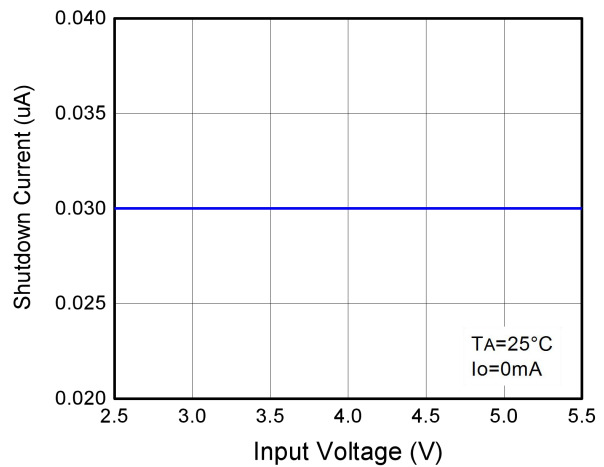
**Recommend Operating Conditions**

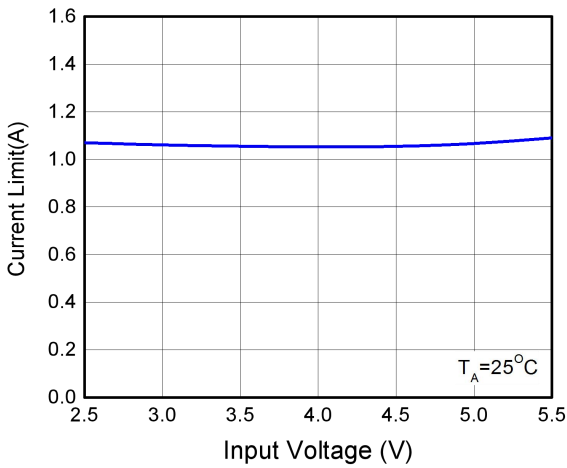
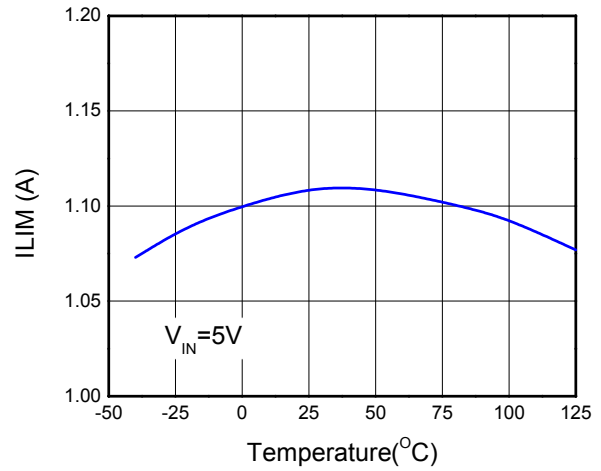
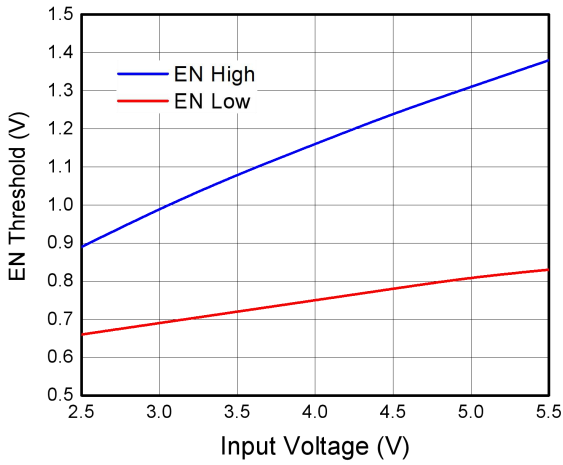
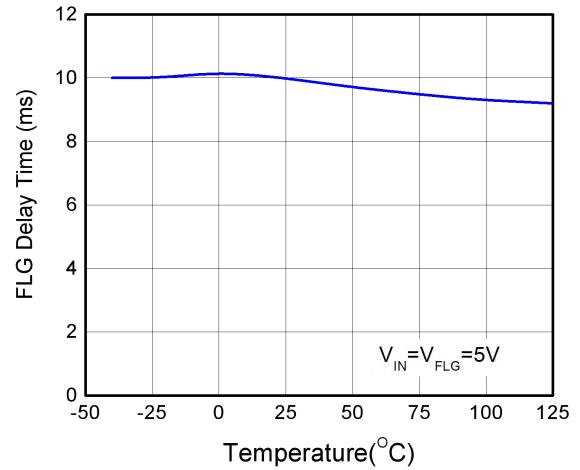
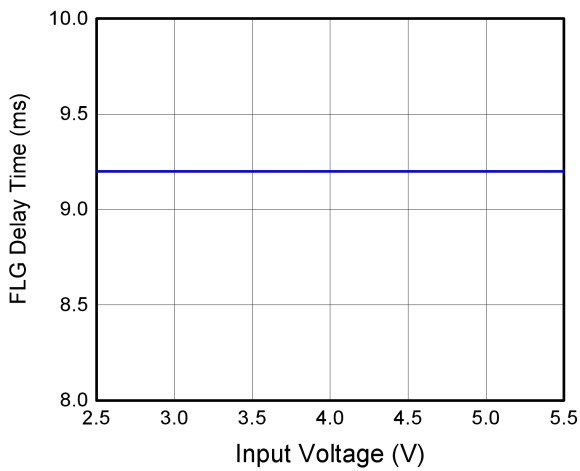
Parameter	Symbol	Value	Unit
Supply input voltage range	$V_{IN}$	2.5~5.5	V
Operating ambient temperature	$T_A$	-40~85	°C
Thermal Resistance	$R_{\theta JA}$	250	°C/W

**Electronics Characteristics (Ta=25°C, VIN=5V, CIN=COUT=1μF, unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Quiescent supply current	I <sub>Q</sub>	I <sub>OUT</sub> =0, V <sub>IN</sub> =V <sub>EN</sub> =5V		48	60	μA
Shutdown current	I <sub>SD</sub>	V <sub>EN</sub> =0V			1	μA
Reverse current	I <sub>REV</sub>	V <sub>IN</sub> =V <sub>EN</sub> =0V, V <sub>OUT</sub> =5V, Current flow to V <sub>IN</sub>			1	μA
Main-FET ON resistance <sup>(1)</sup>	R <sub>ON</sub>	V <sub>IN</sub> =V <sub>EN</sub> =5V, I <sub>OUT</sub> =500mA		80		mΩ
Auto-discharge FET ON resistance	R <sub>DCHG</sub>	V <sub>EN</sub> =0V, V <sub>IN</sub> =V <sub>OUT</sub> =5V		65		Ω
Over-current trip threshold	I <sub>OC</sub>	Current ramp (≤100A/s) on OUT	0.7	1	1.4	A
Short-circuit output current	I <sub>OS</sub>	OUT shorted to GND		0.45		A
Short circuit current limiting response time	t <sub>SHORT</sub>	OUT connected to GND, C <sub>L</sub> =1μF		2		μs
EN input low voltage	V <sub>IL</sub>	V <sub>IN</sub> =5V			0.4	V
EN input high voltage	V <sub>IH</sub>	V <sub>IN</sub> =5V	1.6			V
OUT pin turn-on time after EN ON	t <sub>ON</sub>	C <sub>L</sub> =1μF, R <sub>L</sub> =5ohm		20		μs
Fault flag output blanking time	t <sub>BLANK</sub>			9		ms
Over-temperature shutdown threshold	T <sub>SD</sub>			160		°C
Over-temperature threshold hysteresis	T <sub>HYS</sub>			35		°C
Under voltage lock out threshold	V <sub>UVLO</sub>			2.2		V
Under voltage lock out hysteresis	V <sub>UVLO-HYS</sub>			200		mV

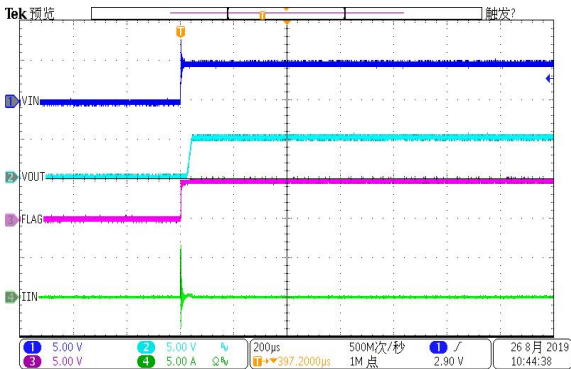
Note: (1) Pulse test, T<sub>P</sub>=380us

**Typical Characteristics (Ta=25°C, unless otherwise noted)**

**ON Resistance vs. Input Voltage**

**ON Resistance vs. Temperature**

**Quiescent current vs. Input Voltage**

**Quiescent current vs. Temperature**

**Shut-down Current vs. Temperature**

**Shut-down Current vs. Input Voltage**

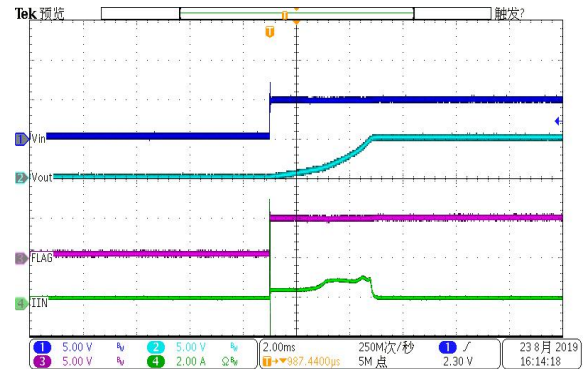

**Current Limit vs. Input Voltage**

**Current Limit vs. Temperature**

**EN Threshold vs. Input Voltage**

**Fault Flag Blanking time vs. Temperature**

**Fault Flag Blanking time vs. Input Voltage**

**Startup from Power ON**

VIN=5V, Cin=Cout=1uF, no Load

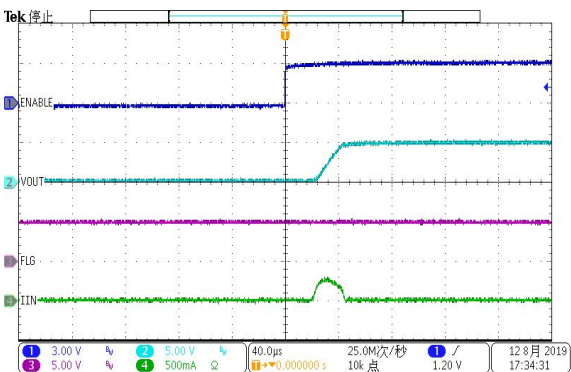


VIN=5V, Cin=1uF, Cout=1000uF, no Load

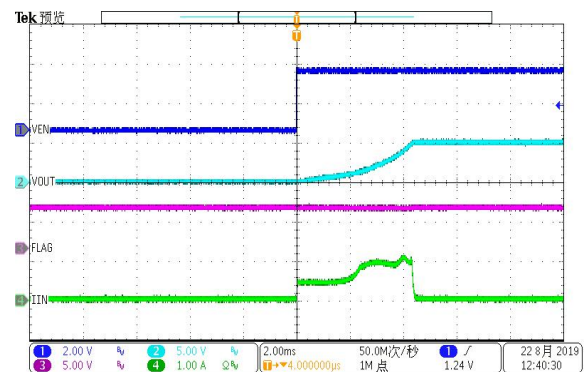


**Startup from Enable ON**

VIN=5V, Cin=Cout=1uF, no Load

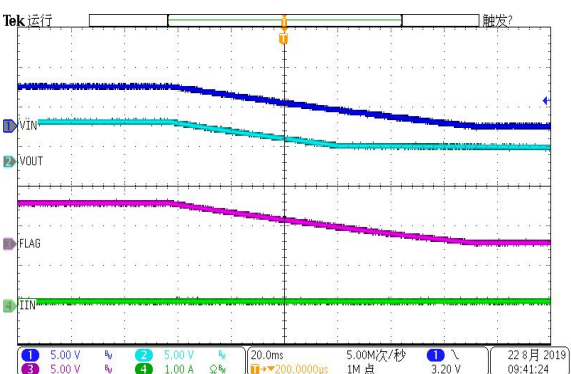


VIN=5V, Cin=1uF, Cout=1000uF, no Load



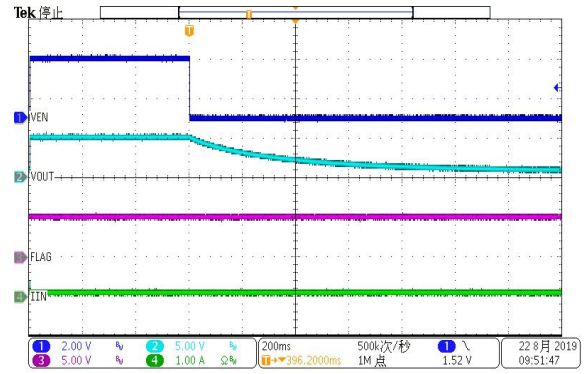
**Shutdown from Power OFF**

VIN=5V, Cin=Cout=1uF, no Load



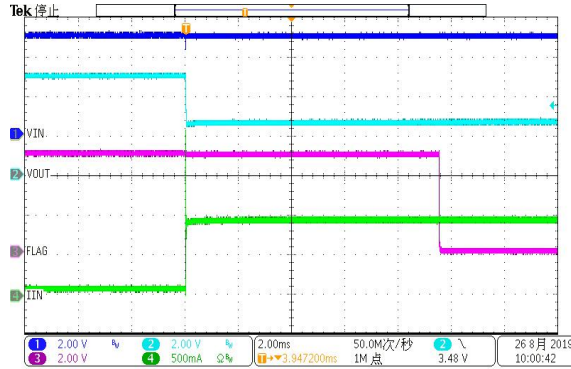
**Shutdown from Enable OFF**

VIN=5V, Cin=Cout=1uF, no Load



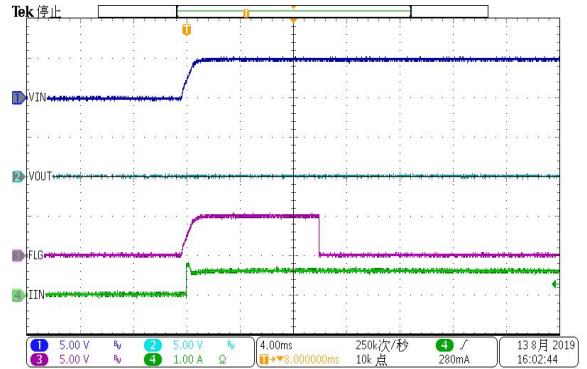
**Current Limit Response**

VIN=5V, Cin=Cout=1uF, RL=3Ω



**Start into Short Circuit**

VIN=5V, Cin=Cout=1uF, RL=0Ω



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## Operation Information

### Power Switch

The power switch is an P-channel MOSFET with low  $R_{DS(ON)}$  for power management or USB power distribution applications. The WS4611 has reverse voltage protection to prevents current flow from OUT to IN and IN to OUT when device is off.

### Current-Limit Protection

The WS4611 provide current limit protection function to protect power source when over-current condition occurs.

### Short-Circuit Protection

The WS4611 provide short circuit protection function. The output current will be limited to safe level. The short-circuit protection is used to reduce power dissipation of the device and protect power source during short-circuit condition.

### Fault indicate

The  $\overline{FLG}$  open drain output is asserted (active low) with 8ms(Typ.) delay when an over-current or over-temperature condition is encountered. The  $\overline{FLG}$  signal will remain asserted until the over-current or over-temperature condition is removed.

### UVLO Protection

To avoid malfunction of the WS4611 at low input voltages, an under voltage lockout is included that disables the device, until the input voltage exceeds 2.2V (Typ.).

### Shutdown Mode

Drive EN to GND to place the WS4611 in shutdown mode. In shutdown mode, input current falls to smaller than 1uA.

### Thermal Shutdown

As soon as the junction temperature ( $T_J$ ) exceeds 160°C (Typ.), the WS4611 goes into thermal shutdown. In this mode, the device is turned off and will turn on again until Junction temperature falls below 125°C (Typ.).



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## Application Information

### Input Capacitor

A 1 $\mu$ F input bypass ceramic capacitor( $C_{IN}$ ) from IN to GND, located near the WS4611 is strongly recommended to suppress the voltage overshooting during short circuit fault event. Without the bypass capacitor, the output short may cause sufficient ringing on the input (from supply lead inductance) to damage the device.

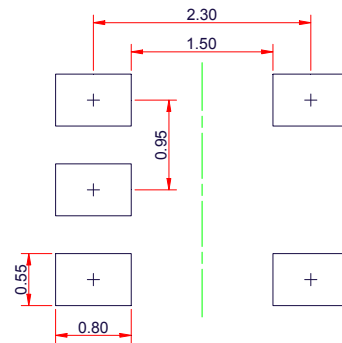
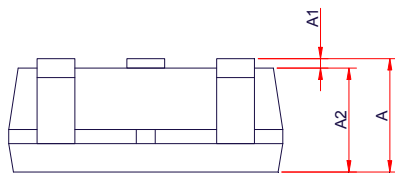
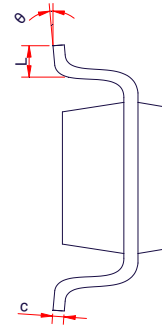
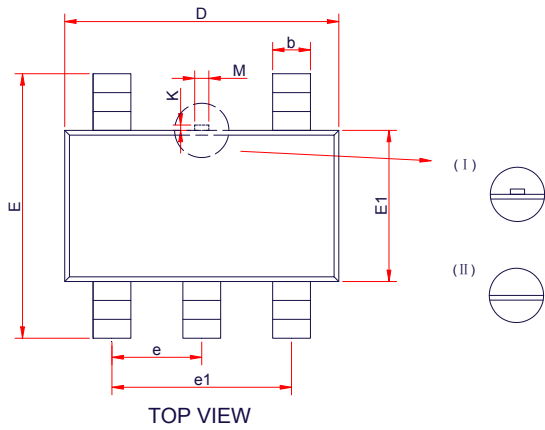
### Output Capacitor

A low ESR, 150 $\mu$ F aluminum electrolytic or tantalum between OUT and GND is strongly recommended to reduce the voltage droop during hot-plug of downstream peripheral. Higher value output capacitor is better when the output load is heavy. Additionally, bypassing the output with a 1 $\mu$ F ceramic capacitor improves the immunity of the device to short-circuit transients.

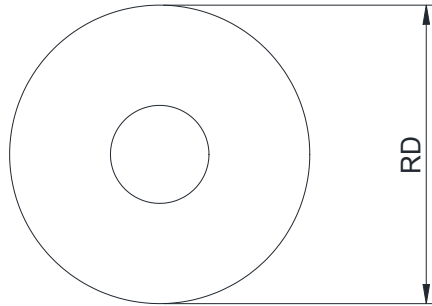
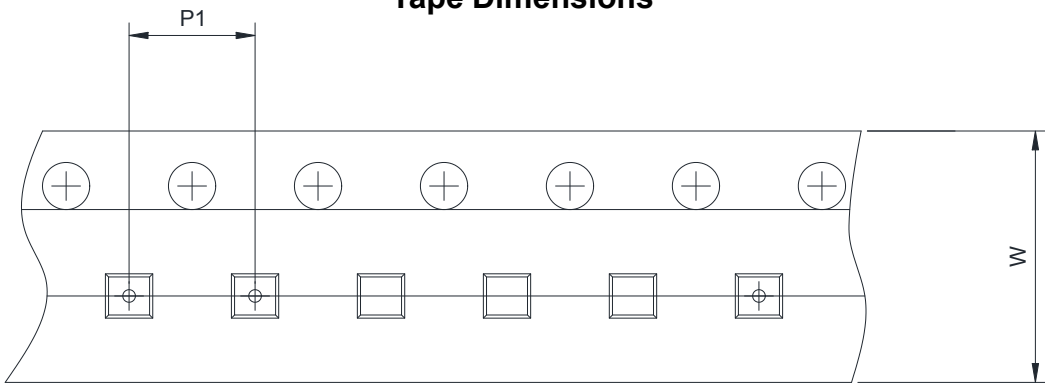
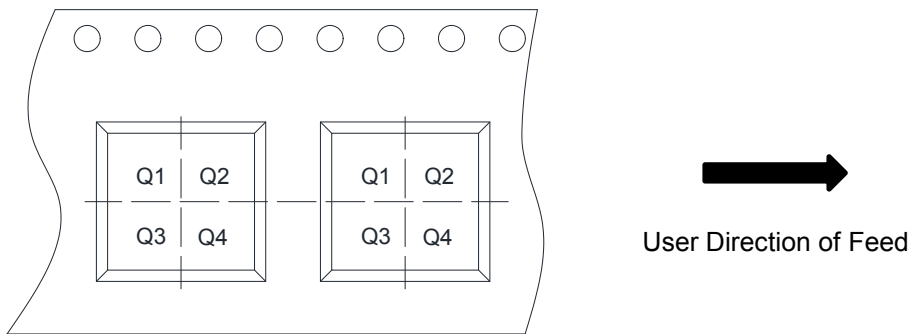
### PCB Layout consideration

The PCB layout should be carefully performed to maximize thermal dissipation and to minimize voltage drop. The following guidelines must be considered:

1. Please place the input capacitors near the IN pin as close as possible.
2. Output decoupling capacitors for load must be placed near the load as close as possible for decoupling high frequency ripples.
3. Locate WS4611 and output capacitors near the load to reduce parasitic resistance and inductance for excellent load transient performance.
4. The negative pins of the input and output capacitors and the GND pin must be connected to the ground plane of the load.
5. Keep IN and OUT traces as wide and short as possible.

**PACKAGE OUTLINE DIMENSIONS**
**SOT-23-5L**


Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	-	-	1.45
A1	0.00	-	0.15
A2	0.90	1.10	1.30
b	0.30	0.40	0.50
c	0.10	-	0.21
D	2.72	2.92	3.12
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.45	0.60
M	0.10	0.15	0.25
K	0.00	-	0.25
θ	0°	-	8°

**TAPE AND REEL INFORMATION**
**Reel Dimensions**

**Tape Dimensions**

**Quadrant Assignments For PIN1 Orientation In Tape**


RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm <input type="checkbox"/> 16mm
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm	<input checked="" type="checkbox"/> 4mm <input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1	<input type="checkbox"/> Q2 <input checked="" type="checkbox"/> Q3 <input type="checkbox"/> Q4