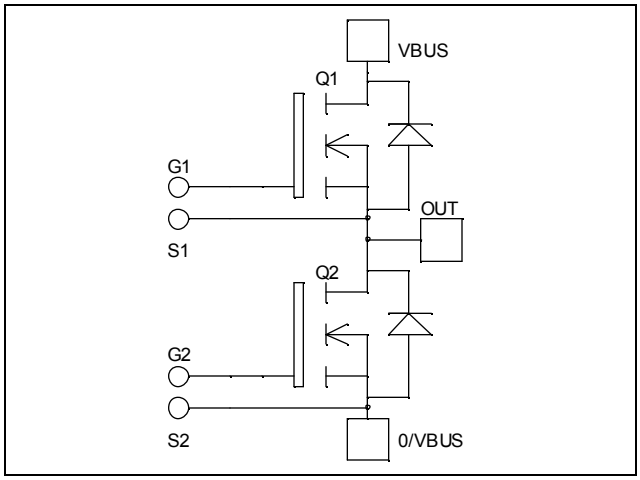
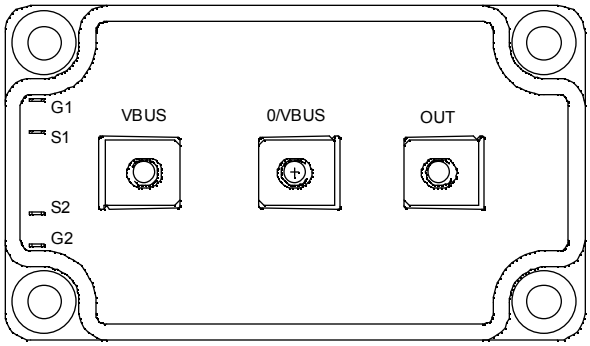
Phase leg MOSFET Power Module

V_{DSS} = 500V
R_{DSon} = 19mΩ typ @ Tj = 25°C
I_D = 163A @ Tc = 25°C





Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- Power MOS 7® FREDFETs
  - Low R_{DSon}
  - Low input and Miller capacitance
  - Low gate charge
  - Fast intrinsic reverse diode
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{DSS}	Drain - Source Breakdown Voltage		500	V
I_D	Continuous Drain Current	T_c = 25°C	163	A
		T_c = 80°C	122	
I_{DM}	Pulsed Drain current		652	
V_{GS}	Gate - Source Voltage		±30	V
R_{DSon}	Drain - Source ON Resistance		22.5	mΩ
P_D	Maximum Power Dissipation	T_c = 25°C	1136	W
I_{AR}	Avalanche current (repetitive and non repetitive)		46	A
E_{AR}	Repetitive Avalanche Energy		50	mJ
E_{AS}	Single Pulse Avalanche Energy		2500	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ T_j = 25°C unless otherwise specified

Electrical Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0V, V_{DS} = 500V	T_j = 25°C			200	µA
		V_{GS} = 0V, V_{DS} = 400V	T_j = 125°C			1000	
$R_{DS(on)}$	Drain – Source on Resistance	V_{GS} = 10V, I_D = 81.5A			19	22.5	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 10mA		3		5	V
I_{GSS}	Gate – Source Leakage Current	V_{GS} = ±30 V, V_{DS} = 0V				±200	nA

Dynamic Characteristics

Symbol	*Characteristic*	*Test Conditions*	*Min*	*Typ*	*Max*	*Unit*
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} = 25V f = 1MHz		22.4		nF
C_{oss}	Output Capacitance			4.8		
C_{rss}	Reverse Transfer Capacitance			0.36		
Q_g	Total gate Charge	V_{GS} = 10V V_{Bus} = 250V I_D = 163A		492		nC
Q_{gs}	Gate – Source Charge			132		
Q_{gd}	Gate – Drain Charge			260		
$T_{d(on)}$	Turn-on Delay Time	**Inductive switching @ 125°C** V_{GS} = 15V V_{Bus} = 333V I_D = 163A R_G = 1Ω		18		ns
T_r	Rise Time			35		
$T_{d(off)}$	Turn-off Delay Time			87		
T_f	Fall Time			77		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 25°C** V_{GS} = 15V, V_{Bus} = 333V I_D = 163A, R_G = 1Ω		3020		µJ
E_{off}	Turn-off Switching Energy			2904		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 125°C** V_{GS} = 15V, V_{Bus} = 333V I_D = 163A, R_G = 1Ω		4964		µJ
E_{off}	Turn-off Switching Energy			3384		

Source - Drain diode ratings and characteristics

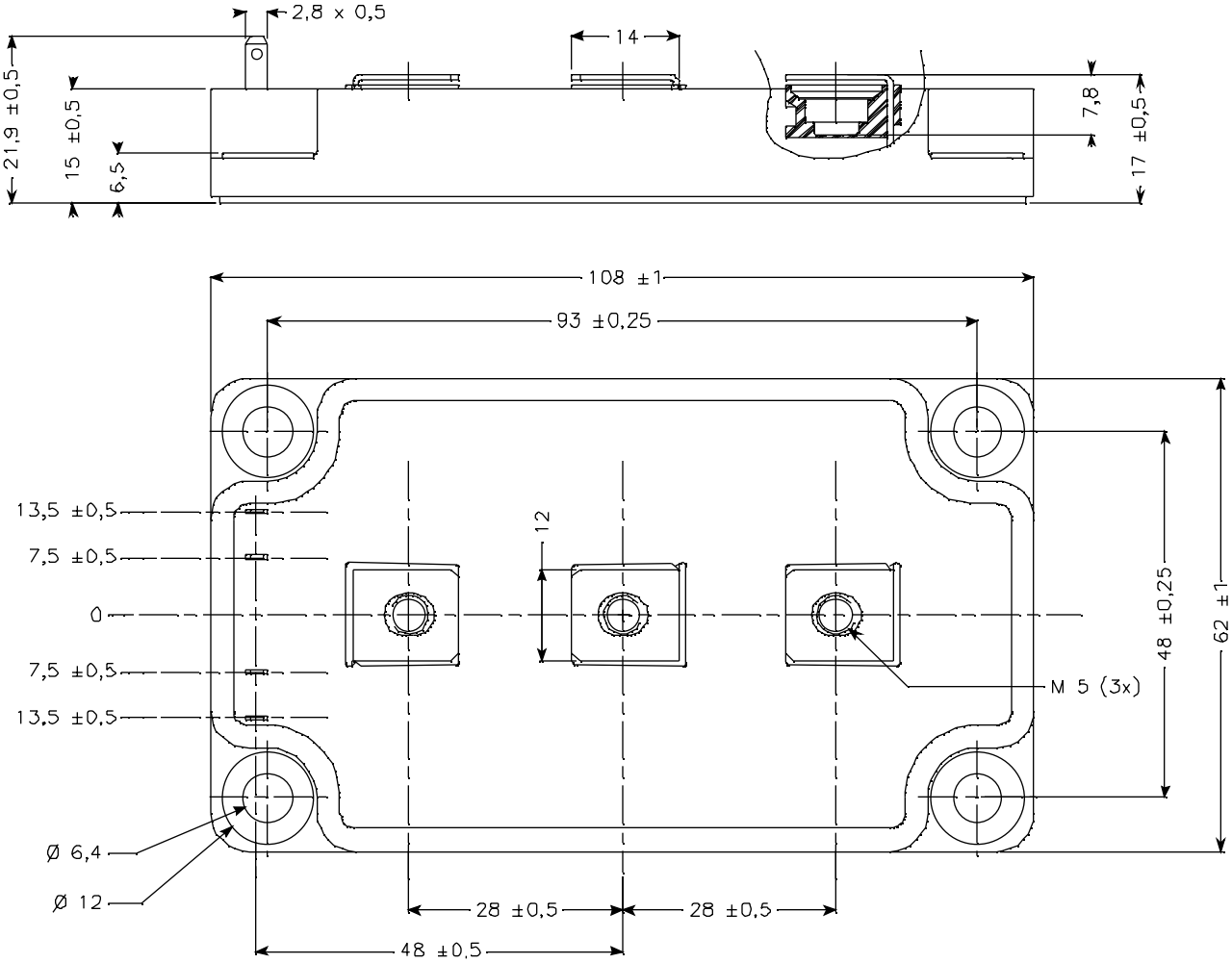
Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_S	Continuous Source current (Body diode)		Tc = 25°C			163	A
			Tc = 80°C			122	
V_{SD}	Diode Forward Voltage	V_{GS} = 0V, I_S = - 163A				1.3	V
dv/dt	Peak Diode Recovery ❶					15	V/ns
t_{rr}	Reverse Recovery Time	I_S = -163A V_R = 333V di_S/dt = 400A/µs	T_j = 25°C		233		ns
			T_j = 125°C		499		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		7.6		µC
			T_j = 125°C		22.8		

❶ dv/dt numbers reflect the limitations of the circuit rather than the device itself.
$I_S \le$ - 163A di/dt ≤ 700A/µs $V_R \le V_{DSS}$ $T_j \le$ 150°C

Thermal and package characteristics

Symbol	*Characteristic*			*Min*	*Typ*	*Max*	*Unit*
R_{thJC}	Junction to Case Thermal Resistance					0.11	°C/W
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, I isol<1mA, 50/60Hz			2500			V
T_J	Operating junction temperature range			-40		150	°C
T_{STG}	Storage Temperature Range			-40		125	
T_C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
		For terminals	M5	2		3.5	
Wt	Package Weight					280	g

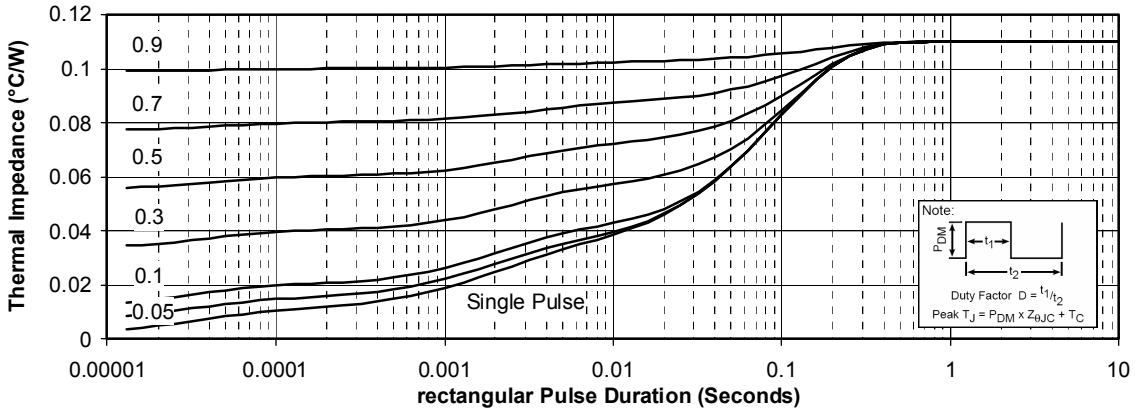
SP6 Package outline (dimensions in mm)



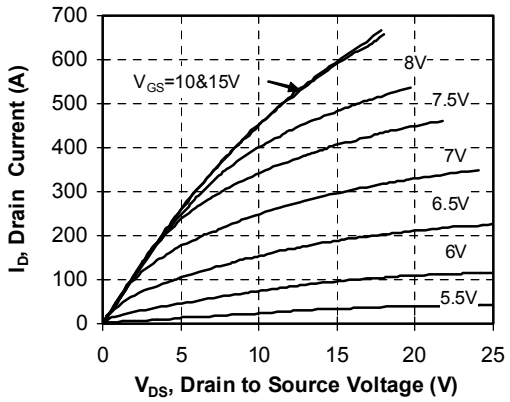
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

Typical Performance Curve

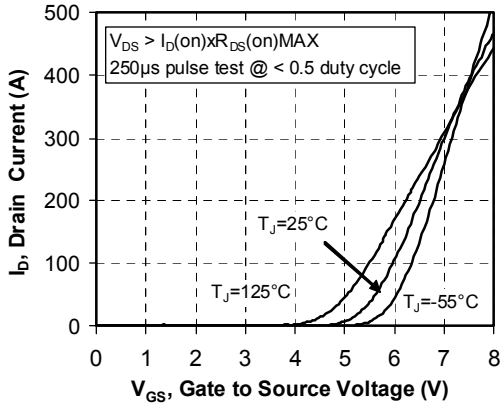
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



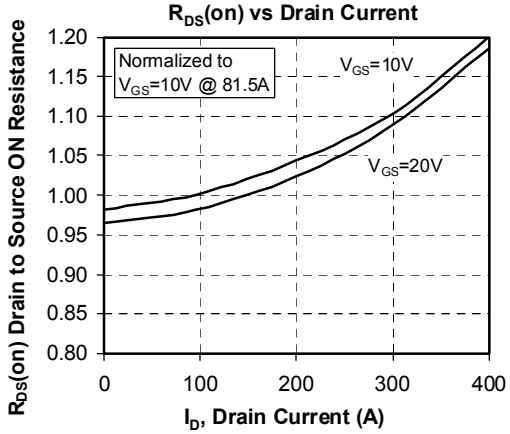
Low Voltage Output Characteristics



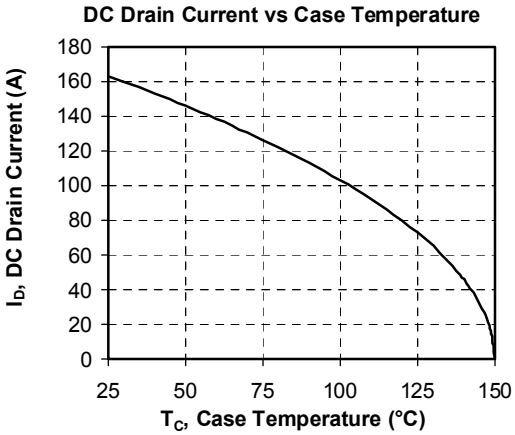
Transfert Characteristics



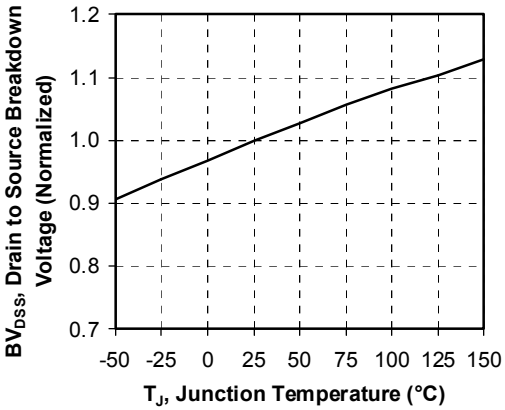
R_{DS}(on) vs Drain Current



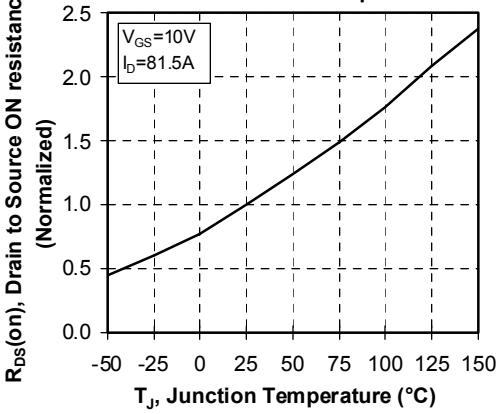
DC Drain Current vs Case Temperature



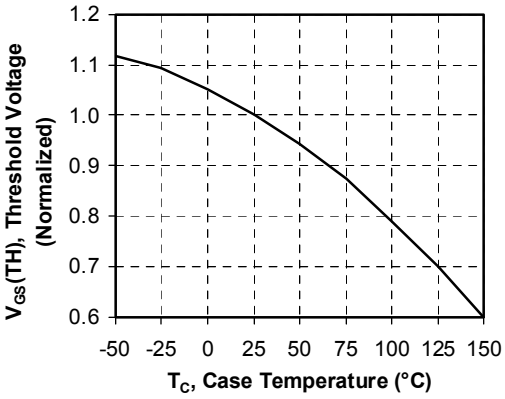
Breakdown Voltage vs Temperature



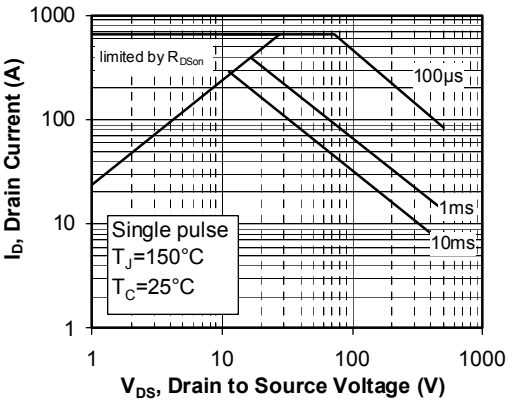
ON resistance vs Temperature



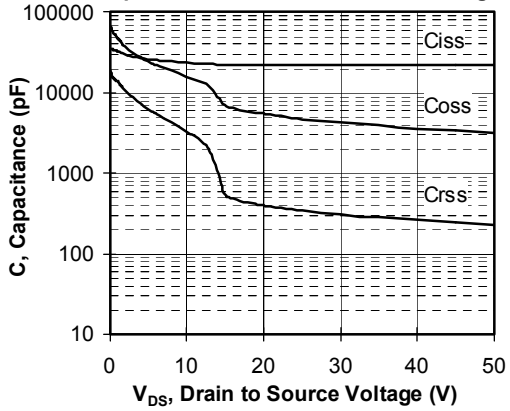
Threshold Voltage vs Temperature



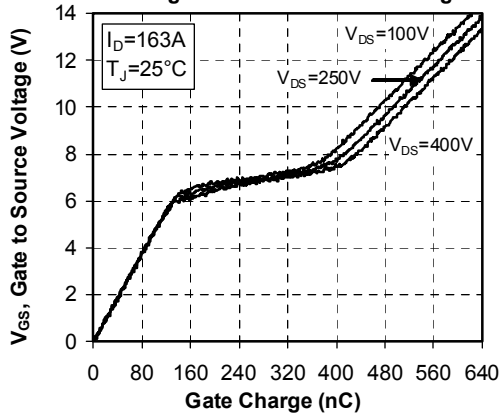
Maximum Safe Operating Area



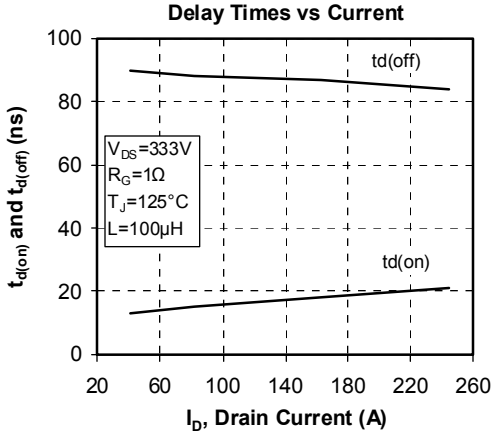
Capacitance vs Drain to Source Voltage



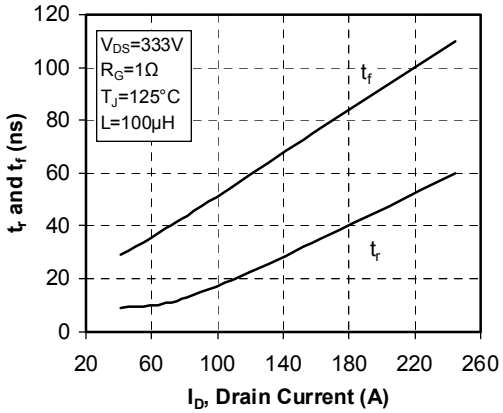
Gate Charge vs Gate to Source Voltage



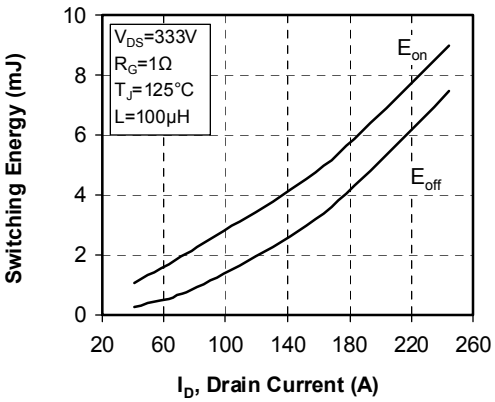
Delay Times vs Current



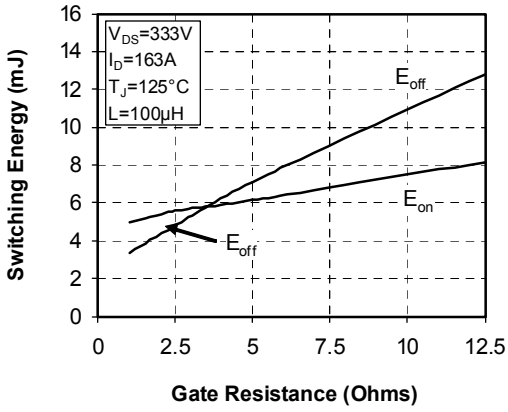
Rise and Fall times vs Current



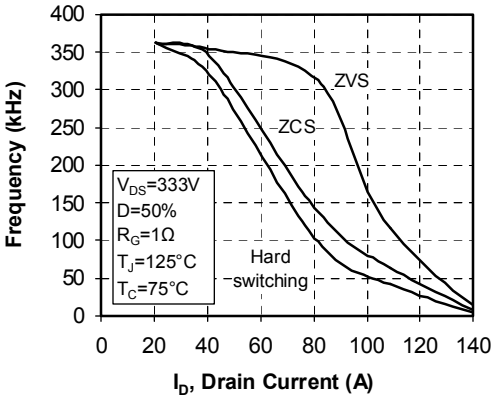
Switching Energy vs Current



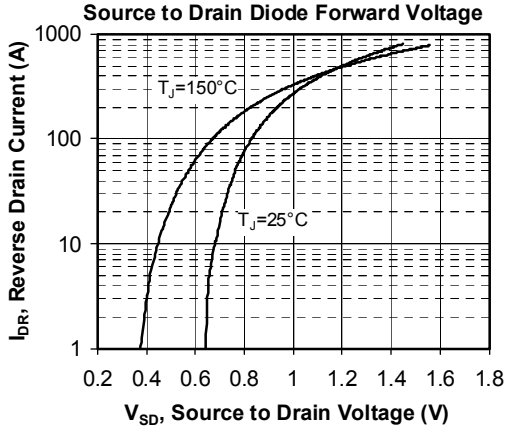
Switching Energy vs Gate Resistance



Operating Frequency vs Drain Current



Source to Drain Diode Forward Voltage



Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.