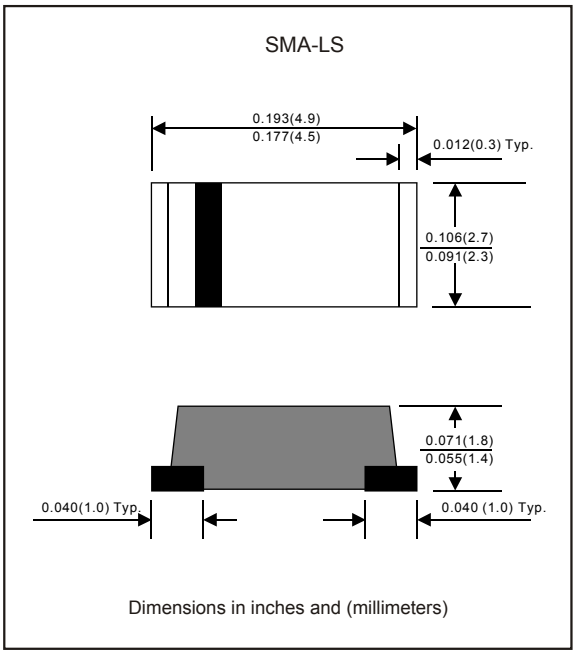
PFFM201LS THRU PFFM207LS

2.0A SUFRACE MOUNT FAST RECOVERY RECTIFIERS-50-1000V

Features

- Batch process design,excellent power dissipationoffers better reverse leakagecurrent and thermalresistance.
- Low profile surfacemounted application inorder to optimize board space.
- High current capability.
- Fast switching forhigh efficiency.
- High surge currentcapability.
- Glass passivated chipjunction.
- RoHS product for packing code suffix "G"
  Halogen free product for packing code suffix "H"
- **Moisture Sensitivity Level 1**



Mechanical data

- Epoxy:UL94-V0 rated flameretardant
- Case : Moldedplastic, JEDEC SMA-LS
- Terminals: Solder plated,solderable per MIL-STD-750, Method 2026
- Polarity : Indicatedby cathode band
- Mounting Position :Any
- Weight : Approximated 0.05gram

Maximum ratings (AT T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	I_O			2.0	A
Forward surge current	8.3ms single halfsine-wave superimposed on rate load (JEDECmethode)	I_{FSM}			50	A
Reverse current	V_R = V_{RRM} T_A = 25°C	I_R			5.0	uA
	V_R = V_{RRM} T_A = 125°C				100	
Thermal resistance	Junction to ambient	$R_{\theta JA}$		35		°C/W
Diode junction capacitance	f=1MHz and applied4V DC reversevoltage	C_J		40		pF
Storage temperature		T_{STG}	-65		+175	°C

SYMBOLS	V_{RRM} *1 (V)	V_{RMS} *2 (V)	V_R *3 (V)	V_F *4 (V)	T_{RR} *5 (nS)	Operating temperature T_J, (°C)
PFFM201LS	50	35	50	1.30	150	-55 to +150
PFFM202LS	100	70	100			
PFFM203LS	200	140	200			
PFFM204LS	400	280	400			
PFFM205LS	600	420	600		250	
PFFM206LS	800	560	800		500	
PFFM207LS	1000	700	1000			

*1 Repetitive peakreverse voltage

*2 RMS voltage

*3 Continuous reversevoltage

*4 Maximum forwardvoltage@IF=2A

*5 Reverse recoverytime

Rating and characteristic curves

FIG.1-TYPICAL FORWARD CHARACTERISTICS

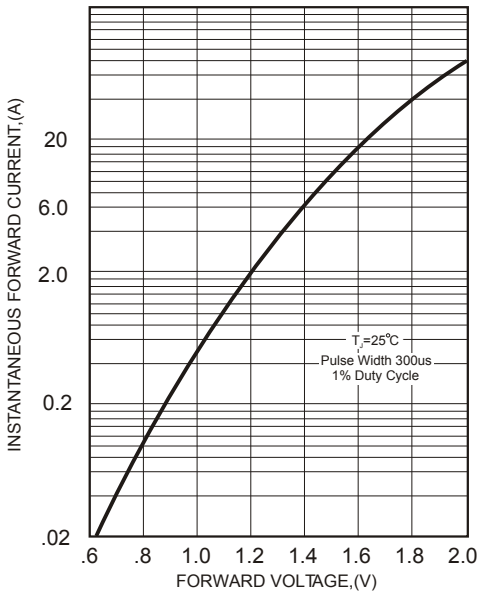


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

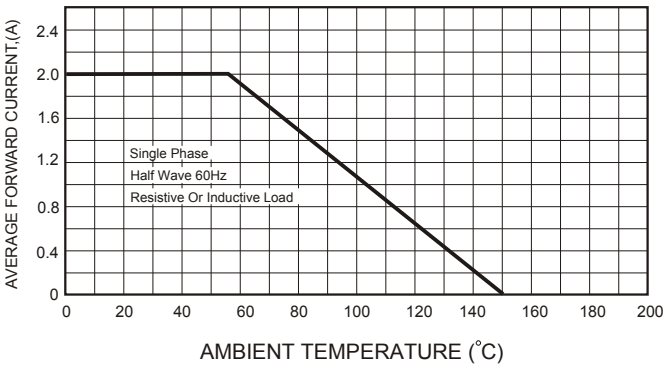
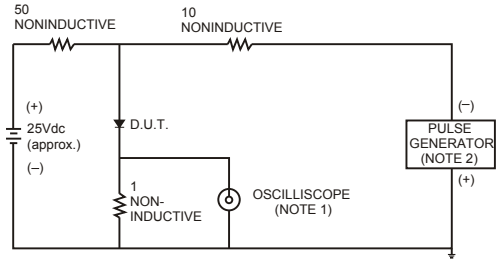


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF.

2. Rise Time= 10ns max., Source Impedance= 50 ohms.

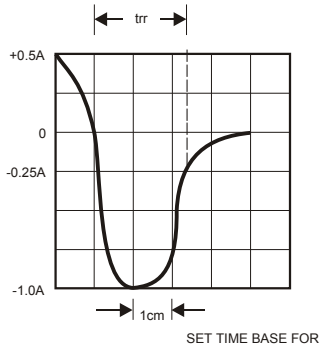


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

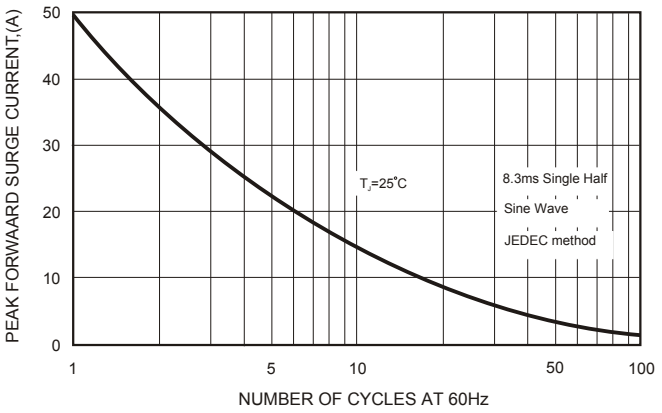
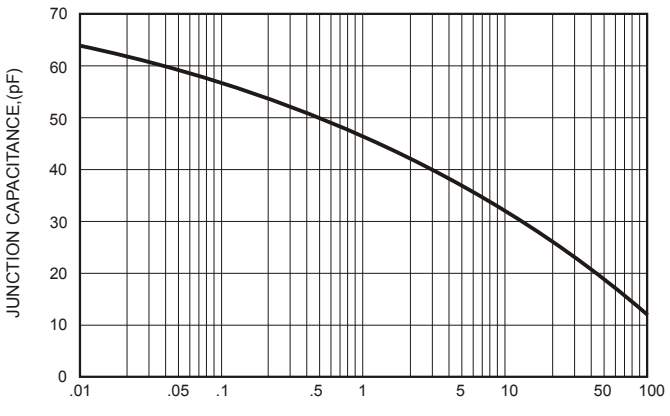


FIG.5-TYPICAL JUNCTION CAPACITANCE



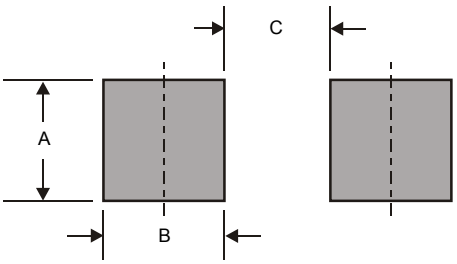
Pinning information

Pin	Simplidied outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
PFFM201LS	F21
PFFM202LS	F22
PFFM203LS	F23
PFFM204LS	F24
PFFM205LS	F25
PFFM206LS	F26
PFFM207LS	F27

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMA-LS	0.110 (2.80)	0.063 (1.60)	0.087 (2.20)