

DV200-2800D Series

HIGH RELIABILITY DC-DC CONVERTERS

DESCRIPTION

The DV200 series of high reliability, isolated DC-DC converters is operable over a wide (-55 °C to +100 °C) temperature range with no power derating. Unique to the DV200 series is a high speed magnetic feedback circuit. Operating at a nominal fixed frequency of 500 kHz, these regulated, isolated units utilize well-controlled undervoltage lockout circuitry to eliminate slow start-up problems. The current sharing function allows a maximum of five units to be connected in parallel to boost the total output power to 5 times. The output voltage is trimmable up to +10% or down –20%.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

FEATURES

- High Reliability
- Output Voltage Trim Up +10% or Down –20%
- Wide Input Voltage Range: 16 to 50 Volts
- Up to 200 Watts Output Power
- Up to 70% of Rated Output Power is Available for Each Output
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Short Circuit Protection
- Current Limit Protection
- Input Transient Voltage: 55 Volts for 1 second
- High Power Density: ≈ 80 W/in³
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVMN28 EMI Filter

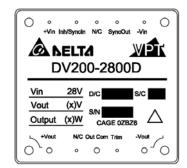


Figure 1 – DV200-2800D DC-DC Converter (Not To Scale)



+25°C

-65°C to +135°C



 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +28V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

ABSOLUTE MAXIMUM RATINGS

 $\begin{array}{ll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)}^4 & 55 \ \mbox{Volts} \\ \mbox{Output Power}^{1,3} & 200 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +100^{\circ}$C)} & 44 \ \mbox{Watts} \\ \end{array}$

Junction Temperature Rise to Case

Storage Temperature

Lead Solder Temperature (10 seconds) 270°C Weight (Maximum) 115 grams

Parameter		Conditions	DV200-2805D			DV200-2812D			Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Ullits
STATIC									
INPUT		Continuous	16	28	50	16	28	50	V
Voltage ⁷		Transient, 1 sec ⁴	-	-	55	-	-	55	V
Current		Inhibited	-	6	10	-	6	10	mA
Current		No Load	-	70	100	-	50	80	mA
Ripple Current		Full Load ⁵ , 20Hz to 10MHz	-	-	150	-	-	250	mA _{p-p}
Inhibit Pin Input ⁴			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Vol	tage⁴		8	9.5	11	8	9.5	11	V
UVLO Turn On	-		13.5	14	15.8	13.5	14	15.8	V
UVLO Turn Off⁴			10	12	14	10	12	14	V
	+V _{OUT}	T _{CASE} = 25°C	4.95	5.00	5.05	11.88	12.00	12.12	V
OUTPUT +V ₀		T _{CASE} = -55°C to +100°C	4.925	5.00	5.075	11.82	12.00	12.18	V
Voltage	-V _{OUT}	T _{CASE} = 25°C	4.80	5.00	5.20	11.76	12.00	12.24	V
	-V _{OUT}	T _{CASE} = -55°C to +100°C	4.75	5.00	5.25	11.52	12.00	12.48	V
		V _{IN} = 18V to 50V	-	-	150	-	-	175	W
	Total	V _{IN} = 16V to 18V	-	-	90	-	-	105	W
Power ^{3,6,7}	$\pm V_{\text{OUT}}$	V _{IN} = 18V to 50V Either Output	-	-	105	-	-	122.5	W
		V _{IN} = 16V to 18V Either Output	-	-	63	-	-	73.5	W
367	11/	V _{IN} = 18V to 50V Either Output	-	-	21	-	-	10.2	Α
Current ^{3,6,7} ±V _{OUT}		V _{IN} = 16V to 18V Either Output	-	-	12.6	-	-	6.1	Α
Ripple Voltage	$\pm V_{\text{OUT}}$	Full Load ⁵ , 20Hz to 10MHz	-	50	150	-	50	200	mV _{p-p}
Line Degulation	+V _{OUT}	V _{IN} = 18V to 40V	-	10	80	-	10	80	mV
Line Regulation	$-V_{OUT}$	V _{IN} = 18V to 40V	-	10	200	-	10	200	mV
Land Daniel-Kan	+V _{OUT}	No Load to Full Load⁵	-	10	80	-	10	80	mV
Load Regulation	-V _{OUT}	No Load to Full Load⁵	-	10	200	-	10	200	mV
Cross Regulation	-V _{OUT}	V1+ Load 30% - Load 70% V2+ Load 70% - Load 30%	-	-	550	-	-	550	mV
Voltage Trim	=	Full Load	-20	-	10	-20	-	10	%
EFFICIENCY		Full Load⁵	79	82	-	80	84	-	%
		Overload ⁴	-	-	80	-	-	80	W
LOAD FAULT POWER DISSIP	ATION	Short Circuit	-	-	80	-	-	80	W
CAPACITIVE LOAD ⁴		Either Output	-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY			400	500	600	400	500	600	kHz
SYNC FREQUENCY RANGE		$V_H - V_L = 5V$ Duty Cycle = 20% - 80%	450	-	550	450	-	550	kHz
ISOLATION		500 V _{DC} , T _{CASE} = 25°C	100	-	-	100	-	-	МΩ
MTBF (MIL-HDBK-217F)		GB @ T _C = 55°C	-	955	-	-	955	-	kHrs

DV200-2800D Series

 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +28V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V _{DC}	Junction Temperature Rise to Case	+25°C
Input Voltage (Transient, 1 second) ⁴	55 Volts	Storage Temperature	-65°C to +135°C
Output Power ^{1,3}	200 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T _{CASE} = +100°C)	44 Watts	Weight (Maximum)	115 grams

Parameter		Conditions	DV200-2805D			DV200-2812D			Units
			Min	Тур	Max	Min	Тур	Max	Office
DYNAMIC	DYNAMIC								
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	500	-	-	700	mV_{PK}
Load Step Recovery ²		Tiali Load to Full Load	-	-	500	-	-	500	μSec
Line Step Output Transient ⁴	±V _{OUT}	V _{IN} = 18V to 40V	-	300	600	-	600	1200	mV_{PK}
Line Step Recovery ^{2, 4}		V _{IN} - 10V to 40V	-	300	500	-	300	500	μSec
Turn On Delay	±V _{OUT}	V _{IN} = 0V to 28V	-	20	30	-	20	30	mSec
Turn On Overshoot		VIN - UV 10 20V	-	-	50	-	-	50	mV_{PK}

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 110°C.
- 4. Verified by qualification testing.

- Verified by qualification testing.
 Half load at +V_{OUT} and half load at -V_{OUT}.
 Up to 70% of the total power or current can be drawn from any one of the two outputs.
 100% output power available for V_{IN} = 18V to 50V and only 60% output power available for V_{IN} = 16V to 18V.



 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +28V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

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 $\begin{array}{ll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)}^4 & 55 \ \mbox{Volts} \\ \mbox{Output Power}^{1,3} & 200 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +100^{\circ}$C)} & 44 \ \mbox{Watts} \\ \end{array}$

Junction Temperature Rise to Case Storage Temperature

Weight (Maximum)

Lead Solder Temperature (10 seconds)

-65°C to +135°C 270°C 115 grams

+25°C

Parameter		Conditions	D	Units		
		Conditions	Min	Тур	Max	Units
STATIC						
INPUT		Continuous	16	28	50	V
Voltage ⁷		Transient, 1 sec4	-	ı	55	V
Outmant		Inhibited	-	6	10	mA
Current		No Load	-	50	80	mA
Ripple Current		Full Load ⁵ , 20Hz to 10MHz	-	1	300	mA _{p-p}
Inhibit Pin Input⁴			0	1	1.5	V
Inhibit Pin Open Circuit Vol	tage⁴		8	9.5	11	V
UVLO Turn On			13.5	14	15.8	V
UVLO Turn Off⁴			10	12	14	V
	+V _{OUT}	T _{CASE} = 25°C	14.85	15.00	15.15	V
OUTPUT	+ V_{OUT}	T_{CASE} = -55°C to +100°C	14.775	15.00	15.225	V
Voltage	$-V_{OUT}$	T _{CASE} = 25°C	14.70	15.00	15.30	V
	-V _{OUT}	$T_{CASE} = -55^{\circ}C \text{ to } +100^{\circ}C$	14.40	15.00	15.60	V
	Total	V _{IN} = 18V to 50V	-	ı	200	W
		V _{IN} = 16V to 18V	-	-	120	W
Power ^{3,6,7}	$\pm V_{OUT}$	V _{IN} = 18V to 50V Either Output	-	ı	140	W
	T V OUI	V _{IN} = 16V to 18V Either Output	-	-	84	W
Current ^{3,6,7}	$\pm V_{\text{OUT}}$	V _{IN} = 18V to 50V Either Output	-	-	9.3	Α
Current	± V OUT	V _{IN} = 16V to 18V Either Output	-	ı	5.6	Α
Ripple Voltage	$\pm V_{\text{OUT}}$	Full Load ⁵ , 20Hz to 10MHz	-	50	200	mV_{p-p}
Line Regulation	+V _{OUT}	V _{IN} = 18V to 40V	-	10	80	mV
Line Regulation	$-V_{OUT}$	V _{IN} = 18V to 40V	-	10	200	mV
Load Regulation	+V _{OUT}	No Load to Full Load⁵	-	10	80	mV
Load Negulation	$-V_{OUT}$	No Load to Full Load⁵	-	10	200	mV
Cross Regulation	-V _{OUT}	V1+ Load 30% - Load 70% V2+ Load 70% - Load 30%	-	-	550	mV
Voltage Trim		Full Load	-20	-	10	%
EFFICIENCY		Full Load ⁵	81	85	-	%
LOAD FAULT POWER DISSIPATION		Overload ⁴	-	-	80	W
		Short Circuit	-	ı	80	W
CAPACITIVE LOAD4		Either Output	-	-	1000	μF
SWITCHING FREQUENCY			400	500	600	kHz
SYNC FREQUENCY RANGE		$V_H - V_L = 5V$ Duty Cycle = 20% - 80%	450	-	550	kHz
ISOLATION		500 V _{DC} , T _{CASE} = 25°C	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		GB @ T _C = 55°C	-	955	_	kHrs



SPECIFICATIONS (T_{CASE} = -55°C to +100°C, V_{IN} = +28V ± 5%, Full Load⁵, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V _{DC}	Junction Temperature Rise to Case	+25°C
Input Voltage (Transient, 1 second) ⁴	55 Volts	Storage Temperature	-65°C to +135°C
Output Power ^{1,3}	200 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T _{CASE} = +100°C)	44 Watts	Weight (Maximum)	115 grams

Parameter		Conditions	D	Units		
		Conditions	Min	Тур	Max	Office
DYNAMIC						
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	700	mV_{PK}
Load Step Recovery ²		Hall Load to Full Load	-	-	500	μSec
Line Step Output Transient ⁴	±V _{OUT}	\/ = 19\/ to 40\/	-	600	1200	mV_{PK}
Line Step Recovery ^{2, 4}		V_{IN} = 18V to 40V	-	300	500	μSec
Turn On Delay	±V _{OUT}	V _{IN} = 0V to 28V	-	20	30	mSec
Turn On Overshoot		V _{IN} - UV 10 20V	-	-	50	mV_{PK}

Notes: 1. Dependant on output voltage.

- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 110°C.
- 4. Verified by qualification testing.

- 5. Half load at +V_{OUT} and half load at -V_{OUT}.
 6. Up to 70% of the total power or current can be drawn from any one of the two outputs.
 7. 100% output power available for V_{IN} = 18V to 50V and only 60% output power available for V_{IN} = 16V to 18V.



DV200-2800D Series

BLOCK DIAGRAM

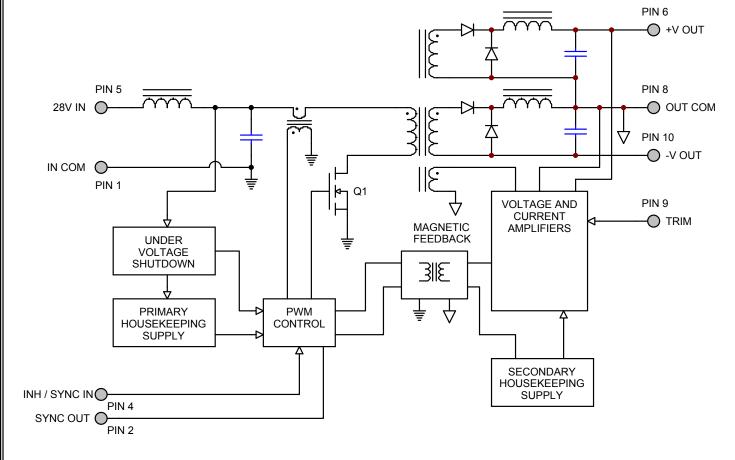


Figure 2

CONNECTION DIAGRAM

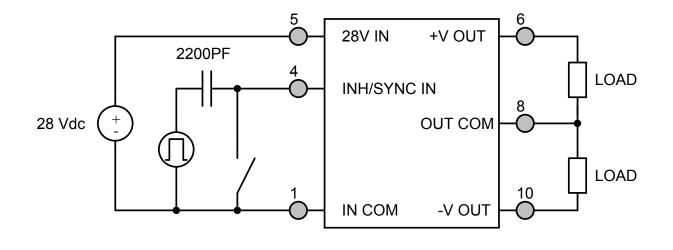


Figure 3



INHIBIT DRIVE CONNECTION DIAGRAMS

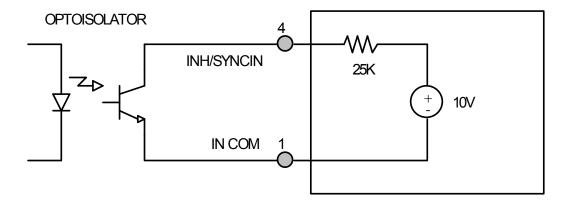
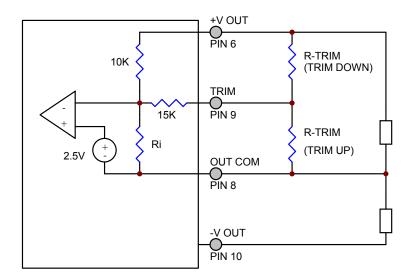


Figure 4 – Isolated Inhibit Drive and Internal Equivalent Circuit



OUTPUT VOLTAGE TRIM



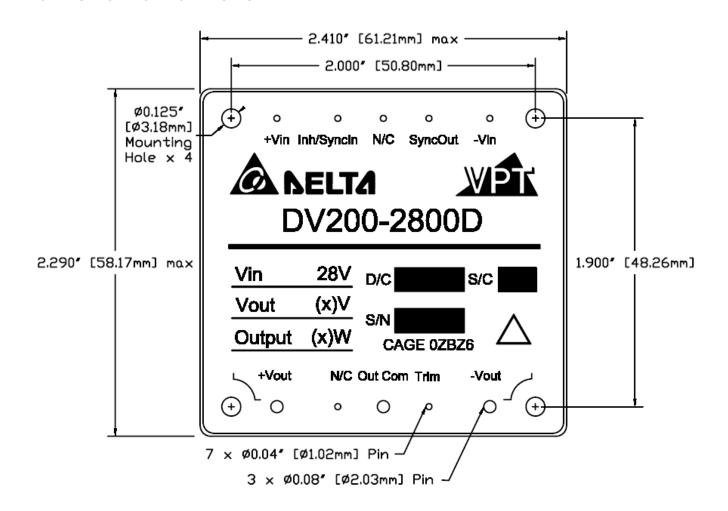
The output voltage can be trimmed down by connecting a resistor between the TRIM pin (PIN 9) and the +V OUT pin (PIN 6), or can be trimmed up by connecting a resistor between the TRIM pin (PIN 9) and the OUT COM pin (PIN 8). The maximum trim range is +10% up and -20% down. The appropriate resistor values versus the output voltage are given in the trim table below.

Figure 5 – Output Voltage Trim

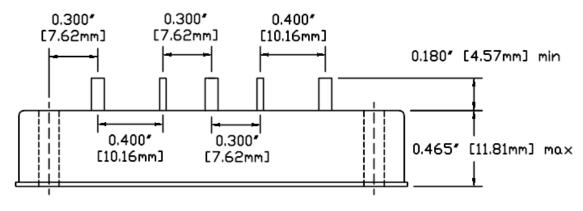
DV200-2805D		DV200	-2812D	DV200-2815D		
±V _{OUT} (V)	R _{TRIM} (Ω)	±V _{OUT} (V)	R _{TRIM} (Ω)	±V _{OUT} (V)	R _{TRIM} (Ω)	
5.5	35k	13.2	5.8k	16.50	1.7k	
5.4	47.5k	13.0	10k	16.25	5k	
5.3	68.3k	12.8	16.2k	16.00	10k	
5.2	110k	12.6	26.6k	15.75	18.3k	
5.1	235k	12.4	47.3k	15.50	35k	
5.0	-	12.2	109k	15.25	85k	
4.9	225k	12.0	-	15.00	-	
4.8	100k	11.8	454k	14.75	475k	
4.7	58.3k	11.6	213k	14.50	225k	
4.6	37.5k	11.4	134k	14.25	142k	
4.5	25k	11.2	94k	14.00	100k	
4.4	16.7k	11.0	70.1k	13.75	75k	
4.3	10.7k	10.8	54.3k	13.50	58.3k	
4.2	6.3k	10.6	42.9k	13.25	46.4k	
4.1	2.8k	10.4	34.4k	13.00	37.5k	
4.0	0	10.2	27.8k	12.75	30.6k	
		10.0	22.5k	12.50	25k	
		9.8	18.2k	12.25	20.5k	
		9.6	14.6k	12.00	16.7k	



PACKAGE SPECIFICATIONS



TOP VIEW



PIN	FUNCTION
1	IN COM
2	SYNC OUT
3	N/C
4	INH / SYNC IN
5	28V IN
6	+V OUT
7	N/C
8	OUT COM
9	TRIM
10	-V OUT

SIDE VIEW

Figure 6 – Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	IN COM	Input Common Connection
2	SYNC OUT	Output Synchronization Signal
3	N/C	No Connection
4	INH / SYNC IN	Logic Low = Disabled Output. Unconnected or open collector TTL or Square-wave Synchronization Signal = Enabled Output.
5	28V IN	Positive Input Voltage Connection
6	+V OUT	Positive Output Voltage Connection
7	N/C	No Connection
8	OUT COM	Output Common Connection
9	TRIM	Trim Output Voltage to +10%, -20% of Nominal Value
10	-V OUT	Negative Output Voltage Connection

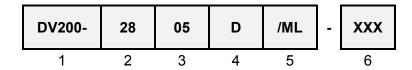
ENVIRONMENTAL SCREENING

Screening	Condition	Standard (No Suffix)	Military /ML
Pre-Cap Inspection	IPC-A-610 Class II	•	•
Temperature Cycling	-55°C, 100°C, 10 Cycles		•
Burn-In	96 hours at +100°C 12 hours at +100°C	•	•
Final Electrical	100% at -55°C, 25°C, 100°C ¹ 100% at 25°C	•	•
Final Inspection	MIL-STD-883, Test Method 2009	•	•

Note: 1. 100% R&R testing at –55°C, +25°C, and +100°C with all test data included in product shipment.



ORDERING INFORMATION



(1) (2)

Product Series	Nominal Input Voltage		Output	Voltage
DV200-	28	28 Volts	05 12 15	±5 Volts ±12 Volts ±15 Volts

(4) (5)

Number of Outputs		Screenir	ng Code ¹	Additional Screening Code	
D	Dual	None /ML	Standard Military	Contact Sales	

Notes: 1. VPT Inc. reserves the right to ship higher screened products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 353-3010 **Fax**: (425) 353-4030

E-mail: vptsales@vpt-inc.com

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