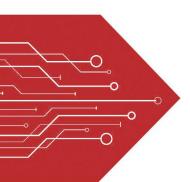
MSKSEMI















ESD

TVS

TSS

MOV

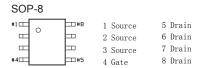
GDT

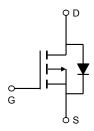
PLED

Broduct data sheet









■ Features

- V_{DS} (V) =-30V
- ID =-15 A (VGS =-10V)
- RDS(ON) < 7.5m Ω (VGS =-10V)
- RDS(ON) < 12m Ω (VGS =-4.5V)

Absolute Maximum Ratings Ta = 25° C

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage		VDS	-30	V	
Gate-Source Voltage		Vgs	±20		
Continuous Drain Current	Ta=25°C	lo	-15		
	Ta=70°C		-12.8	Α	
Pulsed Drain Current	IDM	-80	A		
Avalanche Current		las,lar			30
Avalanche energy	L=0.1mH	Eas,Ear	135	mJ	
Power Dissipation	Ta=25°C	PD	3.1	W	
	Ta=70°C		2	VV	
Thermal Resistance.Junction- to-Ambient	t ≤ 10s	RthJA	40		
	Steady-State		75	°C/W	
Thermal Resistance.Junction- to-Lead		RthJL	24		
Junction Temperature		TJ	150	$^{\circ}$	
Junction Storage Temperature Range		Tstg	-55 to 150		

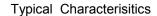


Electrical Characteristics Ta = 25℃

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VDSS	ID=-250 μ A, VGS=0V	-30			V
Zoro Coto Voltago Proin Current	lean	VDS=-30V, VGS=0V			-5	uA
Zero Gate Voltage Drain Current	IDSS	VDS=-30V, VGS=0V, TJ=55°C			-25	
Gate-Body leakage current	IGSS	VDS=0V, VGS=±20V			±100	nA
Gate Threshold Voltage	VGS(th)	VDS=VGS ID=-250 μ A	-1.4		-2.7	V
Static Drain-Source On-Resistance	Rds(on)	Vgs=-10V, Ip=-15A			7.5	
		Vgs=-10V, lp=-15A TJ=125℃			11.5	mΩ
		Vgs=-4.5V, ID=-10A			12	
On state drain current	ID(ON)	Vgs=-10V, Vps=-5V	-80			Α
Forward Transconductance	gFS	VDS=-5V, ID=-15A	35	50		S
Input Capacitance	Ciss			5270	6400	pF
Output Capacitance	Coss	Vgs=0V, Vds=-15V, f=1MHz		945		
Reverse Transfer Capacitance	Crss	1		745		
Gate resistance	Rg	Vgs=0V, Vps=0V, f=1MHz		2	3	Ω
Total Gate Charge (10V)	Qg			100	120	nC
Total Gate Charge (4.5V)	Qg	Vgs=-10V, Vds=-15V, ld=-15A		51.5		
Gate Source Charge	Qgs	VGS10V, VDS15V, ID15A		14.5		
Gate Drain Charge	Qgd]		23		
Turn-On DelayTime	td(on)			14		ns
Turn-On Rise Time	tr	Vgs=-10V, Vds=-15V, RL=1Ω,		16.5		
Turn-Off DelayTime	td(off)	Rgen=3Ω		76.5		
Turn-Off Fall Time	tf			37.5		
Body Diode Reverse Recovery Time	trr	I _F =-15A, d _i /d _t =100A/us		36.7	45	
Body Diode Reverse Recovery Charge	Qrr	1115A, u/u(-100A/u5		28		nC
Maximum Body-Diode Continuous Current	Is				-5	Α
Diode Forward Voltage	Vsd	Is=-1A,VGS=0V			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.





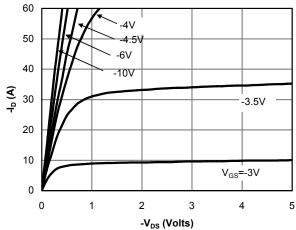


Fig 1: On-Region Characteristics (Note E)

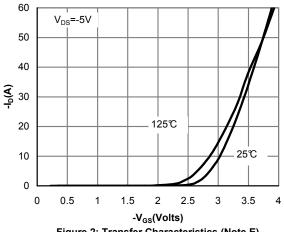


Figure 2: Transfer Characteristics (Note E)

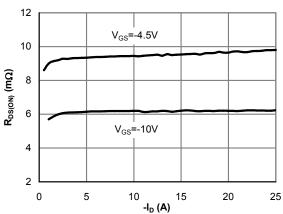


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

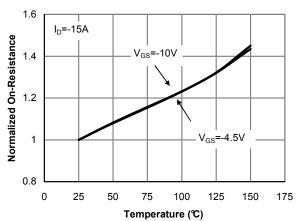


Figure 4: On-Resistance vs. Junction Temperature (Note E)

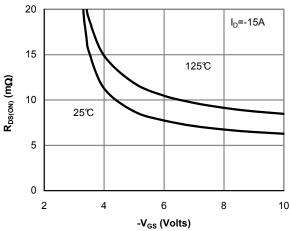


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

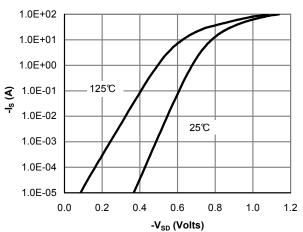
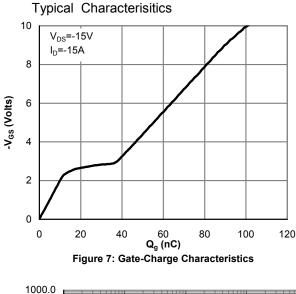


Figure 6: Body-Diode Characteristics (Note E)







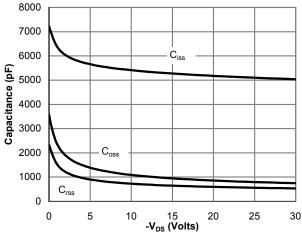
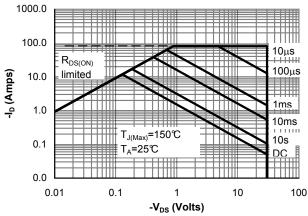


Figure 8: Capacitance Characteristics



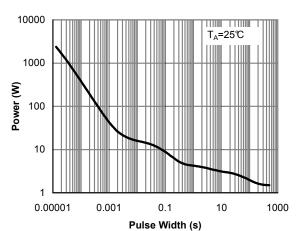


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

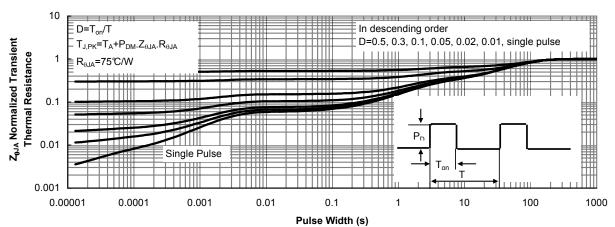
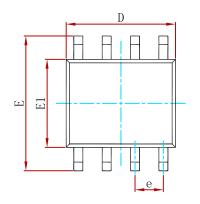
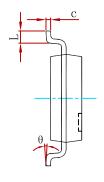


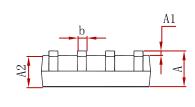
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)



PACKAGE MECHANICAL DATA

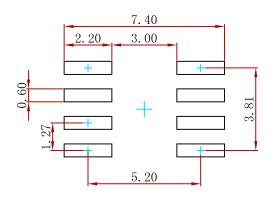






Symbol	Dimensions In	Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0.100	0. 250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0. 250	0.007	0.010	
D	4.800	5.000	0.189	0. 197	
e	1.270 (BSC)		0.050 (BSC)		
E	5.800	6. 200	0. 228	0. 244	
E1	3.800	4.000	0. 150	0. 157	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
 2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
AO4409-M	S SOP-8	3000



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