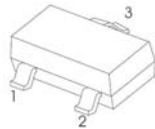


### FEATURE

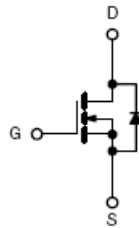
- High density cell design for low  $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

### SOT-23

1. GATE
2. SOURCE
3. DRAIN

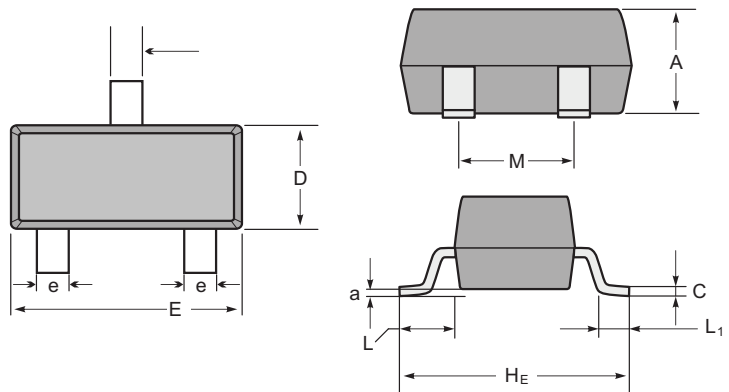


### Equivalent Circuit



### Marking

Type number	Marking code
SI2302	A2SHB



SOT-23 mechanical data

UNIT		A	C	D	E	H <sub>E</sub>	e	M	L	L <sub>1</sub>	a
mm	max	1.1	0.15	1.4	3.0	2.6	0.5	1.95	0.55 (ref)	0.36 (ref)	0.0
	min	0.9	0.08	1.2	2.8	2.2	0.3	1.7			0.15
mil	max	43	6	55	118	102	20	77	22 (ref)	14 (ref)	0.0
	min	35	3	47	110	87	12	67			6

### Maximum ratings ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current	$I_D$	3	A
Continuous Source-Drain Current(Diode Conduction)	$I_S$	0.6	
Power Dissipation	$P_D$	1.2	W
Thermal Resistance from Junction to Ambient ( $t \leq 5s$ )	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Operating Junction	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	

# SI2302

## Electrical characteristics (T<sub>a</sub>=25°C unless otherwise noted)

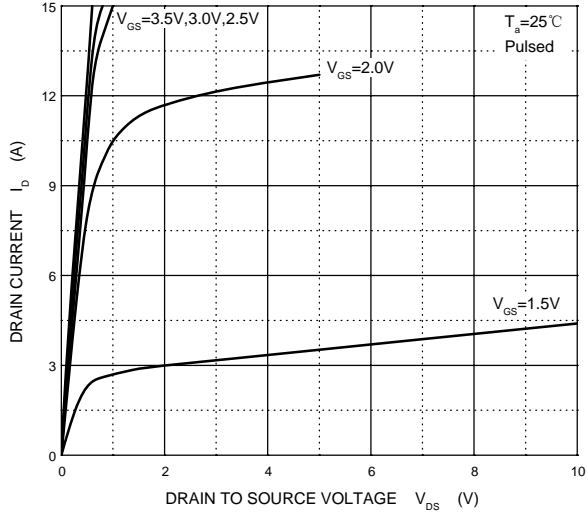
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Static</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 10μA	20			V
Gate-threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 50μA	0.65	0.95	1.2	
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±8V			±100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V			1	μA
Drain-source on-resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3.6A		0.025	0.040	Ω
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.1A		0.050	0.95	
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 3.6A		8		S
Diode forward voltage	V <sub>SD</sub>	I <sub>S</sub> = 0.94A, V <sub>GS</sub> = 0V		0.76	1.2	V
<b>Dynamic</b>						
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3.6A		4.0	10	nC
Gate-source charge	Q <sub>gs</sub>			0.65		
Gate-drain charge	Q <sub>gd</sub>			1.5		
Input capacitance <sup>b</sup>	C <sub>iss</sub>	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1MHz		300		pF
Output capacitance <sup>b</sup>	C <sub>oss</sub>			120		
Reverse transfer capacitance <sup>b</sup>	C <sub>rss</sub>			80		
<b>Switching<sup>b</sup></b>						
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10V, R <sub>L</sub> = 5.5Ω, I <sub>D</sub> ≈ 3.6A, V <sub>GEN</sub> = 4.5V, R <sub>g</sub> = 6Ω		7	15	ns
Rise time	t <sub>r</sub>			55	80	
Turn-off delay time	t <sub>d(off)</sub>			16	60	
Fall time	t <sub>f</sub>			10	25	

### Notes :

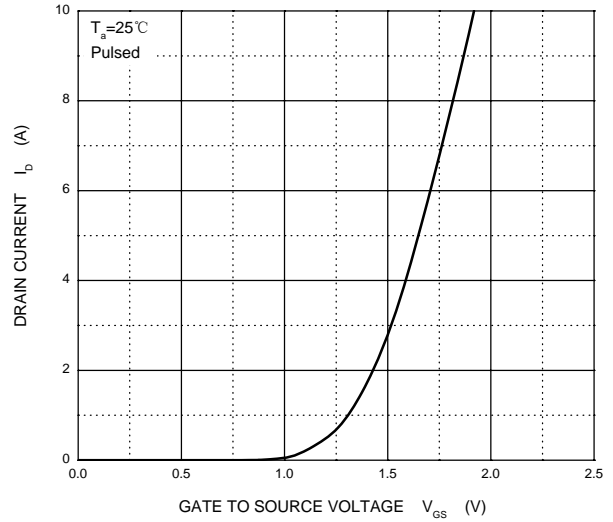
- Pulse Test : Pulse width ≤ 300μs, duty cycle ≤ 2%.
- These parameters have no way to verify.

## RATING AND CHARACTERISTIC CURVES (SI2302)

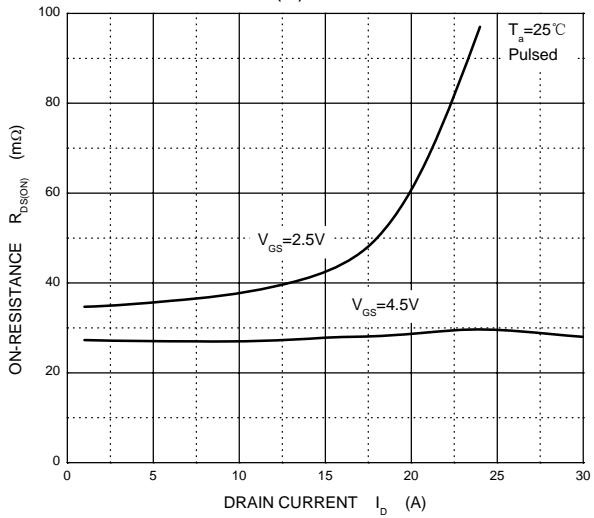
**Output Characteristics**



**Transfer Characteristics**



$R_{DS(ON)}$  —  $I_D$



$R_{DS(ON)}$  —  $V_{GS}$

