

3.3 Volt TVS Array For ESD and Latch-Up Protection

PROTECTION PRODUCTS

Description

The SMS series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD, lightning, and other voltage induced transient events. Each device will protect up to four lines operating at 3.3 volts.

The SMS3.3 is a solid state device designed specifically for transient suppression. It is constructed using Semtech's proprietary EPD process technology. The EPD process provides low standoff voltages with significant reductions in leakage current and capacitance over traditional pn junction processes. They offer desirable characteristics for board level protection including fast response time, low clamping voltage and no device degradation.

The SMS3.3 may be used to meet the immunity requirements of IEC 61000-4-2, level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge). The low cost SOT23-6L package makes them ideal for use in portable electronics such as cell phones, PDA's and notebook computers.

Features

- Transient Protection to
 - ♦ IEC 61000-4-2 (ESD): 15kV (Air), 8kV (Contact)
 - ♦ IEC 61000-4-4 (EFT): 40A (5/50ns)
 - ♦ IEC 61000-4-5 (Lightning): 12A (8/20 μ s)
- Protects four I/O lines
- Working voltage: 3.3V
- Low leakage current ($< 1\mu\text{A}$)
- Low clamping voltage
- Low capacitance (35pF typical)
- Solid-state EPD TVS technology

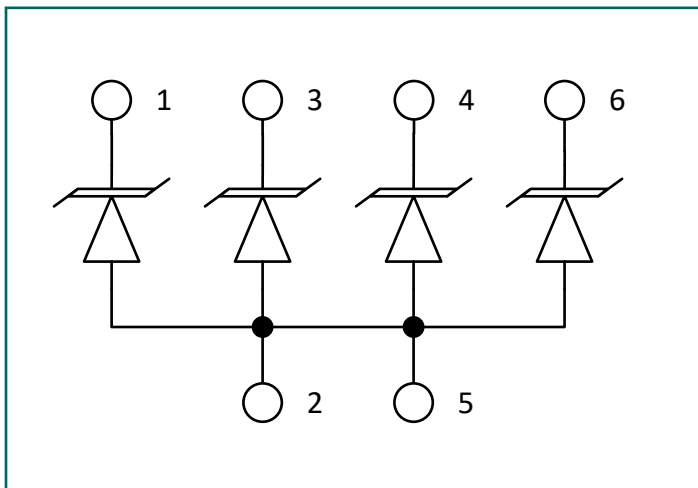
Mechanical Characteristics

- EIAJ SOT23-6L Package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Molding Compound Flammability Rating: UL 94V-0
- Marking : Marking Code
- Packaging : Tape and Reel per EIA 481

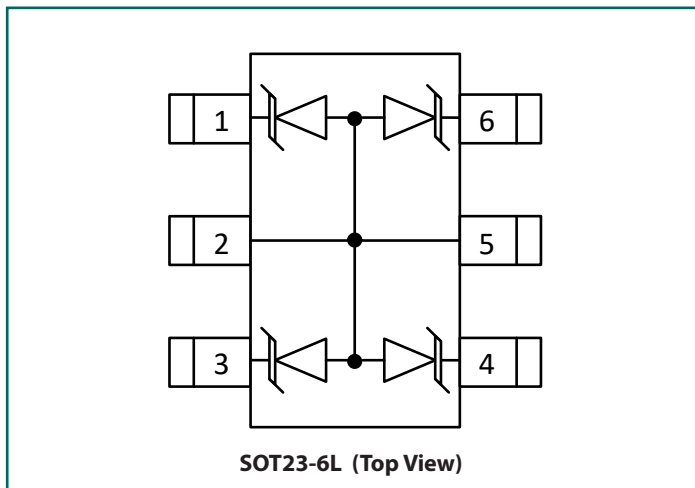
Applications

- Cell phone Handsets and Accessories
- Microprocessor Based Equipment
- Personal Digital Assistants (PDA's) and Pagers
- Desktop PC and Servers
- Notebook, Laptop and Palmtop Computers
- Portable Instrumentation
- Peripherals
- MP3 Players
- Cordless Phones

Circuit Diagram



Schematic and Pin Configuration



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	P _{PK}	220	W
Peak Pulse Current (tp = 8/20μs)	I _{PP}	12	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	>25 >15	kV
Soldering Temperature	T _L	260 (10 seconds)	°C
Operating Temperature	T _J	-55to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

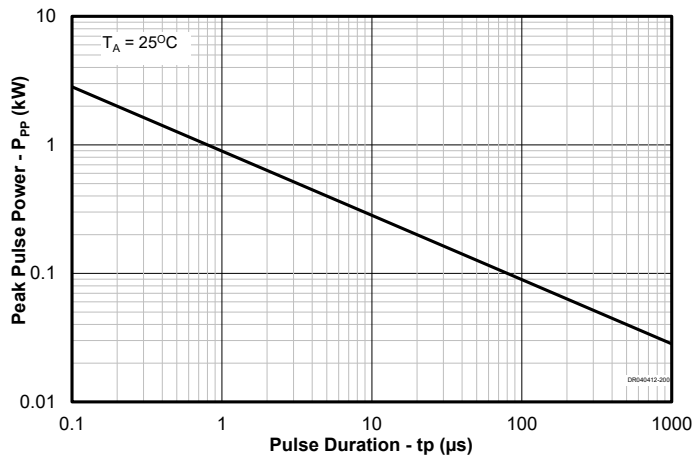
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}				3.3	V
Punch-Through Voltage	V _{PT}	I _{PT} = 2μA	3.5			V
Snap-Back Voltage	V _{SB}	I _{SB} = 50mA	2.8			V
Reverse Leakage Current	I _R	V _{RWM} = 3.3V, T = 25°C		0.05	0.5	μA
Clamping Voltage	V _C	I _{PP} = 1A, tp = 8/20μs Any I/O to GND			4.5	V
		I _{PP} = 5A, tp = 8/20μs Any I/O to GND			6.8	
		I _{PP} = 12A, tp = 8/20μs Any I/O to GND			8.7	
Steering Diode Forward Voltage (Reverse Clamping Voltage)	V _F	I _{PP} = 1A, tp = 8/20μs Any I/O to GND			1.7	V
Junction Capacitance	C _J	Each I/O pin and Ground V _R = 0V, f = 1MHz		35	40	pF

Notes:

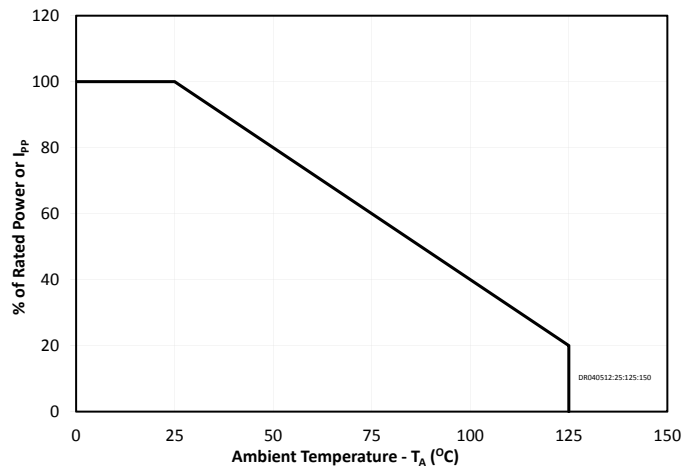
(1): ESD Gun return path to Ground Reference Plane (GRP)

Typical Characteristics

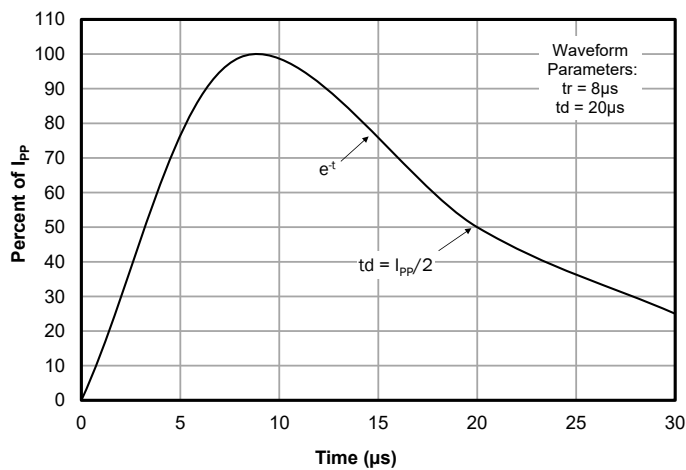
Non-Repetitive Peak Pulse Power vs. Pulse Time



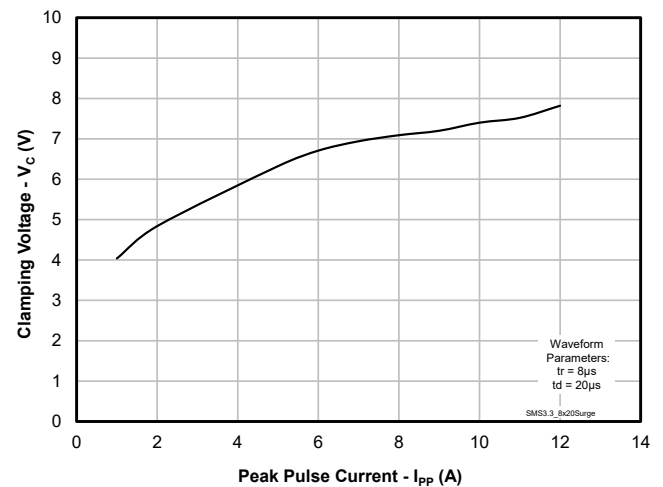
Power Derating Curve



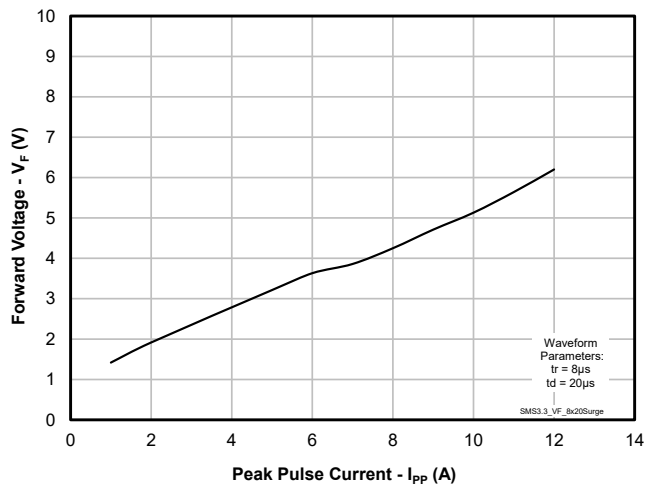
Pulse Waveform



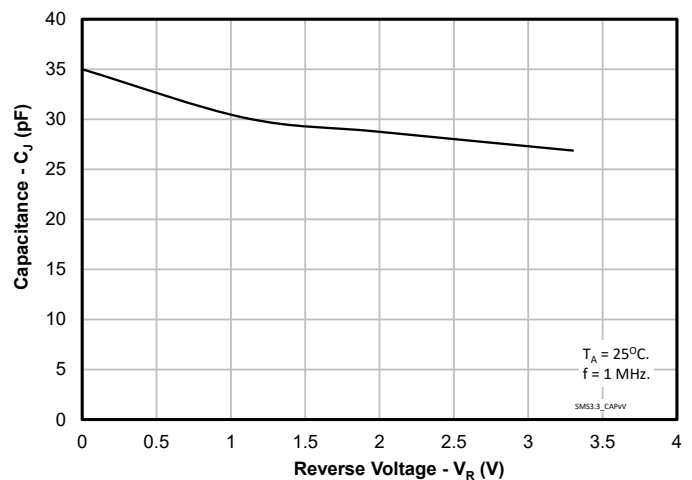
Clamping Voltage vs. Peak Pulse Current (8/20 μ s Pulse)



Forward Voltage vs. Peak Pulse Current (8/20 μ s Pulse)



Capacitance vs. Reverse Voltage



Application Information

Device Connection for Protection of Four Data Lines

The SMS3.3 is designed to protect up to four unidirectional data lines. The device is connected as follows:

1. Unidirectional protection of four I/O lines is achieved by connecting pins 1, 3, 4 and 6 to the data lines. Pin 2 and 5 are connected to ground. The ground connections should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

Circuit Board Layout Recommendations for suppression of ESD

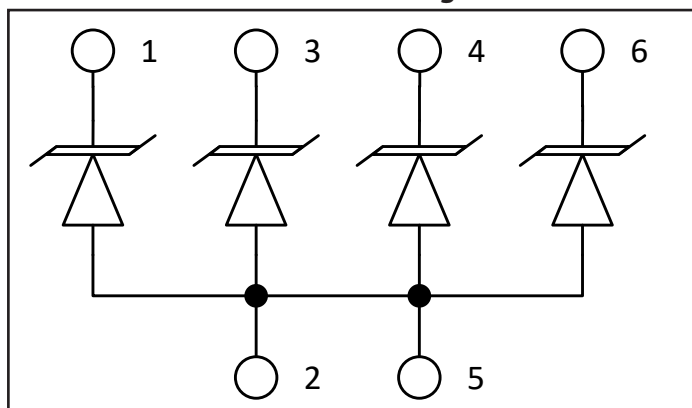
Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

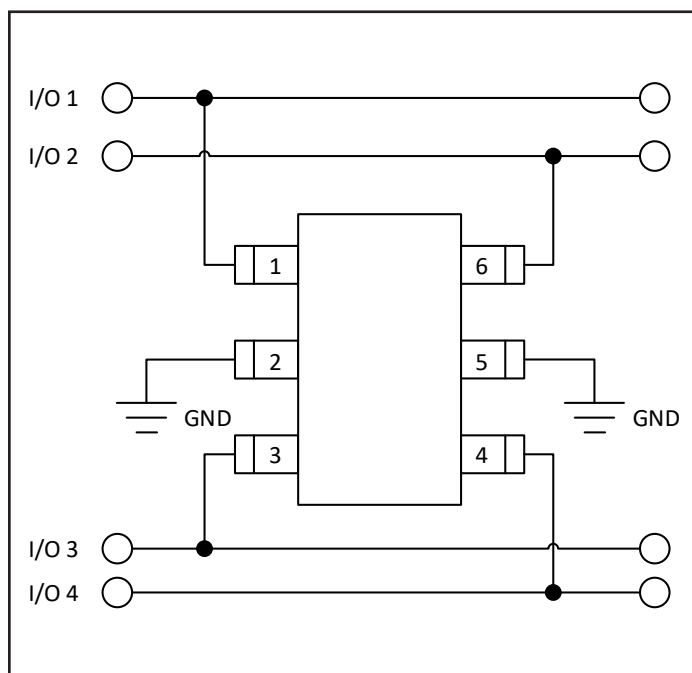
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

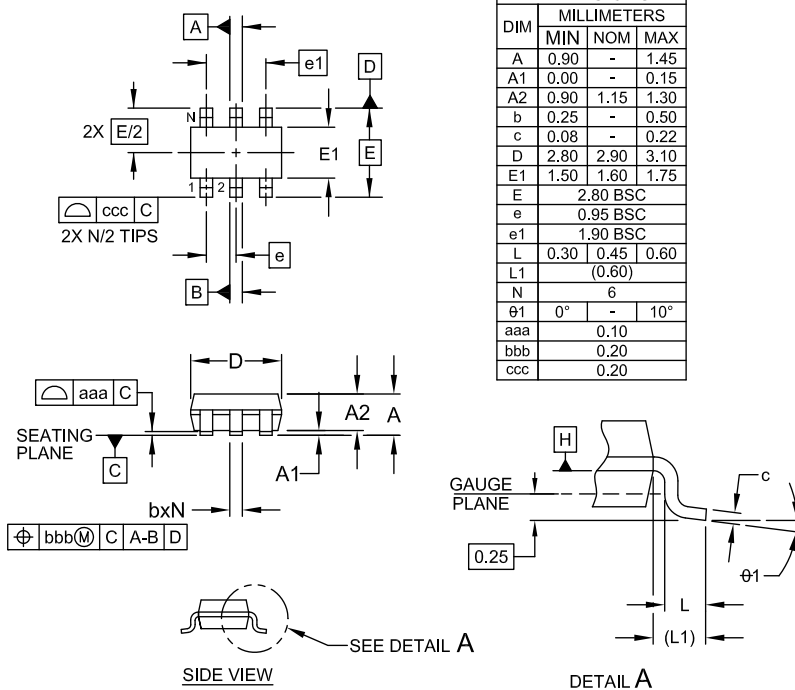
SMS3.3 Circuit Diagram



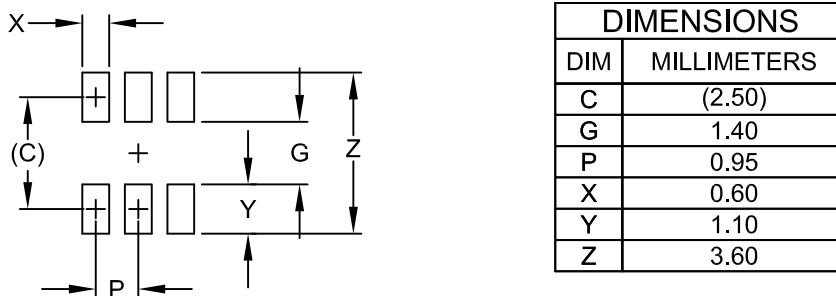
Protection of Four Unidirectional Lines



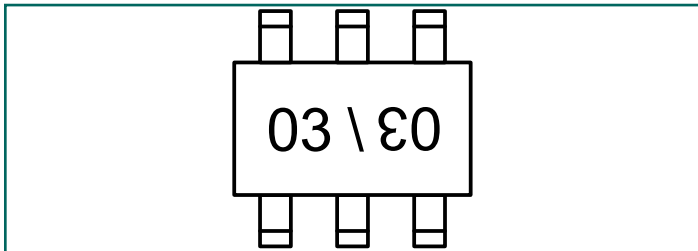
Outline Drawing - SOT23-6L



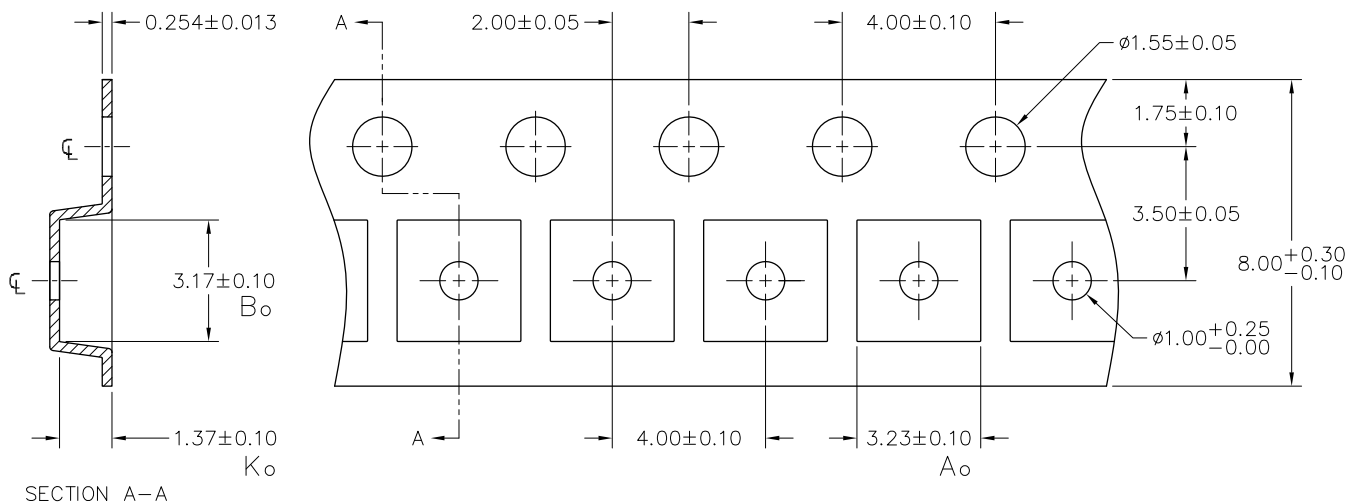
Land Pattern - SOT23-6L



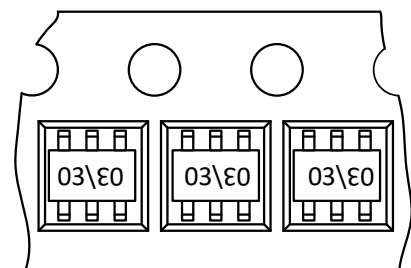
Marking Code



Tape and Reel Specification



NOTES: 1.) ALL DIMENSIONS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.



User Direction of feed

Ordering Information

Part Number	Qty per Reel	Reel Size	Pitch
SMS3.3.TCT	3000	7 Inch	4mm



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