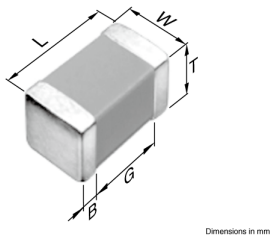
Creation Date : February 20, 2017 (GMT)

Multilayer Ceramic Chip Capacitors

C2012C0G2A682K125AA

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C2012C0G2A682KT****
Applications	Commercial Grade
Feature	Mid Voltage (100 to 630V)
Series	C2012 [EIA 0805]
Status	Production (Not Recommended for New Design)



Size	
Length(L)	2.00mm ±0.20mm
Width(W)	1.25mm ±0.20mm
Thickness(T)	1.25mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	1.00mm to 1.30mm(Flow Soldering) 0.90mm to 1.20mm(Reflow Soldering)
Recommended Land Pattern (PB)	1.00mm to 1.20mm(Flow Soldering) 0.70mm to 0.90mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.80mm to 1.10mm(Flow Soldering) 0.90mm to 1.20mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	6.8nF ±10%
Rated Voltage	100VDC
Temperature Characteristic	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	2000pcs

! Images are for reference only and show exemplary products.
! This PDF document was created based on the data listed on the TDK Corporation website.
! All specifications are subject to change without notice.

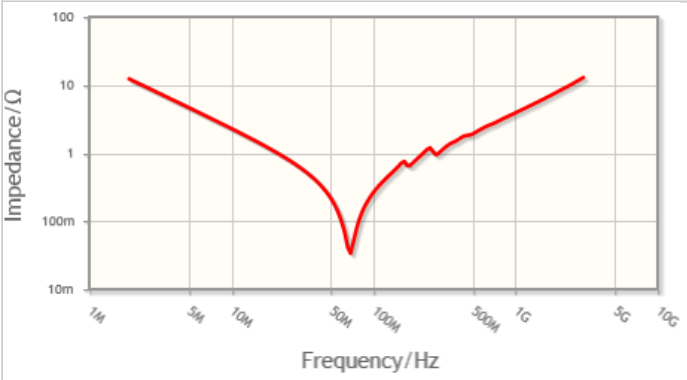
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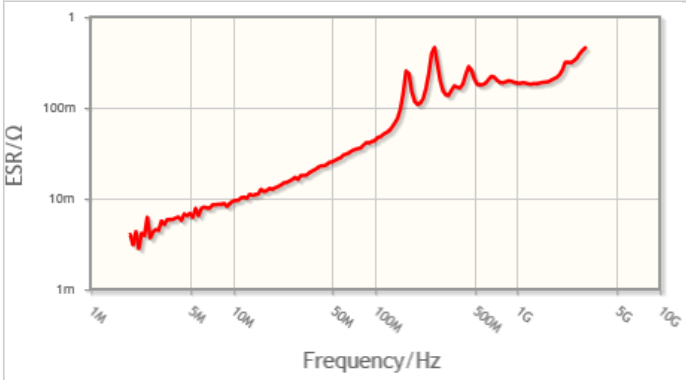
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



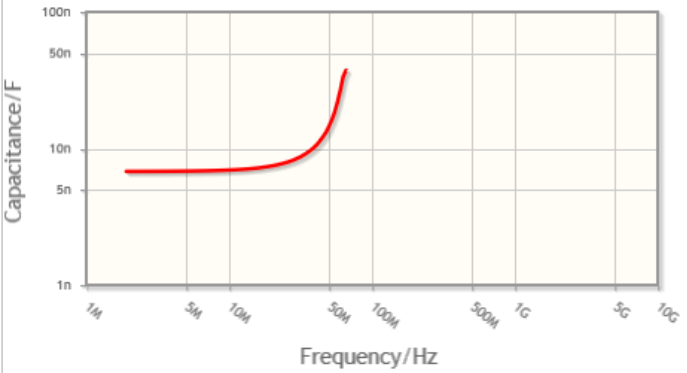
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ESR



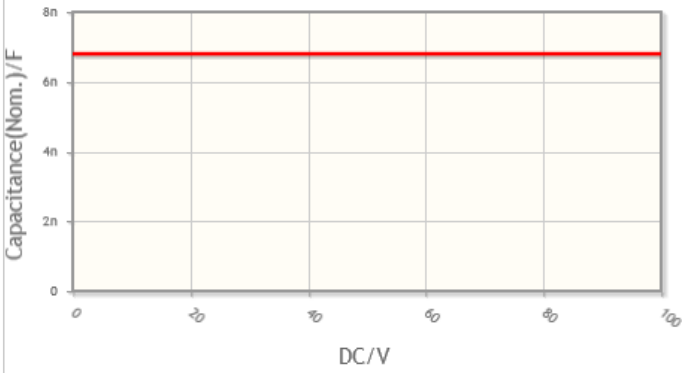
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Capacitance



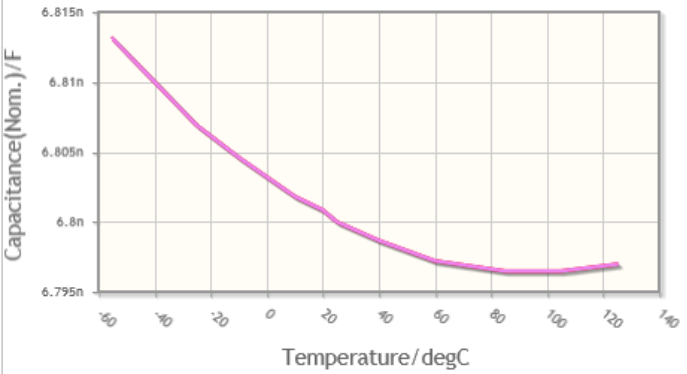
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DC Bias Characteristic



C2012C0G2A682K125AA

Temperature Characteristic



C2012C0G2A682K125AA(No Bias) C2012C0G2A682K125AA(DC Bias = 50V)

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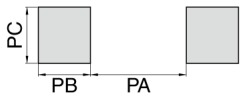
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Associated Images

Land Pattern (Terminal Connection)



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