
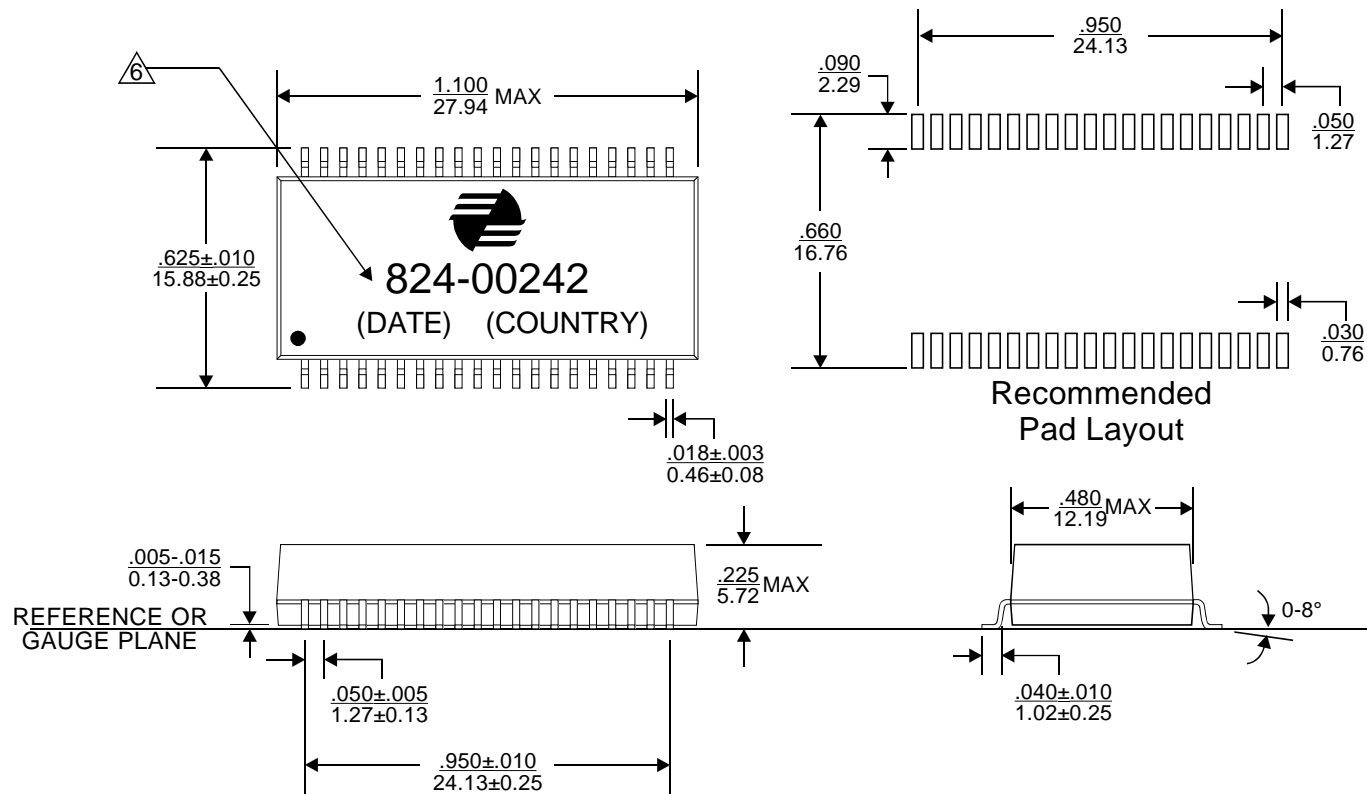


REVISIONS			
REV.	DESCRIPTION	ECN NO.	DATE
01	FIRST RELEASE FOR RFQ#D2004-5613	N/A	11/11/04

PART NUMBER	SOLDER CONTENT	PART DESCRIPTION	TITLE										
824-00242	Sn/Pb	Standard version, containing lead	TRANSFORMER, 100BASE-T, QUAD, SOIC, 40 PIN										
824-00242F	Sn/Ag/Cu	Lead(Pb)-Free version											
<p align="center"><u>WARNING !</u></p> <p>ALL SHEETS OF THIS DOCUMENT ARE CONTROLLED DOCUMENTATION AND ARE NOT TO BE RELEASED OUTSIDE OF E&E OR ITS SUB-CONTRACTORS WITHOUT AUTHORIZATION.</p>	<p>UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCH/mm.</p> <p align="center"><u>TOLERANCE ARE:</u></p> <table border="0"> <tr> <td>INCH</td> <td>mm</td> <td>ANGLE</td> </tr> <tr> <td>.XXX ± .005</td> <td>.XX ± .13</td> <td>X.X ± 0.3</td> </tr> <tr> <td>.XX ± .02</td> <td>.X ± .5</td> <td>X. ± 1</td> </tr> </table>	INCH	mm	ANGLE	.XXX ± .005	.XX ± .13	X.X ± 0.3	.XX ± .02	.X ± .5	X. ± 1	APPROVALS	DATE	 <p align="center">E & E Magnetic Products Ltd.</p>
		INCH	mm	ANGLE									
		.XXX ± .005	.XX ± .13	X.X ± 0.3									
		.XX ± .02	.X ± .5	X. ± 1									
		DRAWN BY	S. CHEUNG	11/11/04	DRAWING NO./MODEL								
PROJ. ENG	D. YUNG	11/11/04											
APPROVED BY	V. TSUI	11/11/04	<p align="center">824-00242</p>										
Q.A.	C.M. TSUI	11/11/04		REV									
			SCALE	PAGE	OF	REV							
			DO NOT SCALE	1	3	01							



1. Dimensions are specified in $\frac{\text{inches}}{\text{mm}}$ with higher precedence in inches.
2. Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$.
3. Coplanarity: $\frac{.004}{0.10}$ max.
4. "(DATE)" includes at least the manufacturing date code (in YYWW format).
5. Dimensions are to be measured in accordance with JEDEC standard 95-1.

6. For lead-free version, 824-00242F, the part will be marked with "824-00242F", instead of "824-00242".

MECHANICAL OUTLINE



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DRAWING NO./MODEL

824-00242

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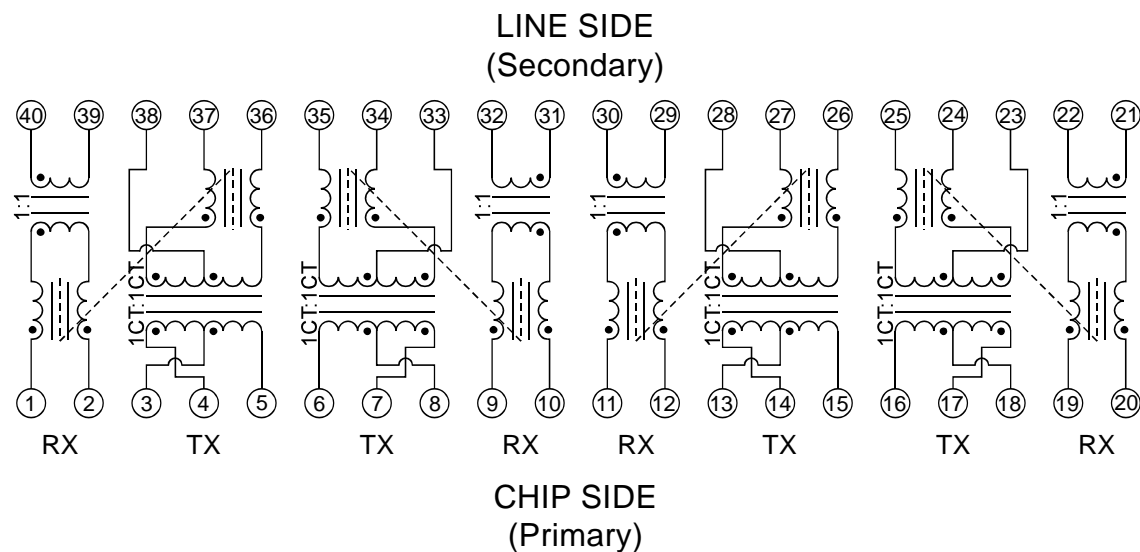
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PAGE

2

OF

3



SCHEMATIC

ELECTRICAL SPECIFICATION @25°C:

PARAMETERS	UNIT	Tx-Side	Rx-Side
Turns Ratio (Primary):(Secondary)	-	1CT:1CT ± 3%	1:1 ± 3%
Polarity (Primary):(Secondary)	-	Per Schematic	Per Schematic
Inductance, Ls(Primary) @100kHz, 0.1Vrms, 8mA _{dc}	uH	350 Min	350 Min
Insertion Loss @0.1-100MHz	dB	1.1 Max	1.1 Max
Return Loss @1-30MHz	dB	18.0 Min	18.0 Min
Return Loss @40MHz	dB	15.5 Min	15.5 Min
Return Loss @50MHz	dB	13.5 Min	13.5 Min
Return Loss @60-80MHz	dB	12.0 Min	12.0 Min
CMRR @0.1-100MHz (for refer. only)	dB	30.0 Min	30.0 Min
Crosstalk @0.1-100MHz (for refer. only)	dB	30.0 Min	
HIPOT (Primary):(Secondary)	Vrms	1500 Min	1500 Min



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DRAWING NO./MODEL

824-00242

REV

01

SCALE

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PAGE

3

OF

3

7. Operating temperature : 0°C to +70°C.