General-purpose Operational Amplifiers / Comparators

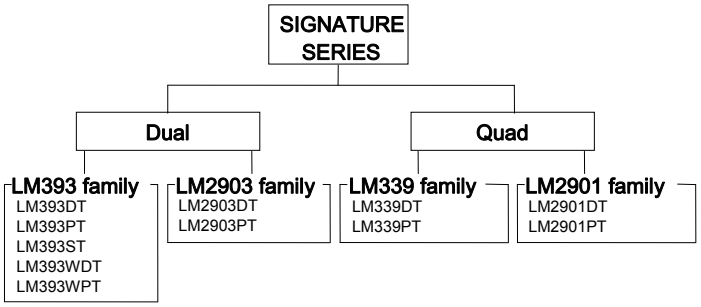
SIGNATURE SERIES Comparators

LM393DT,LM393PT,LM393ST,LM393WDT,LM393WPT, LM2903DT,LM2903PT,LM339DT,LM339PT,LM2901DT,LM2901PT

No.11094ECT04

●Description

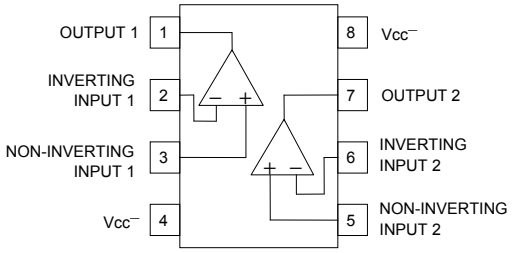
The Universal Standard LM393 / LM339 / LM2903 / LM2901 family monolithic ICs integrate two / four independent comparator circuits on a single chip and feature high gain, low power consumption, and an operating voltage range between 2[V] and 36[V] (single power supply).

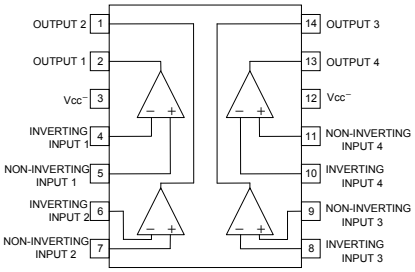


●Features

1) Operating temperature range
   Commercial Grade LM339/393 family : 0[℃] to + 70[℃]
   Extended Industrial Grade LM2903/2901 family : -40[℃] to +125[℃]
2) Open collector output stage
3) Single / dual power supply compatible
4) Low supply current
   0.4[mA] typ.（LM2903/393 family）
   1.1[mA] typ.（LM2901/339 family）
5) Low input-bias current: 25[nA] typ.
6) Low input offset current: 5[nA] typ.
7) Common-mode input voltage range includes ground
8) Differential input voltage is possible to apply the absolute maximum ratings±36[V].
9) Low output saturation voltage
10) TTL, MOS, CMOS compatible output

●Pin Assignment





SO package8	TSSOP8	Mini SO8	SO package14	TSSOP14
LM393DT	LM393PT	LM393ST	LM339DT	LM339PT
LM393WDT	LM393WPT		LM2901DT	LM2901PT
LM2903DT	LM2903PT			

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●Absolute Maximum Ratings (Ta=25℃)

Parameter	Symbol	Ratings				Unit
		LM393 family	LM339 family	LM2903 family	LM2901 family	
Supply Voltage	Vcc^+-Vcc^-	＋36				V
Differential Input Voltage	Vid	±36				V
Common-mode Input Voltage Range	Vicm	-0.3 to +36				V
Operating Temperature Range	Topr	0 to +70		-40 to +125		℃
Storage Temperature Range	Tstg	-65 to +150				℃
Maximum Junction Temperature	Tjmax	+150				℃

●Electric Characteristics

○LM393/339 family(Unless otherwise specified, Vcc^+=+5[V])

Parameter	Symbol	Temperature range	Limits						Unit	Conditions	Fig. No.
			LM393 family			LM339 family					
			Min.	Typ.	Max.	Min.	Typ.	Max.			
Input Offset Voltage (*1)	VIO	25℃	—	1	7	—	1	7	mV	Vcc^+=5 to 30[V],VO=1.4[V], Vicm=0 to -1.5[V]	2
		full range	—	—	9	—	—	9			
Input Offset Current (*1)	IIO	25℃	—	5	50	—	5	50	nA	VO=1.4[V]	2
		full range	—	—	150	—	—	150			
Input Bias Current (*1)	IIB	25℃	—	25	250	—	25	250	nA	VO=1.4[V]	2
		full range	—	—	400	—	—	400			
Large Signal Voltage Gain	AVD	25℃	25	200	—	25	200	—	V/mV	Vcc^+=15[V],VO=1 to 11[V], RL=15[kΩ]	2
Supply Current (All Comparators)	ICC	25℃	—	0.4	1	—	1.1	2	mA	Vcc^+=5V,no load	3
		full range	—	1	2.5	—	1.3	2.5		Vcc^+=30[V],no load	
Input Common-mode Voltage Range	VICM	25℃	—	—	Vcc^+-1.5	—	—	Vcc^+-1.5	V	—	2
		full range	—	—	Vcc^+-2.0	—	—	Vcc^+-2.0			
Differential InputVoltage	VID	25℃	—	—	Vcc^+	—	—	Vcc^+	V	—	—
Low level Output Voltage	VOL	25℃	—	250	400	—	250	400	mV	VID=-1[V],Isink=4[mA]	3
		full range	—	—	700	—	—	700			
High level Output Current	IOH	25℃	—	0.1	—	—	0.1	—	nA	Vcc^+=30[V],VID=1[V] VO=30[V]	3
		full range	—	—	1	—	—	1	μA		
Output Sink Current	Isink	25℃	6	16	—	6	16	—	mA	VID=-1[V],VO=1.5[V]	3
Small Single Response Time	tRE	25℃	—	1.3	—	—	1.3	—	μs	RL=5.1[kΩ], Vcc^+=5[V] VIN=100[mVp-p], Overdrive=5[mV]	3
Large Single Response Time	tREL		—	300	—	—	300	—	ns	RL=5.1[kΩ], Vcc^+=5[V] VIN=TTL input, Vref=1.4[V]	3

(*1) Absolute value

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○LM2903/2901 family(Unless otherwise specified, Vcc+=+5[V])

Parameter	Symbol	Temperature range	Limits						Unit	Conditions	Fig. No.
			LM2903 family			LM2901 family					
			Min.	Typ.	Max.	Min.	Typ.	Max.			
Input Offset Voltage (*2)	VIO	25℃	—	2	7	—	1	7	mV	Vcc^+=5 to 30[V],VO=1.4[V] Vicm=0 to -1.5[V]	2
		full range	—	—	15	—	—	15			
Input Offset Current (*2)	IIO	25℃	—	5	50	—	5	50	nA	VO=1.4[V]	2
		full range	—	—	150	—	—	150			
Input Bias Current (*2)	IIB	25℃	—	25	250	—	25	250	nA	VO=1.4[V]	2
		full range	—	—	400	—	—	400			
Large Signal Voltage Gain	AVD	25℃	25	200	—	25	200	—	V/mV	Vcc^+=15[V],VO=1 to 11[V], RL=15[kΩ]	2
Supply Current (All Comparators)	ICC	25℃	—	0.4	1	—	1.1	2	mA	Vcc^+=5V,no load	3
		full range	—	1	2.5	—	1.3	2.5		Vcc^+=30[V],no load	
Input Common-mode Voltage Range	VICM	25℃	—	—	Vcc^+-1.5	—	—	Vcc^+-1.5	V	—	2
		full range	—	—	Vcc^+-2.0	—	—	Vcc^+-2.0			
Differential Input Voltage	VID	25℃	—	—	Vcc^+	—	—	Vcc^+	V	—	—
Low Level Output Voltage	VOL	25℃	—	250	400	—	250	400	mV	VID=-1[V], Isink=4[mA]	3
		full range	—	—	700	—	—	700			
High Level Output Current	Isink	25℃	—	0.1	—	—	0.1	—	nA	Vcc^+=30[V],VID=1[V] VO=30[V]	3
		full range	—	—	1	—	—	1	μA		
Output Sink Current	IOL	25℃	6	16	—	6	16	—	mA	VID=-1[V],VO=1.5[V]	3
Small Single Response Time	tRE	25℃	—	1.3	—	—	1.3	—	μs	RL=5.1[kΩ], Vcc^+=5[V] VIN=100[mVp-p], Overdrive=5[mV]	3
Large Single Response Time	tREL	25℃	—	—	1.0	—	—	1.0	μs	TTL input Vref=1.4[V] RL=5.1[kΩ] Output voltage at 95%	3

(*2) Absolute value

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●Reference Data LM393 family

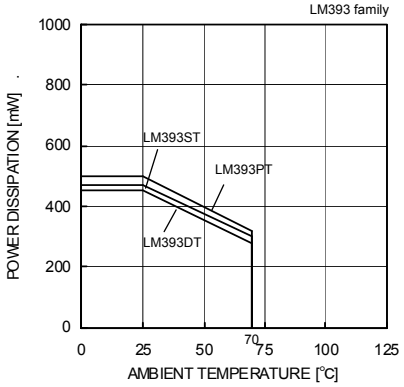


Fig.1
Derating Curve

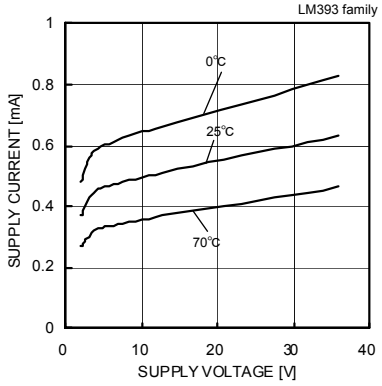


Fig.2
Supply Current – Supply Voltage

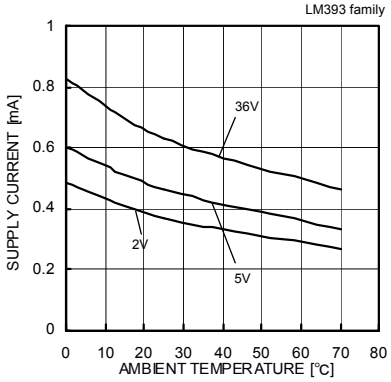


Fig. 3
Supply Current – Ambient Temperature

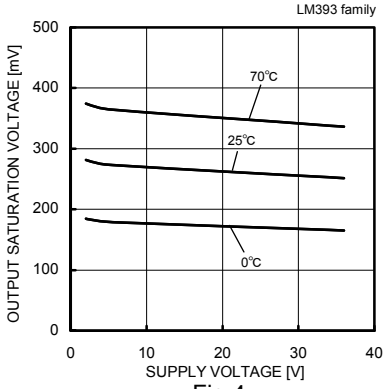


Fig.4
Output Saturation Voltage
– Supply Voltage
(IOL=4[mA])

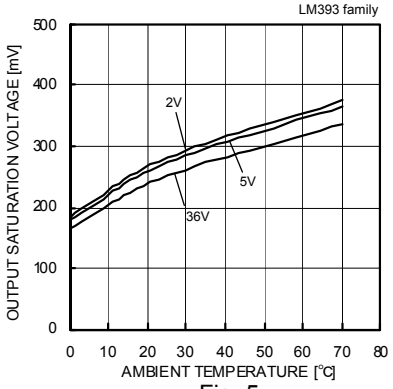


Fig. 5
Output Saturation Voltage
– Ambient Temperature
(IOL=4[mA])

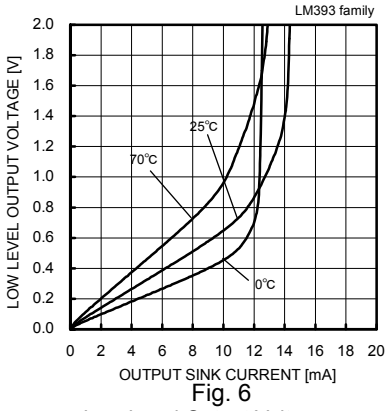


Fig. 6
Low Level Output Voltage
– Output Sink Current
(VCC=5[V])

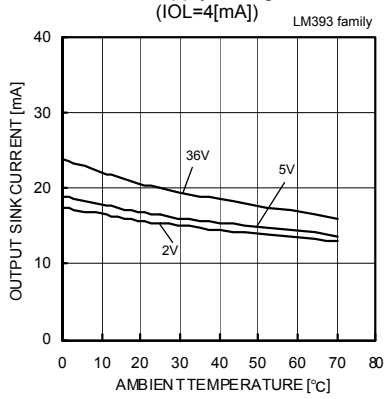


Fig. 7
Output Sink Current – Ambient
Temperature
(VOUT=1.5[V])

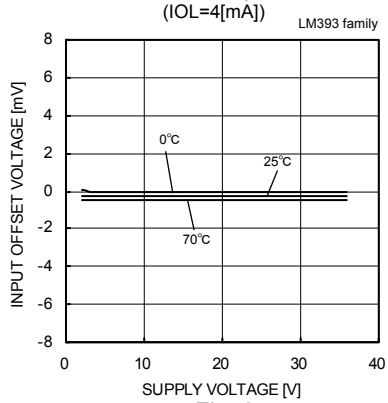


Fig. 8
Input Offset Voltage – Supply Voltage

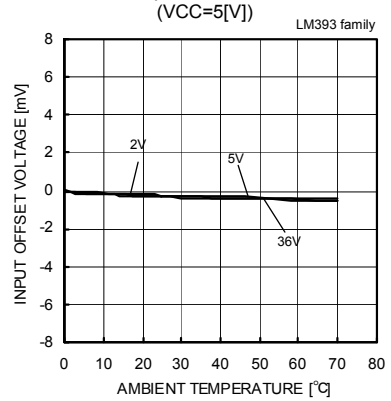


Fig. 9
Input Offset Voltage – Ambient Temperature

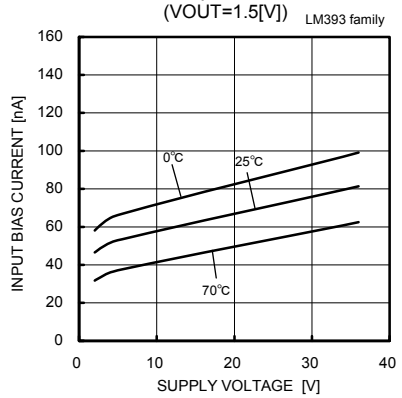


Fig. 10
Input Bias Current – Supply Voltage

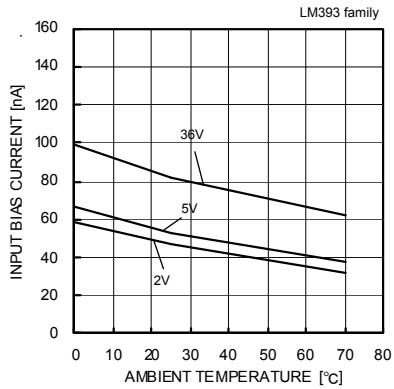


Fig. 11
Input Bias Current – Ambient Temperature

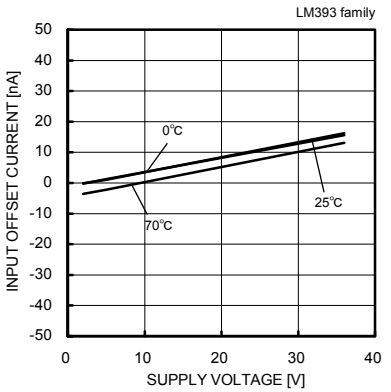


Fig. 12
Input Offset Current – Supply Voltage

(*)The data above is ability value of sample, it is not guaranteed. LM393family: 0[℃]~+70[℃]

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●Reference Data LM393 family

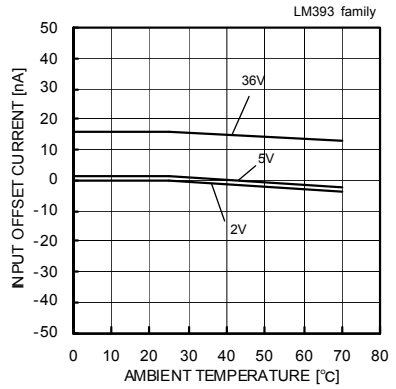


Fig. 13
Input Offset Current – Ambient Temperature

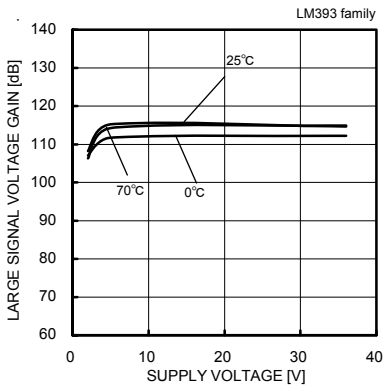


Fig. 14
Large Signal Voltage Gain
– Supply Voltage

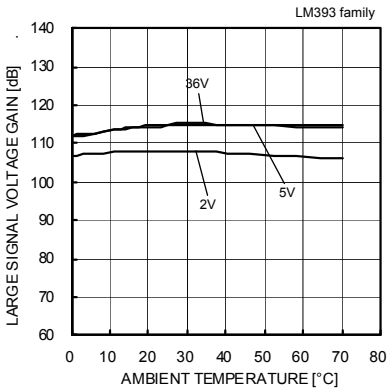


Fig. 15
Large Signal Voltage Gain
– Ambient Temperature

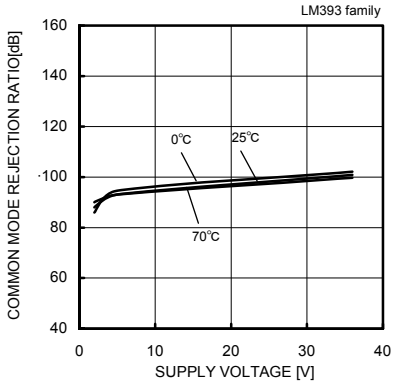


Fig. 16
Common Mode Rejection Ratio
– Supply Voltage

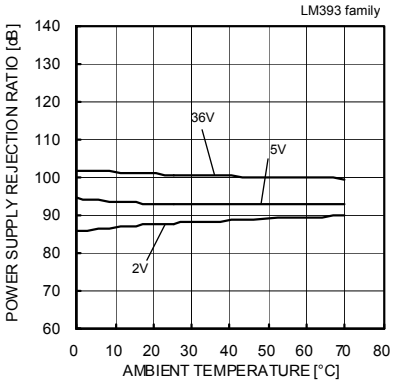


Fig. 17
Common Mode Rejection Ratio
– Ambient Temperature

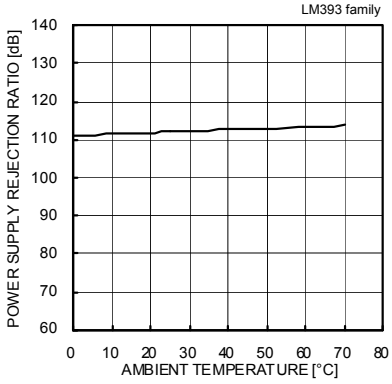


Fig. 18
Power Supply Rejection Ratio
– Ambient Temperature

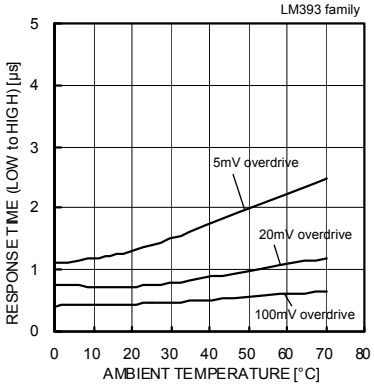


Fig. 19
Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

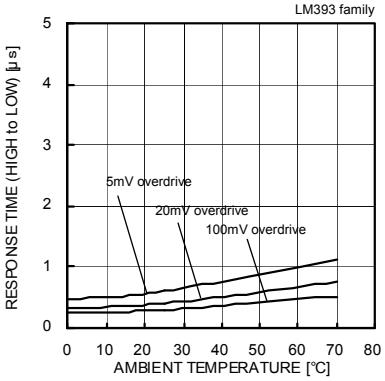


Fig. 20
Response Time (High to Low)
–Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. LM393family: 0[℃]~+70[℃]

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●Reference Data LM339 family

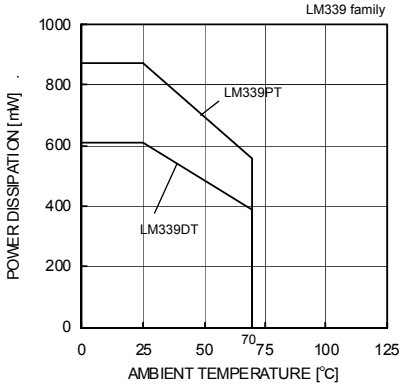


Fig.21
Derating Curve

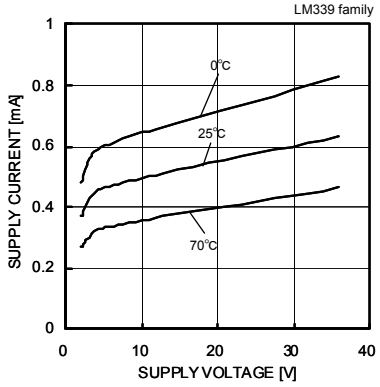


Fig.22
Supply Current – Supply Voltage

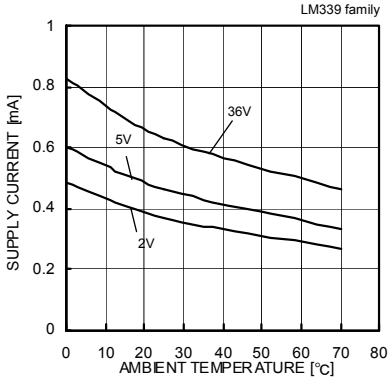


Fig. 23
Supply Current – Ambient Temperature

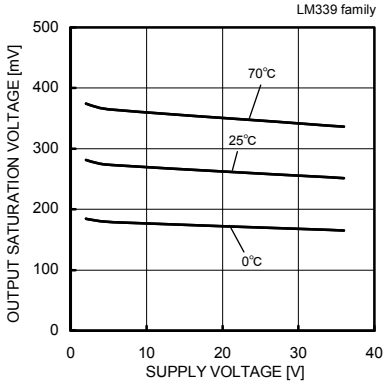


Fig.24
Output Saturation Voltage
– Supply Voltage
(IOL=4[mA])

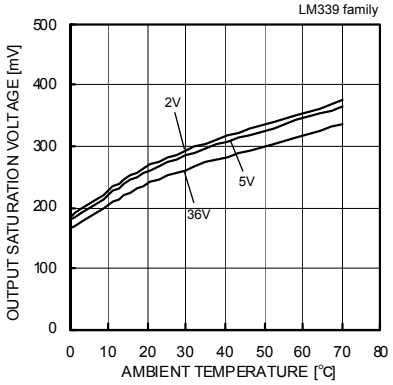


Fig. 25
Output Saturation Voltage
– Ambient Temperature
(IOL=4[mA])

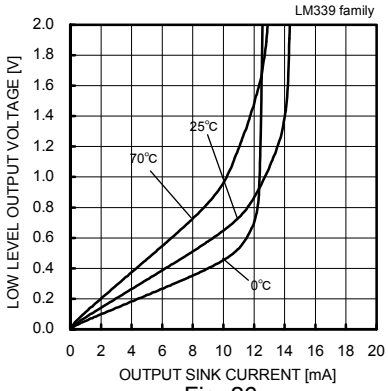


Fig. 26
Low Level Output Voltage
– Output Sink Current
(VCC=5[V])

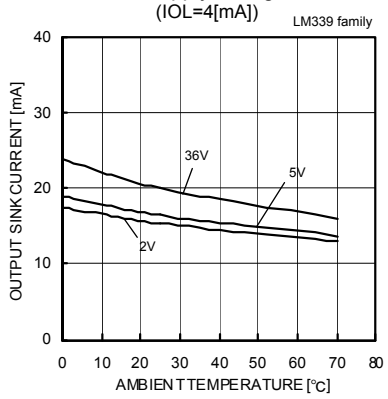


Fig. 27
Output Sink Current – Ambient Temperature
(VOUT=1.5[V])

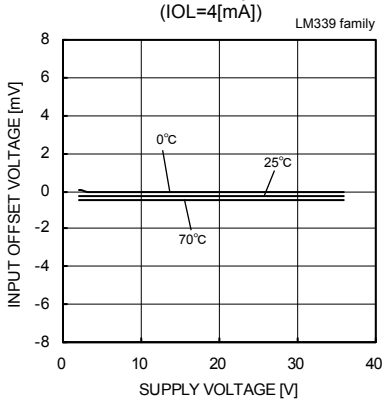


Fig. 28
Input Offset Voltage – Supply Voltage

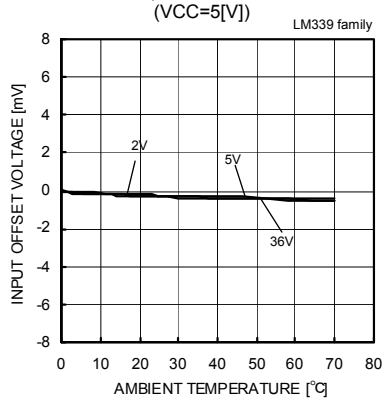


Fig. 29
Input Offset Voltage – Ambient Temperature

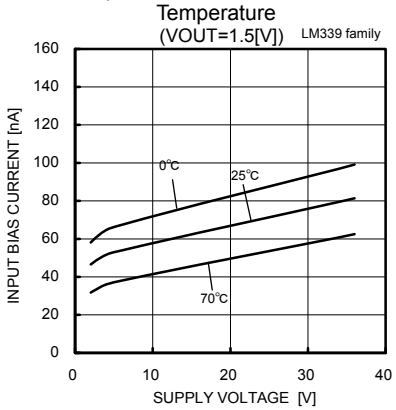


Fig. 30
Input Bias Current – Supply Voltage

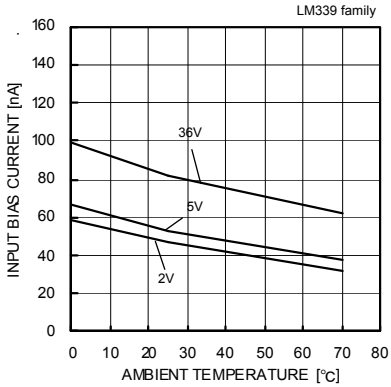


Fig. 31
Input Bias Current – Ambient Temperature

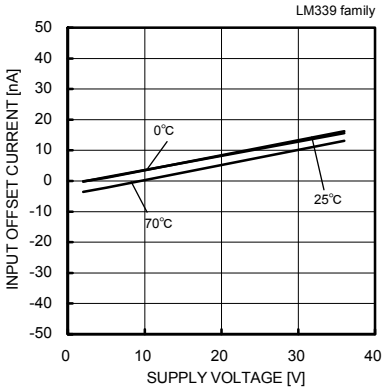


Fig. 32
Input Offset Current – Supply Voltage

(*)The data above is ability value of sample, it is not guaranteed. LM339family: 0[℃]~+70[℃]

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●Reference Data LM339 family

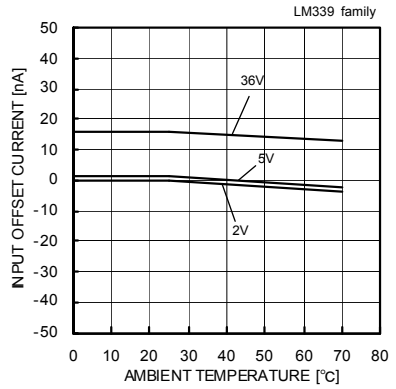


Fig. 33
Input Offset Current – Ambient Temperature

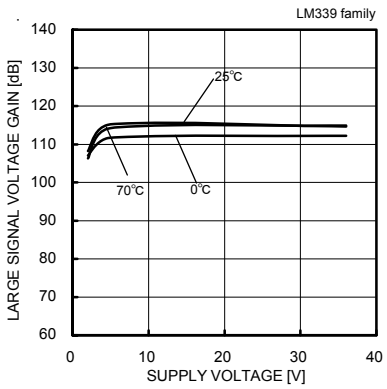


Fig. 34
Large Signal Voltage Gain
– Supply Voltage

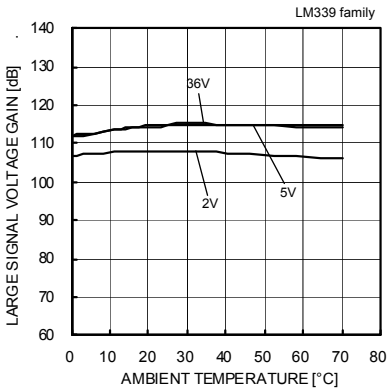


Fig. 35
Large Signal Voltage Gain
– Ambient Temperature

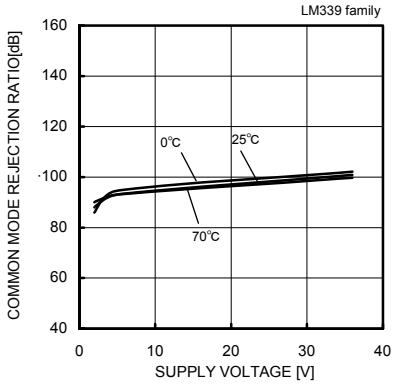


Fig. 36
Common Mode Rejection Ratio
– Supply Voltage

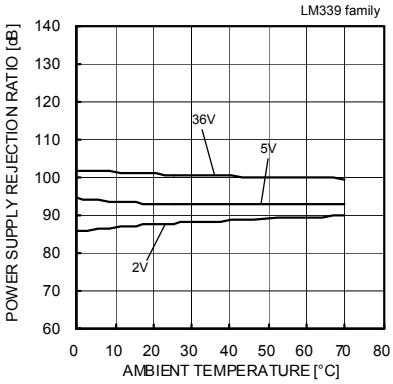


Fig. 37
Common Mode Rejection Ratio
– Ambient Temperature

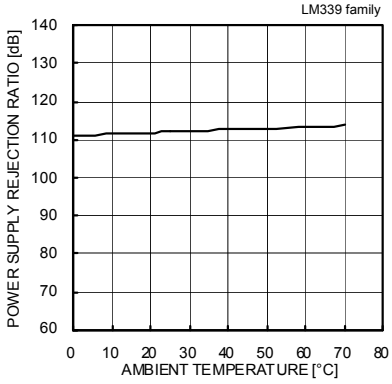


Fig. 38
Power Supply Rejection Ratio
– Ambient Temperature

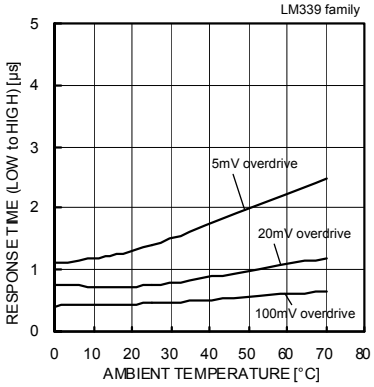


Fig. 39
Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

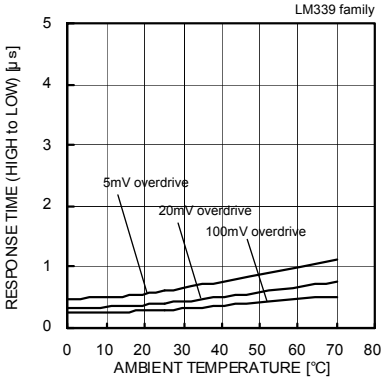


Fig. 40
Response Time (High to Low)
–Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. LM339family: 0[℃]~+70[℃]

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●Reference Data LM2903 family

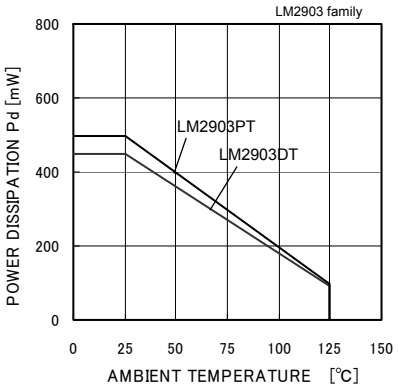


Fig. 41
Derating Curve

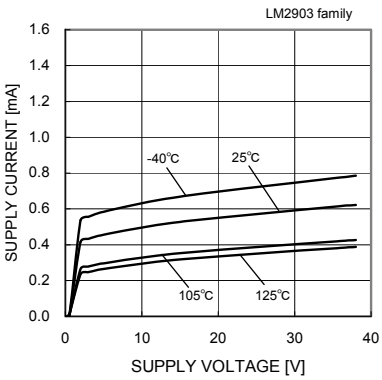


Fig. 42
Supply Current – Supply Voltage

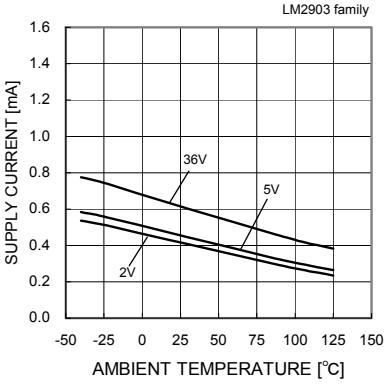


Fig. 43
Supply Current – Ambient Temperature

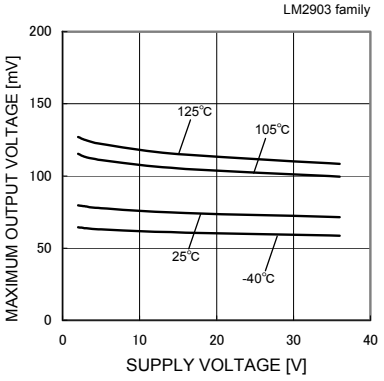


Fig. 44
Output Saturation Voltage
– Supply Voltage
(IOL=4[mA])

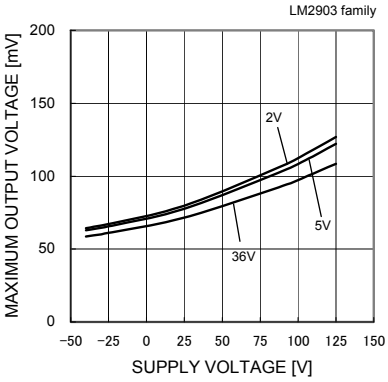


Fig. 45
Output Saturation Voltage
– Ambient Temperature
(IOL=4[mA])

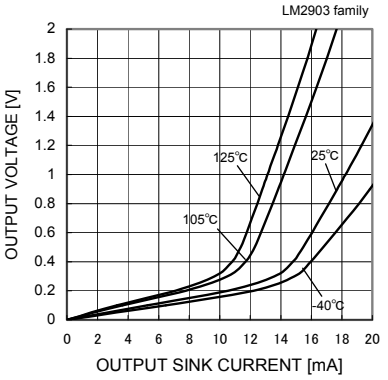


Fig. 46
Low Level Output Voltage
– Output Sink Current
(VCC=5[V])

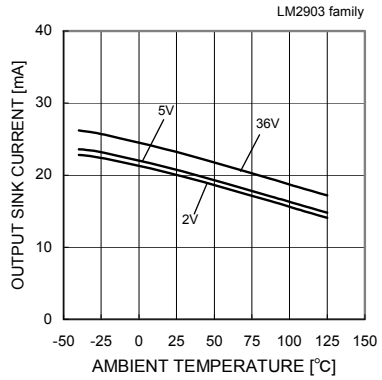


Fig. 47
Output Sink Current – Ambient Temperature
(VOUT=1.5[V])

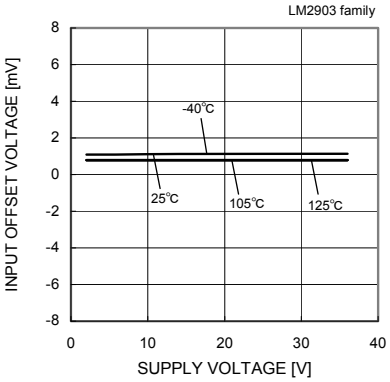


Fig. 48
Input Offset Voltage – Supply Voltage

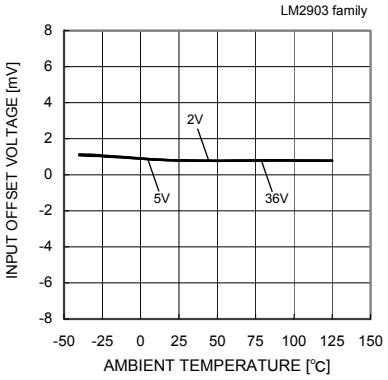


Fig. 49
Input Offset Voltage – Ambient Temperature

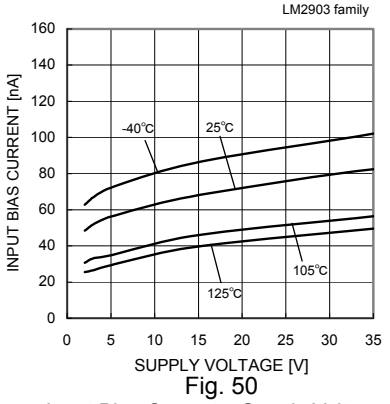


Fig. 50
Input Bias Current – Supply Voltage

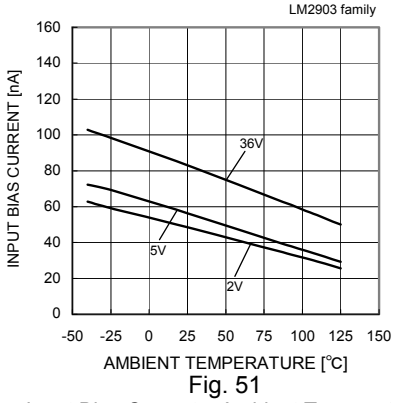


Fig. 51
Input Bias Current – Ambient Temperature

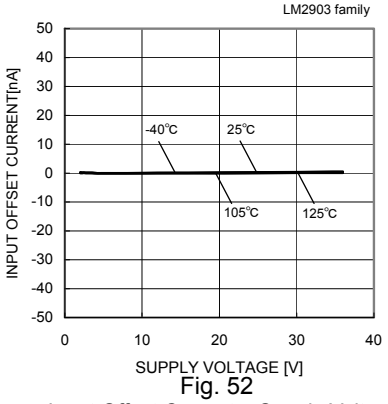


Fig. 52
Input Offset Current – Supply Voltage

(*)The data above is ability value of sample, it is not guaranteed. LM2903family:-40[℃]～+125[℃]

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●Reference Data LM2903 family

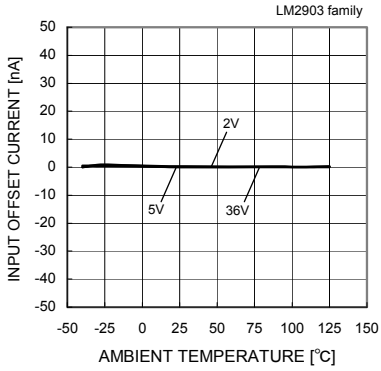


Fig. 53

Input Offset Current – Ambient Temperature

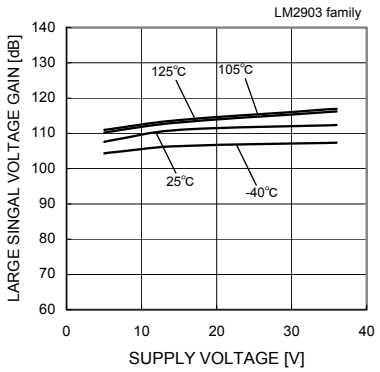


Fig. 54

Large Signal Voltage Gain
– Supply Voltage

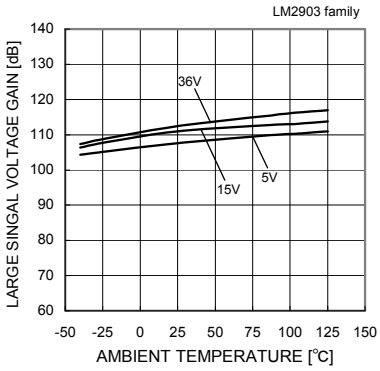


Fig. 55

Large Signal Voltage Gain
– Ambient Temperature

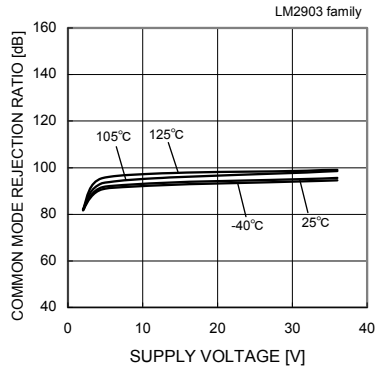


Fig. 56

Common Mode Rejection Ratio
– Supply Voltage

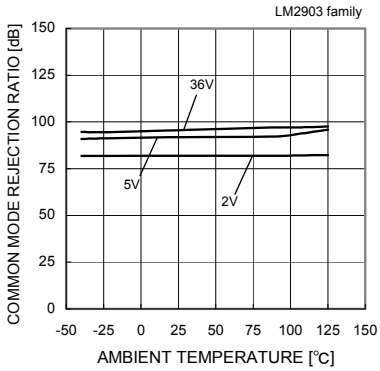


Fig. 57

Common Mode Rejection Ratio
– Ambient Temperature

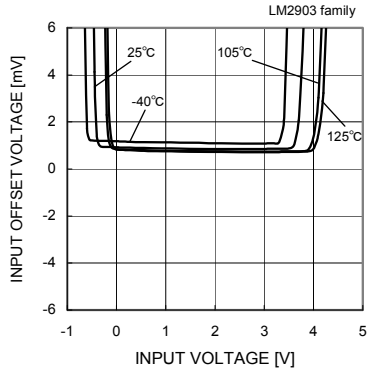


Fig. 58

Input Offset Voltage – Input Voltage
(VCC=5V)

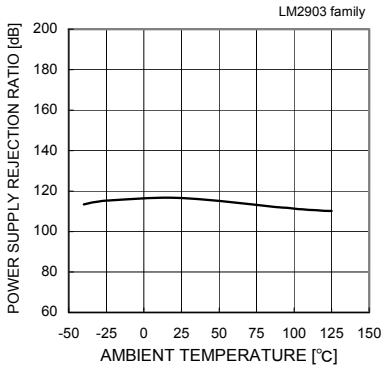


Fig. 59

Power Supply Rejection Ratio
– Ambient Temperature

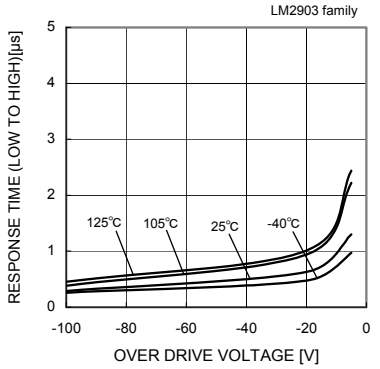


Fig. 60

Response Time (Low to High)
– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

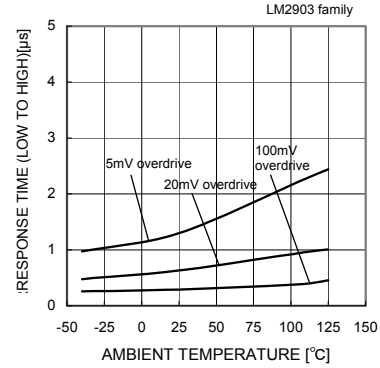


Fig. 61

Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

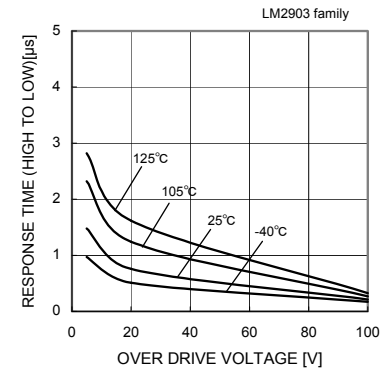


Fig. 62

Response Time (High to Low)
– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

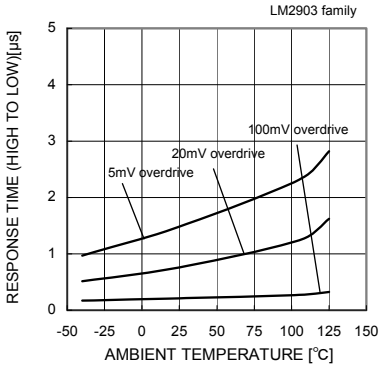


Fig. 63

Response Time (High to Low)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. LM2903family:-40[℃]～+125[℃]

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●Reference Data LM2901 family

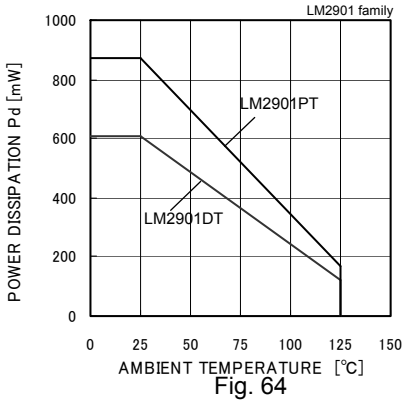


Fig. 64
Derating Curve

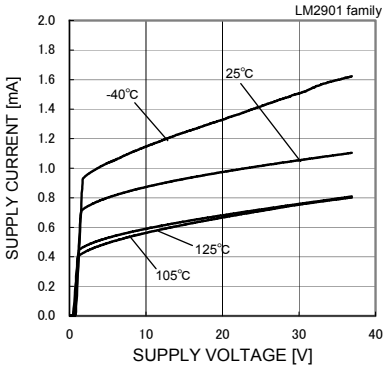


Fig. 65
Supply Current – Supply Voltage

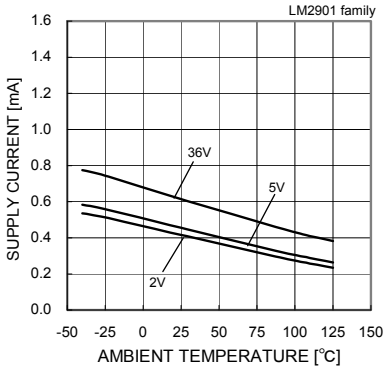


Fig. 66
Supply Current – Ambient Temperature

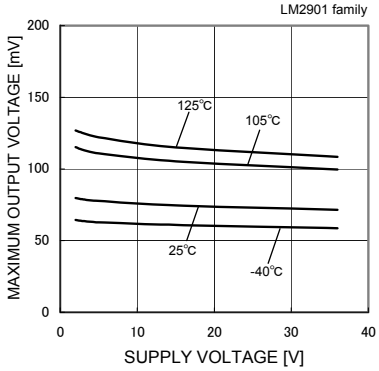


Fig. 67
Output Saturation Voltage
– Supply Voltage
(IOL=4[mA])

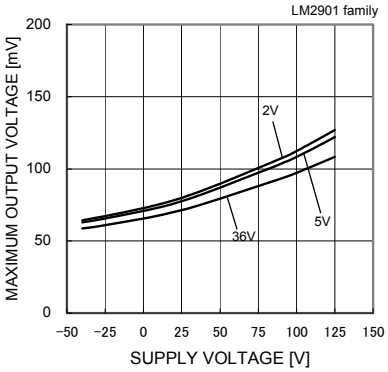


Fig. 68
Output Saturation Voltage
– Ambient Temperature
(IOL=4[mA])

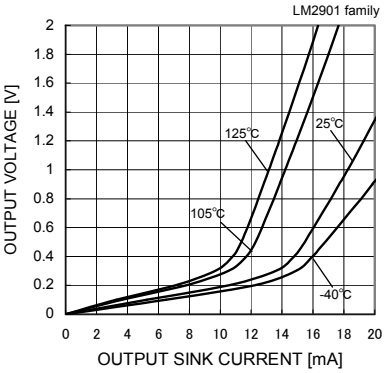


Fig. 69
Low Level Output Voltage
– Output Sink Current
(VCC=5[V])

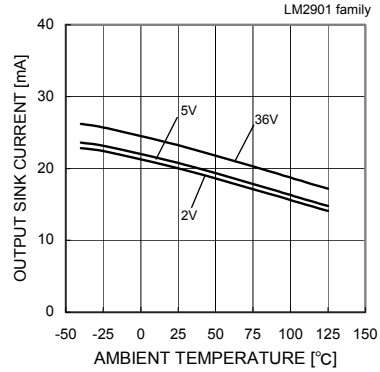


Fig. 70
Output Sink Current – Ambient Temperature
(VOUT=1.5[V])

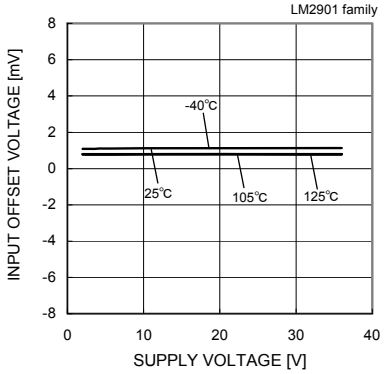


Fig. 71
Input Offset Voltage – Supply Voltage

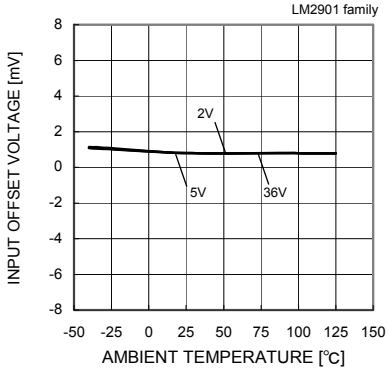


Fig. 72
Input Offset Voltage – Ambient Temperature

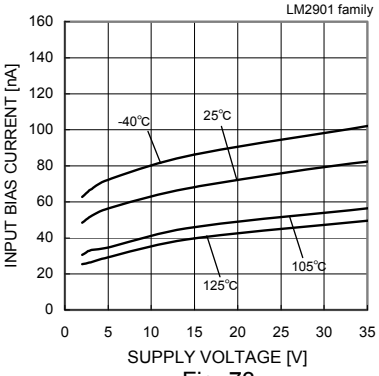


Fig. 73
Input Bias Current – Supply Voltage

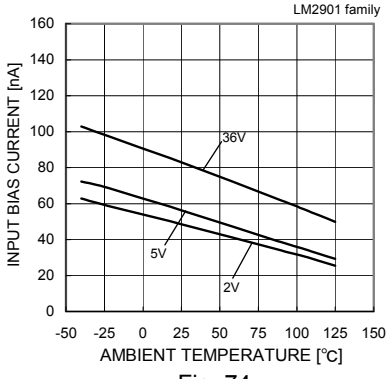


Fig. 74
Input Bias Current – Ambient Temperature

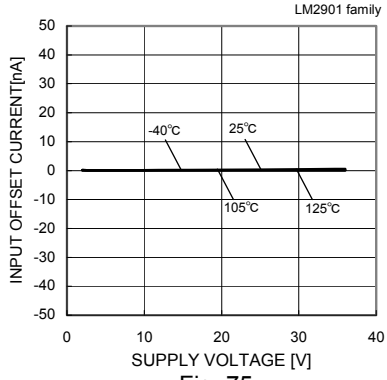


Fig. 75
Input Offset Current – Supply Voltage

(*)The data above is ability value of sample, it is not guaranteed. LM2901family:-40[℃]～+125[℃]

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●Reference Data LM2901 family

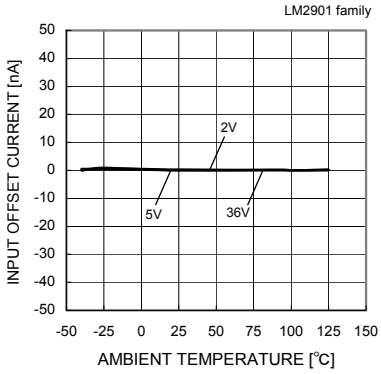


Fig. 76
Input Offset Current – Ambient Temperature

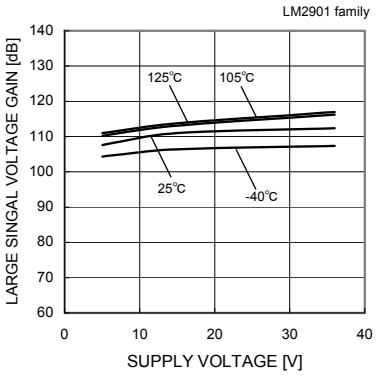


Fig. 77
Large Signal Voltage Gain
– Supply Voltage

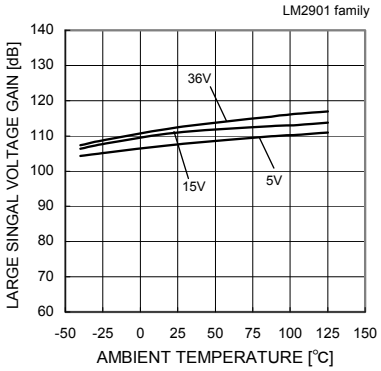


Fig. 78
Large Signal Voltage Gain
– Ambient Temperature

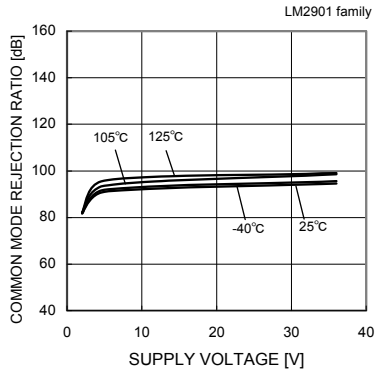


Fig. 79
Common Mode Rejection Ratio
– Supply Voltage

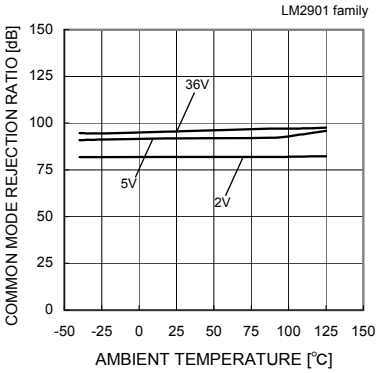


Fig. 80
Common Mode Rejection Ratio
– Ambient Temperature

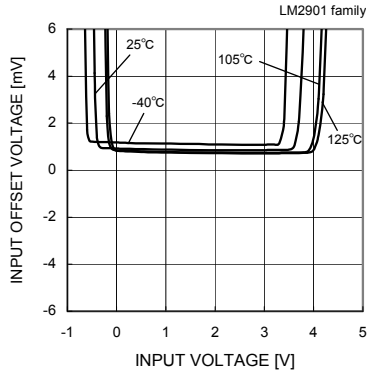


Fig. 81
Input Offset Voltage – Input Voltage
(VCC=5V)

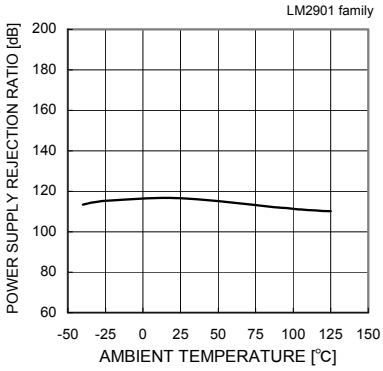


Fig. 82
Power Supply Rejection Ratio
– Ambient Temperature

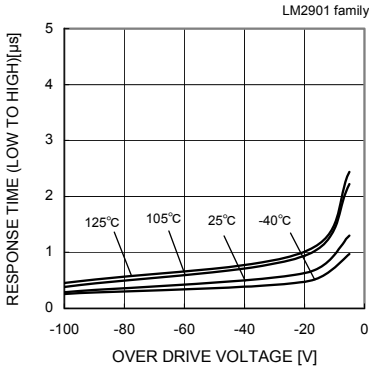


Fig. 83
Response Time (Low to High)
– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

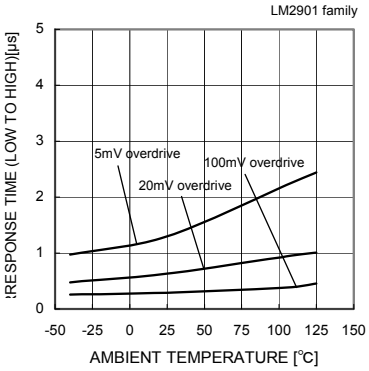


Fig. 84
Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

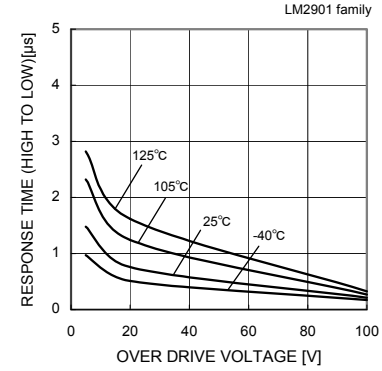


Fig. 85
Response Time (High to Low)
– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

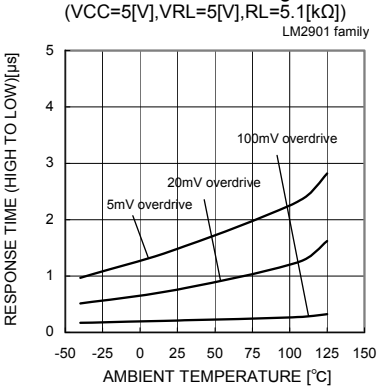


Fig. 86
Response Time (High to Low)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. BA2901: LM2901family:-40[℃]～+125[℃]

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●Circuit Diagram

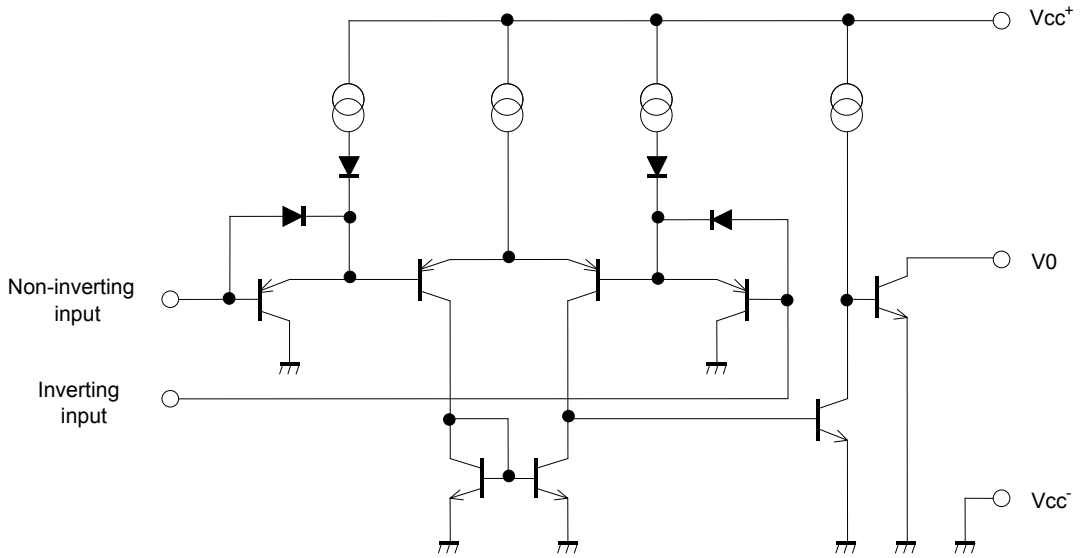


Fig.87 Circuit Diagram (each Comparator)

●Measurement Circuit 1 NULL Method Measurement Condition

Vcc+,Vcc-,EK,Vicm unit : [V]

Parameter	VF	S1	S2	S3	LM393/LM339 family				LM2903/LM2901 family				Calculation
					Vcc+	Vcc-	EK	Vicm	Vcc+	Vcc-	EK	Vicm	
Input Offset Voltage	VF1	ON	ON	ON	5 to 30	0	-1.4	0	5 to 30	0	-1.4	0	1
Input Offset Current	VF2	OFF	OFF	ON	5	0	-1.4	0	5	0	-1.4	0	2
Input Bias Current	VF3	OFF	ON	ON	5	0	-1.4	0	5	0	-1.4	0	3
	VF4	ON	OFF		5	0	-1.4	0	5	0	-1.4	0	
Large Signal Voltage Gain	VF5	ON	ON	ON	15	0	-1.4	0	15	0	-1.4	0	4
	VF6				15	0	-11.4	0	15	0	-11.4	0	

－Calculation－

1.Input offset voltage (Vio)

$$Vio = \frac{|VF1|}{1+ Rf/Rs} \quad [V]$$

2. Input offset current (Iio)

$$Iio = \frac{|VF2 - VF1|}{Ri(1+ Rf / Rs)} \quad [A]$$

3. Input bias current (Ib)

$$Ib = \frac{|VF4 - VF3|}{2\times Ri(1+ Rf / Rs)} \quad [A]$$

4.Large signal voltage gain (Av)

$$AV = 20\times \mathrm{Log} \frac{10\times (1+ Rf/Rs)}{|VF6 - VF5|} \quad [dB]$$

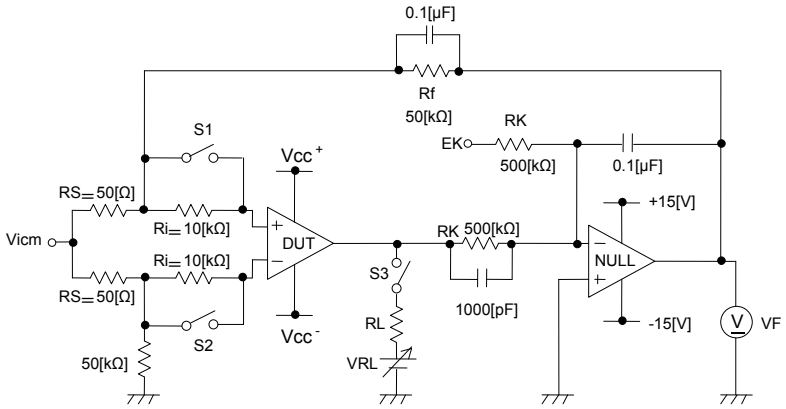


Fig.88 Measurement Circuit 1 (each Comparator)

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●Measurement Circuit 2: Switch Condition

SW No.		SW 1	SW 2	SW 3	SW 4	SW 5	SW 6	SW 7
Supply Current	—	OFF	OFF	OFF	OFF	OFF	OFF	OFF
Output Sink Current	VOL=1.5[V]	OFF	ON	ON	OFF	ON	ON	OFF
Saturation Voltage	IOL=4[mA]	OFF	ON	ON	OFF	OFF	OFF	ON
Output Leakage Current	VOH=36[V]	OFF	ON	ON	OFF	OFF	OFF	ON
Response Time	RL=5.1[kΩ]	ON	OFF	ON	ON	OFF	ON	OFF
	VRL=5[V]							

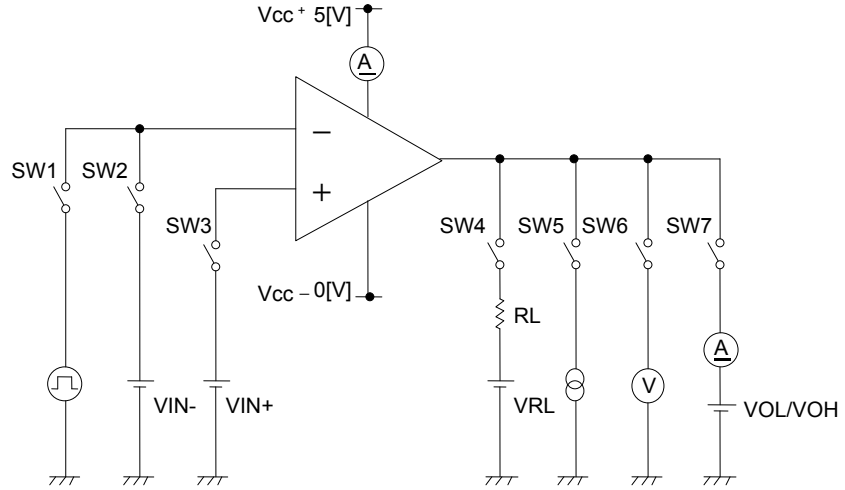


Fig.89 Measurement Circuit 2 (each Comparator)

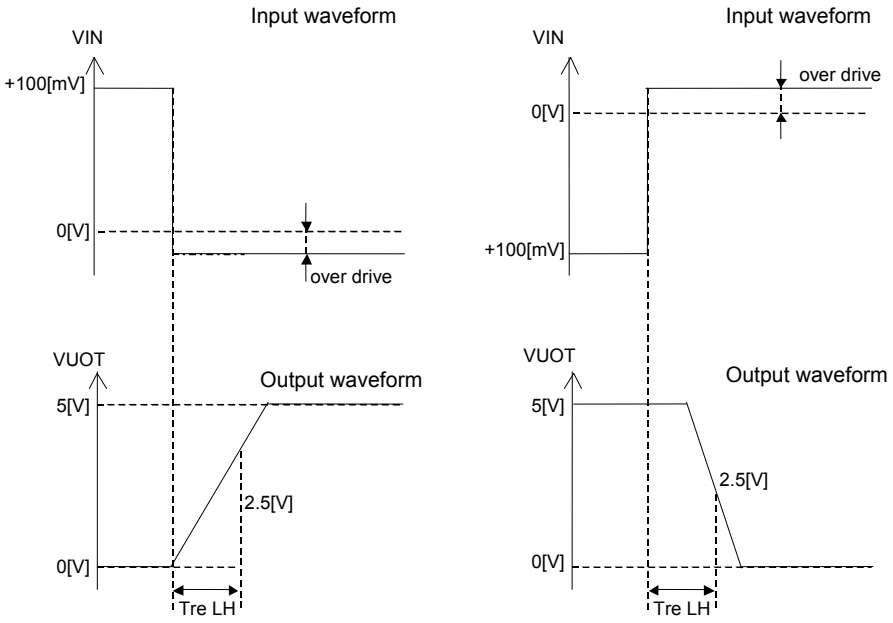


Fig.90 Response Time

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●Description of Electrical Characteristics

Described below are descriptions of the relevant electrical terms.
Please note that item names, symbols, and their meanings may differ from those on another manufacturer's documents.

1. Absolute maximum ratings

The absolute maximum ratings are values that should never be exceeded, since doing so may result in deterioration of electrical characteristics or damage to the part itself as well as peripheral components.

1.1 Power supply voltage (Vcc^+/Vcc^-)

Expresses the maximum voltage that can be supplied between the positive and negative power supply terminals without causing deterioration of the electrical characteristics or destruction of the internal circuitry.

1.2 Differential input voltage (VID)

Indicates the maximum voltage that can be supplied between the non-inverting and inverting terminals without damaging the IC.

1.3 Input common-mode voltage range (VICM)

Signifies the maximum voltage that can be supplied to non-inverting and inverting terminals without causing deterioration of the electrical characteristics or damage to the IC itself. Normal operation is not guaranteed within the input common-mode voltage range of the maximum ratings – use within the input common-mode voltage range of the electric characteristics instead.

1.4 Operating temperature range and storage temperature range (Topr,Tstg)

The operating temperature range indicates the temperature range within which the IC can operate. The higher the ambient temperature, the lower the power consumption of the IC. The storage temperature range denotes the range of temperatures the IC can be stored under without causing excessive deterioration of the electrical characteristics.

1.5 Power dissipation (Pd)

Indicates the power that can be consumed by a particular mounted board at ambient temperature (25℃). For packaged products, Pd is determined by the maximum junction temperature and the thermal resistance.

2. Electric characteristics

2.1 Input offset voltage (VIO)

Signifies the voltage difference between the non-inverting and inverting terminals. It can be thought of as the input voltage difference required for setting the output voltage to 0V.

2.2 Input offset current (IIO)

Indicates the difference of the input bias current between the non-inverting and inverting terminals.

2.3 Input bias current (IIB)

Denotes the current that flows into or out of the input terminal, it is defined by the average of the input bias current at the non-inverting terminal and the input bias current at the inverting terminal.

2.4 Input common-mode voltage range(VICM)

Indicates the input voltage range under which the IC operates normally.

2.5 Large signal differential voltage gain (AVD)

The amplifying rate (gain) of the output voltage against the voltage difference between the non-inverting and inverting terminals, it is (normally) the amplifying rate (gain) with respect to DC voltage.
AVD = (output voltage fluctuation) / (input offset fluctuation)

2.6 Supply current (ICC)

Indicates the current of the IC itself that flows under specific conditions and during no-load steady state.

2.7 Low level output current (IOL)

Denotes the maximum current that can be output under specific output conditions.

2.8 Low level output voltage (VOL)

Signifies the voltage range that can be output under specific output conditions.

2.9 High level output current (IOH)

Indicates the current that flows into the IC under specific input and output conditions.

2.10 Response time (Tre)

The interval between the application of input and output conditions.

2.11 Common-mode rejection ratio (CMRR)

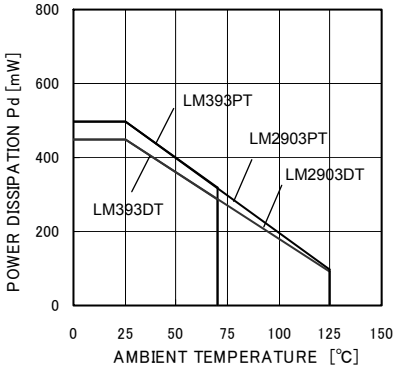
Denotes the ratio of fluctuation of the input offset voltage when the in-phase input voltage is changed (DC fluctuation).
CMRR = (change in input common-mode voltage) / (input offset fluctuation)

2.12 Power supply rejection ratio (PSRR)

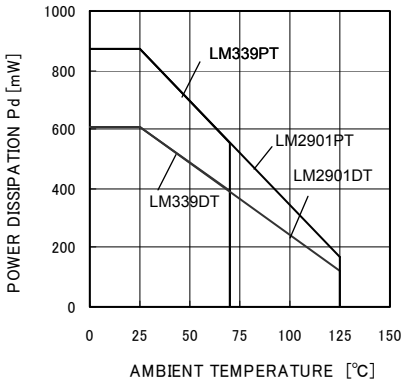
Signifies the ratio of fluctuation of the input offset voltage when the supply voltage is changed (DC fluctuation).
PSRR = (change in power supply voltage) / (input offset fluctuation)

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●Derating Curve



LM393DT/PT/WDT/WPT
LM2903DT/PT



LM339DT/PT
LM2901DT/P

Power Dissipation

Package	Pd[W]	θja [℃/W]
SO package8 (*8)	450	3.6
TSSOP8 (*6)	500	4.0

θja = (Tj-Ta)/Pd[℃/W]

Power Dissipation

Package	Pd[W]	θja [℃/W]
SO package14	610	4.9
TSSOP14	870	7.0

θja = (Tj-Ta)/Pd[℃/W]

Fig.91 Derating Curve

●Precautions

1) Unused circuits
When there are unused circuits it is recommended that they be connected as in Fig.92, setting the non-inverting input terminal to a potential within the in-phase input voltage range (VICM).

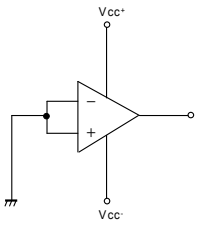


Fig.92 Disable circuit example

2) Input terminal voltage
Applying Vcc^- + 36[V] to the input terminal is possible without causing deterioration of the electrical characteristics or destruction, irrespective of the supply voltage. However, this does not ensure normal circuit operation.
Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

3) Power supply (single / dual)
The op-amp operates when the specified voltage supplied is between Vcc^+ and Vcc^-. Therefore, the single supply op-amp can be used as a dual supply op-amp as well.

4) Power dissipation Pd
Using the unit in excess of the rated power dissipation may cause deterioration in the electrical characteristics due to a rise in chip temperature, including reduced current capability. Therefore, please take into consideration the power dissipation (Pd) under actual operating conditions and apply a sufficient margin in thermal design. Refer to the thermal derating curves for more information.

5) Short-circuit between pins and erroneous mounting
Incorrect mounting may damage the IC. In addition, the presence of foreign particles between the outputs, the output and the power supply, or the output and Vcc^- may result in IC destruction.

6) Terminal short-circuits
When output and Vcc^+ terminals are shorted, excessive output current may flow, resulting in undue heat generation and, subsequently, destruction.

7) Operation in a strong electromagnetic field
Operation in a strong electromagnetic field may cause malfunctions.

8) Radiation
This IC is not designed to withstand radiation.

9) IC handing
Applying mechanical stress to the IC by deflecting or bending the board may cause fluctuations in the electrical characteristics due to piezoelectric (piezo) effects.

10) Board inspection
Connecting a capacitor to a pin with low impedance may stress the IC. Therefore, discharging the capacitor after every process is recommended. In addition, when attaching and detaching the jig during the inspection phase, ensure that the power is turned OFF before inspection and removal. Furthermore, please take measures against ESD in the assembly process as well as during transportation and storage.

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●Ordering part number

L	M	3	3	9	W	D	T

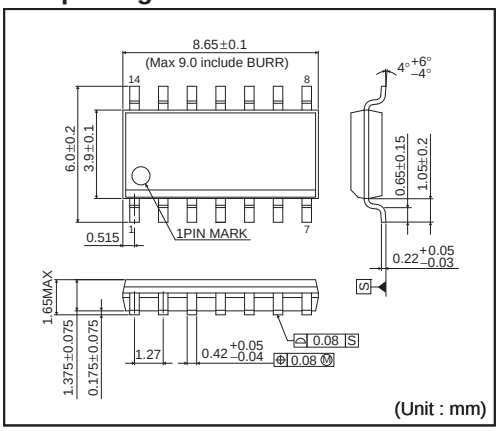
Family name
LM393
LM339
LM2901
LM2903

ESD Tolerance applicable
W : 2kV
None : Normal

Package type
D : S.O package
P : TSSOP
S : Mini SO

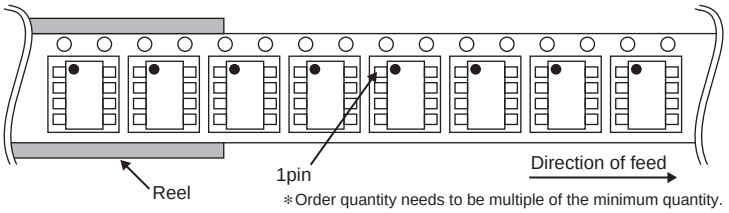
Packaging and forming specification
T: Embossed tape and reel

S.O package14



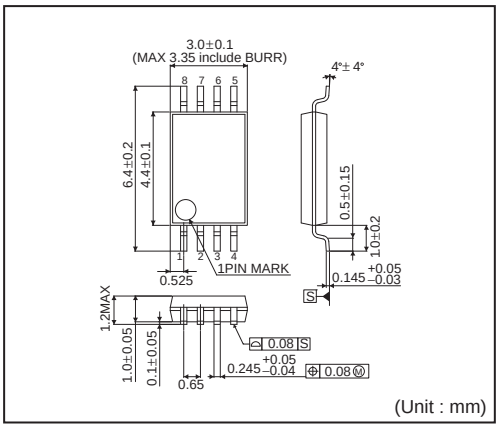
<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	(The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



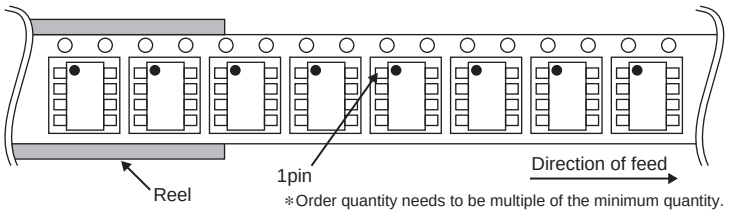
∗Order quantity needs to be multiple of the minimum quantity.

TSSOP8



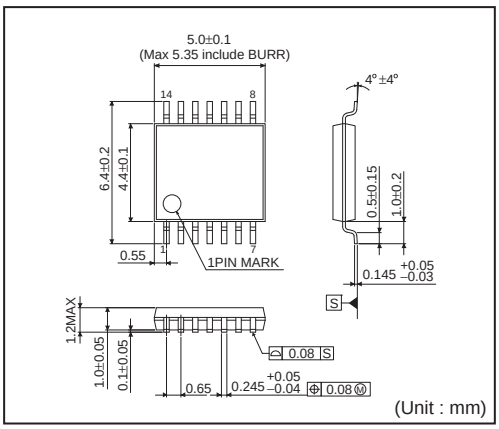
<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	(The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



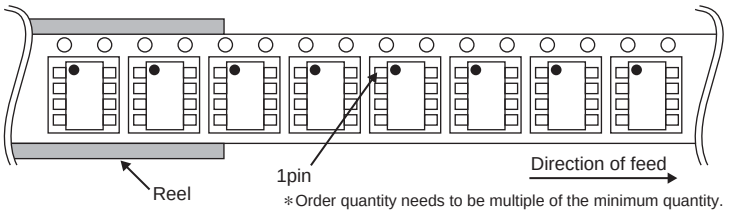
∗Order quantity needs to be multiple of the minimum quantity.

TSSOP14



<Tape and Reel information>

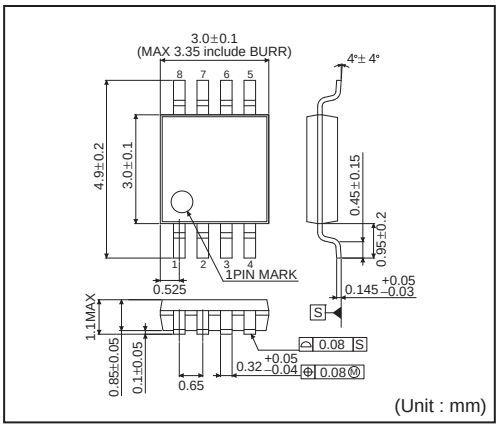
Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	(The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



∗Order quantity needs to be multiple of the minimum quantity.

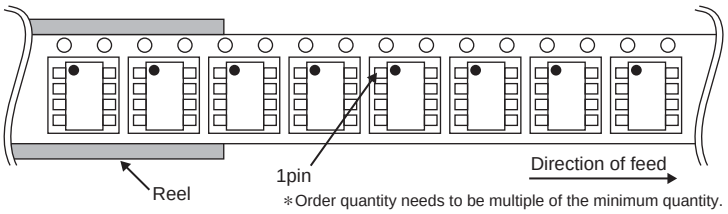
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Mini SO8



<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	(The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



＊Order quantity needs to be multiple of the minimum quantity.

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