



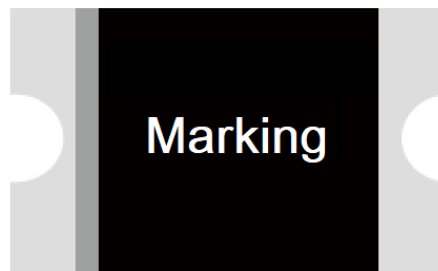
Product Features: Small surface mountable, Solid state, Faster time to trip than standard SMD devices, Lower resistance than standard SMD devices

Operation Current: 1.40A~6.00A

Maximum Voltage: 6V_{DC}

Temperature Range : -40°C to 85°C

Applications: All high-density boards



Electrical Characteristics (23°C)

Part Number	Hold Current	Trip Current	Rated Voltage	Max Current	Typical Power	Max Time to Trip		Resistance	
						Current	Time	R _{MIN}	R _{1MAX}
	I _H , A	I _T , A	V _{MAX} , V _{DC}	I _{MAX} , A	P _d , W	A	Sec	Ohms	Ohms
F1812L140SL-06	1.40	3.60	6	100	1.0	8.0	3.00	0.0100	0.0350
F1812L190SL-06	1.90	4.90	6	100	1.0	8.0	5.00	0.0030	0.0250
F1812L270SL-06	2.70	6.20	6	100	1.0	13.5	3.00	0.0030	0.0230
F1812L300SL-06	3.00	7.00	6	100	1.0	15.0	2.00	0.0030	0.0220
F1812L370SL-06	3.70	9.10	6	100	1.0	18.5	2.00	0.0030	0.0180
F1812L500SL-06	5.00	10.00	6	100	1.0	25.0	2.00	0.0015	0.0140
F1812L600SL-06	6.00	12.00	6	100	1.0	30.0	3.00	0.0010	0.0100

Thermal Derating for PPTC Device at Various Ambient Temperatures

Temperatures	-40°C	-20°C	0°C	23°C	30°C	40°C	50°C	60°C	70°C	85°C
Current Derating %	145%	130%	115%	100%	92%	84%	77%	69%	61%	50%



Typical Time-To-Trip at 23°C

A = F1812L140SL-06

B = F1812L190SL-06

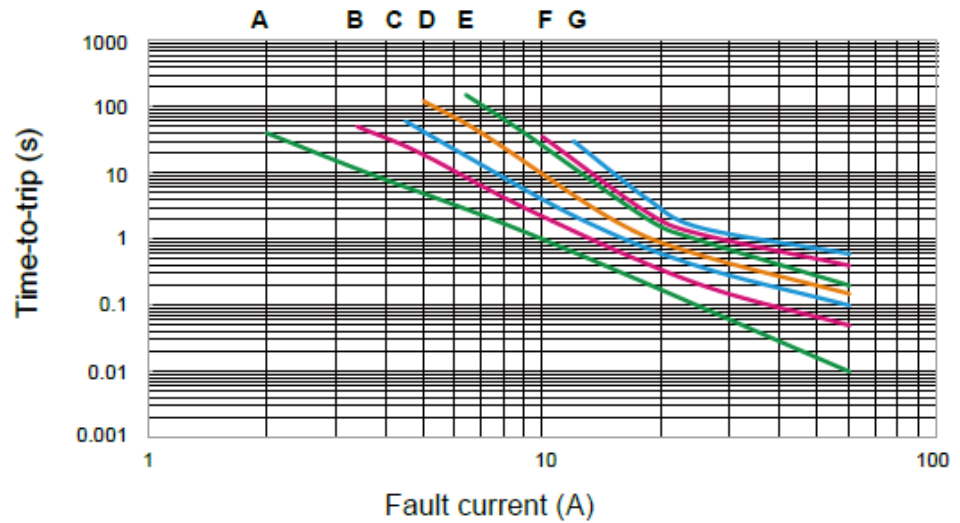
C = F1812L270SL-06

D = F1812L300SL-06

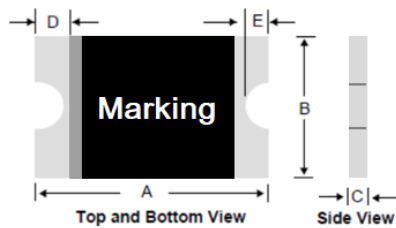
E = F1812L370SL-06

F = F1812L500SL-06

G = F1812L600SL-06



Product Dimensions (Millimeters)

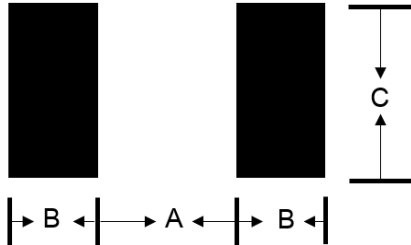


Part Number	A		B		C		D		E	
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max
F1812L140SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L190SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L270SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L300SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L370SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L500SL-06	4.37	4.73	3.07	3.41	0.30	0.70	0.25	0.95	0.25	0.65
F1812L600SL-06	4.37	4.73	3.07	3.41	0.30	1.00	0.25	0.95	0.25	0.65



Pad Layouts, Solder Reflow and Rework Recommendations

The dimension in the table below provide the recommended pad layout for each F1812L device



Pad dimensions (millimeters)			
Device	A Nominal	B Nominal	C Nominal
F1812L	3.45	1.78	3.50

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (T _{smax} to T _p)	3°C/second max.
Preheat:	
Temperature Min (T _{smin})	150°C
Temperature Max T _{smax})	200°C
Time (t _s T _{smin} to T _{smax})	60~180 seconds
Time maintained above:	
Temperature(T _L)	217°C
Time (t _L)	60~150 seconds
Peak/Classification Temperature(T _p):	260°C
Time within 5°C of actual Peak:	
Temperature (t _p)	20~40 seconds
Ramp-Down Rate:	6°C/second max.
Time 25°C to Peak Temperature:	8 minutes max.

Note 1: All temperatures refer to of the package,
measured on the package body surface.

Solder reflow

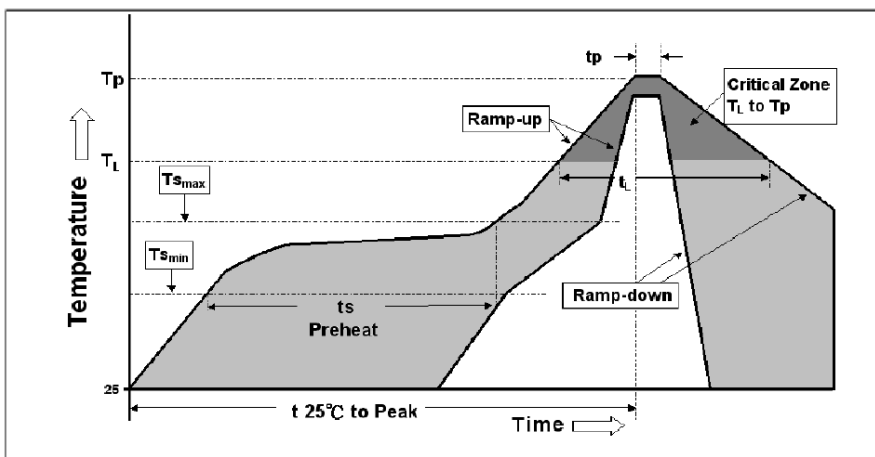
※ Due to “Lead Free” nature, Temperature and Dwelling time for the soldering zone is higher than those for Regular. This may cause damage to other components.

1. Recommended max past thickness > 0.25mm.
2. Devices can be cleaned using standard methods and aqueous solvent.
3. Rework use standard industry practices.
4. Storage Environment: < 30°C / 60%RH

Caution:

1. If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.
2. Devices are not designed to be wave soldered to the bottom side of the board.

Reflow Profile



NOTE: Specification subject to change without notice.