<u>TOSHIBA</u>

TOSHIBA Photocoupler Photorelay

TLP206GA

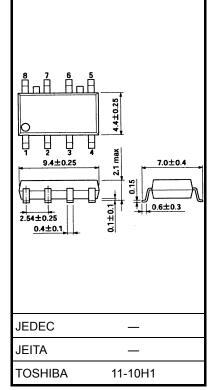
PBX Telecommunication Modem · FAX Cards, Modems In PC Measurement Instrumentation

The TOSHIBA TLP206GA consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

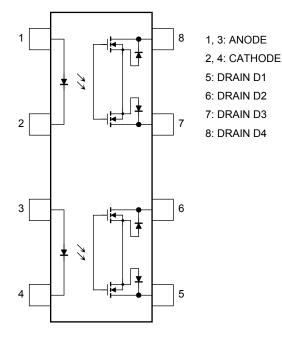
The TLP206GA is a 2-Form-A switch, which is suitable for replacement of mechanical relays in many applications.

- 8 pin SOP (2.54SOP8): 2.1 mm high, 2.54 mm pitch
- 2-form-A
- Peak off-state voltage: 400 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 120 mA (max)
- On-state resistance: 35Ω (max)
- Isolation voltage: 1500 Vrms (min)
- BSI approved: BS EN60065:2002, certificate no.8753 BS EN60950-1:2002, certificate no.8754

Pin Configuration (top view)



Weight: 0.2 g (typ.)



Unit: mm

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
LED	Forward current	lF	50	mA
	Forward current derating (Ta ≧ 25°C)	∆I _F /°C	-0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	А
	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	V _{OFF}	400	V
tor	On-state current	I _{ON}	120	mA
Detector	On-state current derating (Ta ≧ 25°C)	∆l _{ON} /°C	-1.2	mA/°C
	Junction temperature	Тj	125	°C
Operating temperature range		T _{opr}	-40 to 85	°C
Storage temperature range		T _{stg}	–55 to 125	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
	tion voltage 1 min, R.H. \leq 60%) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{DD}	_	_	320	V
Forward current	١ _F	5	7.5	25	mA
On-state current	I _{ON}	_	_	120	mA
Operating temperature	T _{opr}	-20		65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	$V_R = 5 V$	_	_	10	μA
	Capacitance	CT	V = 0, f = 1 MHz	_	30	_	pF
Detector	Off-state current	IOFF	V _{OFF} = 400 V			1	μA
	Capacitance	C _{OFF}	V = 0, f = 1 MHz		70		pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 120 mA	_	1	3	mA
Return LED current	I _{FC}	I _{OFF} = 100 μA	0.1	_	_	mA
On-state resistance	R _{ON}	I _{ON} = 120 mA, I _F = 5 mA		17	35	Ω

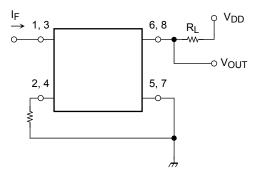
Isolation Characteristics (Ta = 25°C)

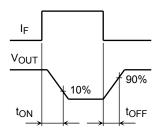
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	$V_S = 0, f = 1 MHz$	—	0.8	_	pF
Isolation resistance	R _S	$V_S = 500 \text{ V}, \text{ R.H.} \leq 60\%$	$5 imes 10^{10}$	10 ¹⁴	_	Ω
	BVS	AC, 1 min	1500	_	_	Vrms
Isolation voltage		AC, 1 s, in oil		3000	_	VIIIS
		DC, 1 min, in oil	—	3000	_	Vdc

Switching Characteristics (Ta = 25°C)

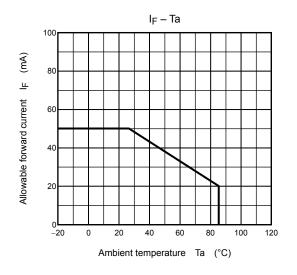
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t _{ON}	$R_{L} = 200 \; \Omega \tag{Note}$	2) —	0.3	1	ms
Turn-off time	tOFF	$V_{DD} = 20 \text{ V}, \text{ I}_F = 5 \text{ mA}$	—	0.1	1	ms

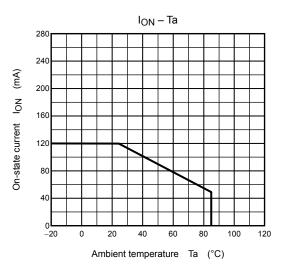
Note 2: Switching time test circuit

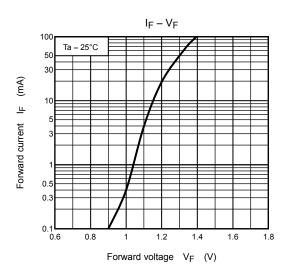


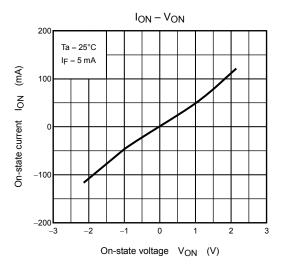


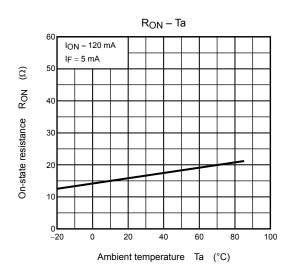
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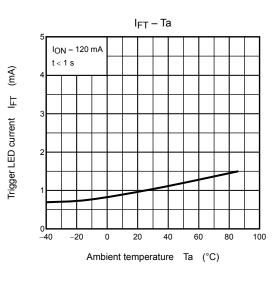




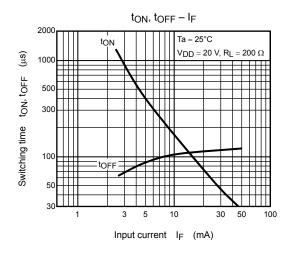


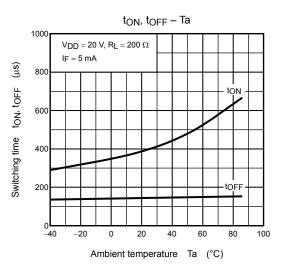


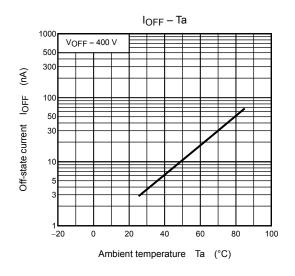




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